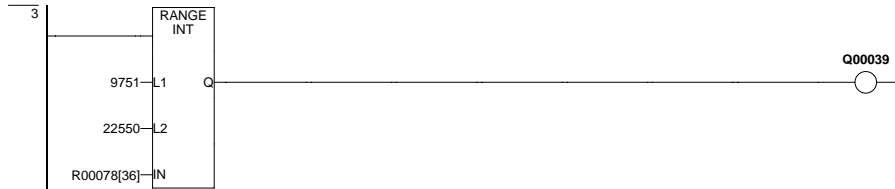


Q00038 %Q00038
 LD Block,'Tan_com': COIL 00002;
R00078[36] %R00114
 LD Block,'Tan_com': RANGE_INT 00002, 00003, 00004; MOVE_INT 00013;



Q00039 %Q00039
 LD Block,'Tan_com': COIL 00003;
R00078[36] %R00114
 LD Block,'Tan_com': RANGE_INT 00002, 00003, 00004; MOVE_INT 00013;



Q00040 %Q00040
 LD Block,'Tan_com': COIL 00004;
R00078[36] %R00114
 LD Block,'Tan_com': RANGE_INT 00002, 00003, 00004; MOVE_INT 00013;



AI0018 %AI0018
 LD Block,'Tan_com': INT_TO_REAL 00005; MOVE_INT 00013;
R01155 %R01155
 LD Block,'Tan_com': MUL_REAL 00006; INT_TO_REAL 00005;



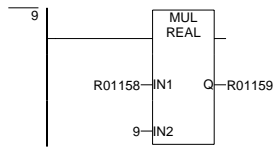
R01156 %R01156
 LD Block,'Tan_com': MUL_REAL 00006; SUB_REAL 00007;
R01155 %R01155
 LD Block,'Tan_com': MUL_REAL 00006; INT_TO_REAL 00005;



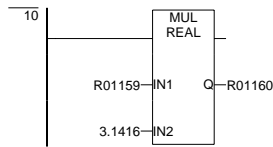
R01157 %R01157
 LD Block,'Tan_com': SUB_REAL 00007, 00008;
R01156 %R01156
 LD Block,'Tan_com': MUL_REAL 00006; SUB_REAL 00007;



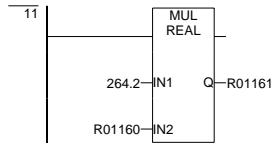
R01158 %R01158
 LD Block,'Tan_com': MUL_REAL 00009; SUB_REAL 00008;
R01157 %R01157
 LD Block,'Tan_com': SUB_REAL 00007, 00008;



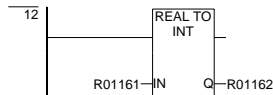
R01158 %R01158
LD Block,'Tan_com': MUL_REAL 00009; SUB_REAL 00008;
R01159 %R01159
LD Block,'Tan_com': MUL_REAL 00009, 00010;



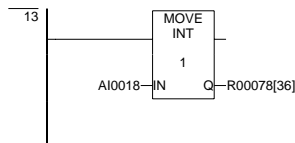
R01159 %R01159
LD Block,'Tan_com': MUL_REAL 00009, 00010;
R01160 %R01160
LD Block,'Tan_com': MUL_REAL 00010, 00011;



R01161 %R01161
LD Block,'Tan_com': MUL_REAL 00011; REAL_TO_INT 00012;
R01160 %R01160
LD Block,'Tan_com': MUL_REAL 00010, 00011;



R01161 %R01161
LD Block,'Tan_com': MUL_REAL 00011; REAL_TO_INT 00012;
R01162 %R01162
LD Block,'Tan_com': REAL_TO_INT 00012;



AI0018 %AI0018
LD Block,'Tan_com': INT_TO_REAL 00005; MOVE_INT 00013;
R00078[36] %R00114
LD Block,'Tan_com': RANGE_INT 00002, 00003, 00004; MOVE_INT 00013;



Q00041 %Q00041
LD Block,'Tan_com': COIL 00015;
R00078[39] %R00117
LD Block,'Tan_com': RANGE_INT 00015, 00016, 00017; MOVE_INT 00026;



Q00042 %Q00042
LD Block,'Tan_com': COIL 00016;
R00078[39] %R00117
LD Block,'Tan_com': RANGE_INT 00015, 00016, 00017; MOVE_INT 00026;



Q00043 %Q00043
 LD Block,'Tan_com': COIL 00017;
R00078[39] %R00117
 LD Block,'Tan_com': RANGE_INT 00015, 00016, 00017; MOVE_INT 00026;



AI0019 %AI0019
 LD Block,'Tan_com': INT_TO_REAL 00018; MOVE_INT 00026;
R01163 %R01163
 LD Block,'Tan_com': MUL_REAL 00019; INT_TO_REAL 00018;



R01164 %R01164
 LD Block,'Tan_com': MUL_REAL 00019; SUB_REAL 00020;
R01163 %R01163
 LD Block,'Tan_com': MUL_REAL 00019; INT_TO_REAL 00018;



R01165 %R01165
 LD Block,'Tan_com': SUB_REAL 00020, 00021;
R01164 %R01164
 LD Block,'Tan_com': MUL_REAL 00019; SUB_REAL 00020;



R01166 %R01166
 LD Block,'Tan_com': MUL_REAL 00022; SUB_REAL 00021;
R01165 %R01165
 LD Block,'Tan_com': SUB_REAL 00020, 00021;



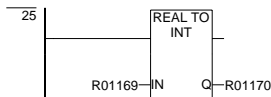
R01166 %R01166
 LD Block,'Tan_com': MUL_REAL 00022; SUB_REAL 00021;
R01167 %R01167
 LD Block,'Tan_com': MUL_REAL 00022, 00023;



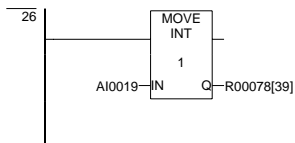
R01167 %R01167
 LD Block,'Tan_com': MUL_REAL 00022, 00023;
R01168 %R01168
 LD Block,'Tan_com': MUL_REAL 00023, 00024;



R01169 %R01169
 LD Block,'Tan_com': MUL_REAL 00024; REAL_TO_INT 00025;
R01168 %R01168
 LD Block,'Tan_com': MUL_REAL 00023, 00024;



R01169 %R01169
LD Block,'Tan_com': MUL_REAL 00024; REAL_TO_INT 00025;
R01170 %R01170
LD Block,'Tan_com': REAL_TO_INT 00025;



AI0019 %AI0019
LD Block,'Tan_com': INT_TO_REAL 00018; MOVE_INT 00026;
R00078[39] %R00117
LD Block,'Tan_com': RANGE_INT 00015, 00016, 00017; MOVE_INT 00026;



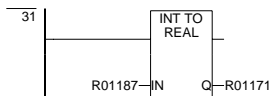
Q00044 %Q00044
LD Block,'Tan_com': COIL 00028;
R01187 %R01187
LD Block,'Tan_com': INT_TO_REAL 00031; RANGE_INT 00028, 00029, 00030;



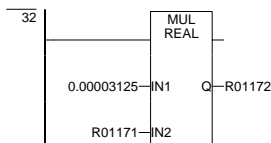
Q00045 %Q00045
LD Block,'Tan_com': COIL 00029;
R01187 %R01187
LD Block,'Tan_com': INT_TO_REAL 00031; RANGE_INT 00028, 00029, 00030;



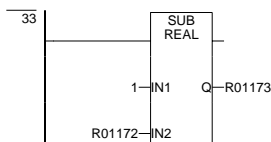
Q00046 %Q00046
LD Block,'Tan_com': COIL 00030;
R01187 %R01187
LD Block,'Tan_com': INT_TO_REAL 00031; RANGE_INT 00028, 00029, 00030;



R01187 %R01187
LD Block,'Tan_com': INT_TO_REAL 00031; RANGE_INT 00028, 00029, 00030;
R01171 %R01171
LD Block,'Tan_com': MUL_REAL 00032; INT_TO_REAL 00031;



R01172 %R01172
LD Block,'Tan_com': MUL_REAL 00032; SUB_REAL 00033;
R01171 %R01171
LD Block,'Tan_com': MUL_REAL 00032; INT_TO_REAL 00031;

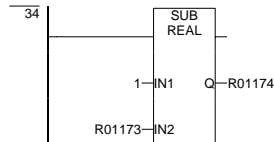


R01173 %R01173

LD Block,'Tan_com': SUB_REAL 00033, 00034;

R01172 %R01172

LD Block,'Tan_com': MUL_REAL 00032; SUB_REAL 00033;

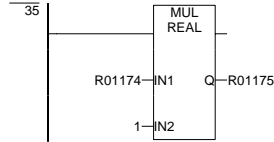


R01174 %R01174

LD Block,'Tan_com': MUL_REAL 00035; SUB_REAL 00034;

R01173 %R01173

LD Block,'Tan_com': SUB_REAL 00033, 00034;

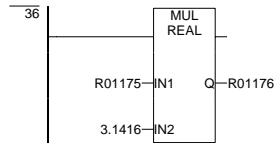


R01174 %R01174

LD Block,'Tan_com': MUL_REAL 00035; SUB_REAL 00034;

R01175 %R01175

LD Block,'Tan_com': MUL_REAL 00035, 00036;

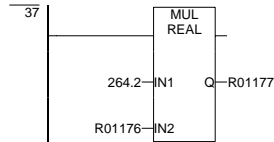


R01175 %R01175

LD Block,'Tan_com': MUL_REAL 00035, 00036;

R01176 %R01176

LD Block,'Tan_com': MUL_REAL 00036, 00037;

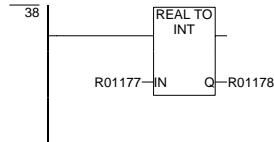


R01177 %R01177

LD Block,'Tan_com': MUL_REAL 00037; REAL_TO_INT 00038;

R01176 %R01176

LD Block,'Tan_com': MUL_REAL 00036, 00037;



R01177 %R01177

LD Block,'Tan_com': MUL_REAL 00037; REAL_TO_INT 00038;

R01178 %R01178

LD Block,'Tan_com': REAL_TO_INT 00038;

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Q00047 %Q00047

LD Block,'Tan_com': COIL 00040;

R00123 %R00123

LD Block,'Tan_com': INT_TO_REAL 00043; RANGE_INT 00040, 00041, 00042;



Q00048 %Q00048

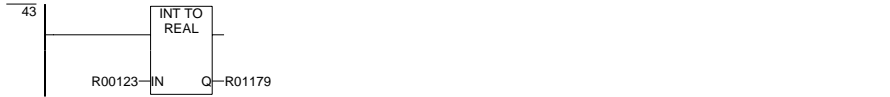
LD Block,'Tan_com': COIL 00041;

R00123 %R00123

LD Block,'Tan_com': INT_TO_REAL 00043; RANGE_INT 00040, 00041, 00042;



Q00049 %Q00049
 LD Block,'Tan_com': COIL 00042;
R00123 %R00123
 LD Block,'Tan_com': INT_TO_REAL 00043; RANGE_INT 00040, 00041, 00042;



R00123 %R00123
 LD Block,'Tan_com': INT_TO_REAL 00043; RANGE_INT 00040, 00041, 00042;
R01179 %R01179
 LD Block,'Tan_com': MUL_REAL 00044; INT_TO_REAL 00043;



R01180 %R01180
 LD Block,'Tan_com': MUL_REAL 00044; SUB_REAL 00045;
R01179 %R01179
 LD Block,'Tan_com': MUL_REAL 00044; INT_TO_REAL 00043;



R01181 %R01181
 LD Block,'Tan_com': SUB_REAL 00045, 00046;
R01180 %R01180
 LD Block,'Tan_com': MUL_REAL 00044; SUB_REAL 00045;



R01182 %R01182
 LD Block,'Tan_com': MUL_REAL 00047; SUB_REAL 00046;
R01181 %R01181
 LD Block,'Tan_com': SUB_REAL 00045, 00046;



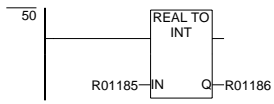
R01182 %R01182
 LD Block,'Tan_com': MUL_REAL 00047; SUB_REAL 00046;
R01183 %R01183
 LD Block,'Tan_com': MUL_REAL 00047, 00048;



R01183 %R01183
 LD Block,'Tan_com': MUL_REAL 00047, 00048;
R01184 %R01184
 LD Block,'Tan_com': MUL_REAL 00048, 00049;



R01185 %R01185
 LD Block,'Tan_com': MUL_REAL 00049; REAL_TO_INT 00050;
R01184 %R01184
 LD Block,'Tan_com': MUL_REAL 00048, 00049;



R01185 %R01185

LD Block,'Tan_com': MUL_REAL 00049; REAL_TO_INT 00050;

R01186 %R01186

LD Block,'Tan_com': REAL_TO_INT 00050;