



M00080 %M00080

LD Block,'UMA_N': NOCON 00002, 00039, 00040, 00043, 00044; COIL 00037;

M00068 %M00068

LD Block,'_MAIN': NOCON 00012, 00051, 00060, 00069;

LD Block,'UMA_N': NOCON 00020, 00021, 00022, 00025, 00031, 00032, 00033, 00034; COIL 00002;

M00081 %M00081

LD Block,'UMA_N': NOCON 00002, 00050, 00052, 00053; COIL 00048;

M00082 %M00082

LD Block,'UMA_N': NOCON 00002, 00059, 00061, 00062; COIL 00057;

M00083 %M00083

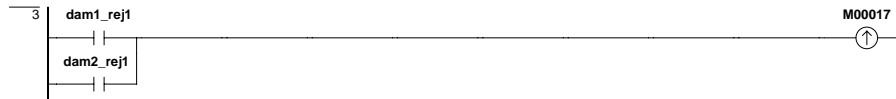
LD Block,'UMA_N': NOCON 00002, 00068, 00070, 00071; COIL 00066;

M00084 %M00084

LD Block,'UMA_N': NOCON 00002, 00077, 00079, 00080; COIL 00075;

M00085 %M00085

LD Block,'UMA_N': NOCON 00002, 00086, 00088, 00089; COIL 00084;



dam1_rej1 %Q00070

LD Block,'UMA_N': NOCON 00003, 00012; COIL 00039;

M00017 %M00017

LD Block,'UMA_N': POSCOIL 00003; NOCON 00009;

dam2_rej1 %Q00071

LD Block,'UMA_N': NOCON 00003, 00012; COIL 00040;



dam1_rej2 %Q00075

LD Block,'UMA_N': NOCON 00004, 00013; COIL 00050;

M00018 %M00018

LD Block,'UMA_N': POSCOIL 00004; NOCON 00009;



dam1_rej3 %Q00077

LD Block,'UMA_N': NOCON 00005, 00014; COIL 00059;

M00019 %M00019

LD Block,'UMA_N': POSCOIL 00005; NOCON 00009;



Dam1_rej4 %Q00079

LD Block,'UMA_N': NOCON 00006, 00015; COIL 00068;

M00020 %M00020

LD Block,'UMA_N': POSCOIL 00006; NOCON 00009;



Dam1_rej5 %Q00081

LD Block,'UMA_N': NOCON 00007, 00016; COIL 00077;

M00025 %M00025

LD Block,'UMA_N': POSCOIL 00007; NOCON 00009;



dam1_rej6 %Q00083

LD Block,'UMA_N': NOCON 00008, 00017; COIL 00086;

M00026 %M00026

LD Block,'UMA_N': POSCOIL 00008; NOCON 00009;



M00017 %M00017
LD Block,'UMA_N': POSCOIL 00003; NOCON 00009;

M01010 %M01010
LD Block,'UMA_N': NOCON 00010; COIL 00009;

M00018 %M00018
LD Block,'UMA_N': POSCOIL 00004; NOCON 00009;

M00019 %M00019
LD Block,'UMA_N': POSCOIL 00005; NOCON 00009;

M00020 %M00020
LD Block,'UMA_N': POSCOIL 00006; NOCON 00009;

M00025 %M00025
LD Block,'UMA_N': POSCOIL 00007; NOCON 00009;

M00026 %M00026
LD Block,'UMA_N': POSCOIL 00008; NOCON 00009;



M01010 %M01010
LD Block,'UMA_N': NOCON 00010; COIL 00009;

R00025[0] %R00025
LD Block,'UMA_N': SUB_INT 00019, 00019; ADD_INT 00010, 00010; MOVE_INT 00011;



#ALW_ON %S00007
LD Block,'Reloj': NOCON 00001, 00002, 00003, 00004, 00005, 00006, 00007, 00008;
LD Block,'flujo_1': NOCON 00003, 00006;
LD Block,'UMA_N': NOCON 00011, 00102, 00198, 00273, 00395, 00510, 00616;
LD Block,'N_T_ch': NOCON 00001, 00002, 00003, 00004, 00005, 00006, 00007;
LD Block,'N_cist': NOCON 00002, 00003, 00004, 00005, 00006, 00007, 00008, 00010, 00011, 00012, 00013, 00014, 00015, 00016;
LD Block,'cister': NOCON 00020;
LD Block,'genee': NOCON 00011;
LD Block,'incend': NOCON 00026, 00032, 00033, 00034, 00036, 00039, 00040, 00041, 00042, 00043, 00044, 00045;
LD Block,'agua': NOCON 00001, 00002, 00003, 00006, 00007, 00008, 00009, 00010, 00011, 00012, 00020, 00021, 00022, 00031, 00032, 00033;
LD Block,'_MAIN': NOCON 00001, 00002, 00003, 00004, 00005, 00006, 00007, 00008, 00009, 00010, 00011, 00026, 00030, 00033, 00034, 00035, 00049, 00067, 00068, 00078, 00095, 00096, 00121, 00126, 00128, 00129;

R00025[0] %R00025
LD Block,'UMA_N': SUB_INT 00019, 00019; ADD_INT 00010, 00010; MOVE_INT 00011;

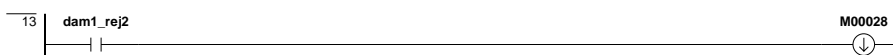
Var_freq %AQ0005
LD Block,'UMA_N': MOVE_INT 00011;



dam1_rej1 %Q00070
LD Block,'UMA_N': NOCON 00003, 00012; COIL 00039;

M00027 %M00027
LD Block,'UMA_N': NEGCOIL 00012; NOCON 00018;

dam2_rej1 %Q00071
LD Block,'UMA_N': NOCON 00003, 00012; COIL 00040;



dam1_rej2 %Q00075
LD Block,'UMA_N': NOCON 00004, 00013; COIL 00050;

M00028 %M00028
LD Block,'UMA_N': NEGCOIL 00013; NOCON 00018;



dam1_rej3 %Q00077
LD Block,'UMA_N': NOCON 00005, 00014; COIL 00059;

M00029 %M00029

LD Block,'UMA_N': NEGCOIL 00014; NOCON 00018;



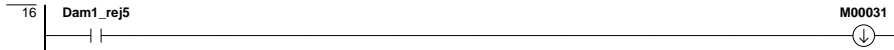
Dam1_rej4 %Q00079

LD Block,'UMA_N': NOCON 00006, 00015; COIL 00068;

M00030 %M00030

LD Block,'UMA_N': NEGCOIL 00015; NOCON 00018;

LD Block,'genee': NOCON 00003;



Dam1_rej5 %Q00081

LD Block,'UMA_N': NOCON 00007, 00016; COIL 00077;

M00031 %M00031

LD Block,'UMA_N': NEGCOIL 00016; NOCON 00018;



dam1_rej6 %Q00083

LD Block,'UMA_N': NOCON 00008, 00017; COIL 00086;

M00032 %M00032

LD Block,'UMA_N': NEGCOIL 00017; NOCON 00018;



M00027 %M00027

LD Block,'UMA_N': NEGCOIL 00012; NOCON 00018;

M01011 %M01011

LD Block,'UMA_N': NOCON 00019; COIL 00018;

M00028 %M00028

LD Block,'UMA_N': NEGCOIL 00013; NOCON 00018;

M00029 %M00029

LD Block,'UMA_N': NEGCOIL 00014; NOCON 00018;

M00030 %M00030

LD Block,'UMA_N': NEGCOIL 00015; NOCON 00018;

LD Block,'genee': NOCON 00003;

M00031 %M00031

LD Block,'UMA_N': NEGCOIL 00016; NOCON 00018;

M00032 %M00032

LD Block,'UMA_N': NEGCOIL 00017; NOCON 00018;



M01011 %M01011

LD Block,'UMA_N': NOCON 00019; COIL 00018;

R00025[0] %R00025

LD Block,'UMA_N': SUB_INT 00019, 00019; ADD_INT 00010, 00010; MOVE_INT 00011;



M00068 %M00068

LD Block,'_MAIN': NOCON 00012, 00051, 00060, 00069;

LD Block,'UMA_N': NOCON 00020, 00021, 00022, 00025, 00031, 00032, 00033, 00034; COIL 00002;

Paro_emer %M00075

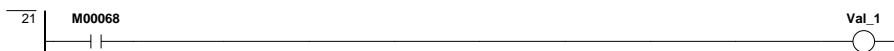
LD Block,'UMA_N': NCCON 00020;

OL_UMA1 %M00073

LD Block,'UMA_N': NCCON 00020;

UMA_1 %Q00060

LD Block,'UMA_N': COIL 00020;



M00068 %M00068

LD Block,'_MAIN': NOCON 00012, 00051, 00060, 00069;

LD Block,'UMA_N': NOCON 00020, 00021, 00022, 00025, 00031, 00032, 00033, 00034; COIL 00002;

Val_1 %Q00061

LD Block,'UMA_N': COIL 00021;



M00068 %M00068

LD Block,'_MAIN': NOCON 00012, 00051, 00060, 00069;
 LD Block,'UMA_N': NOCON 00020, 00021, 00022, 00025, 00031, 00032, 00033, 00034; COIL 00002;
M00070 %M00070
 LD Block,'UMA_N': NCCON 00022; NOCON 00025, 00144; COIL 00024;
F_dam1 %M00071
 LD Block,'UMA_N': NCCON 00022, 00023; NOCON 00027;
M00509 %M00509
 LD Block,'UMA_N': NOCON 00029; COIL 00022;



Dam1 %Q00062
 LD Block,'UMA_N': NOCON 00023; COIL 00029;
M00074 %M00074
 LD Block,'UMA_N': NCCON 00023; NOCON 00024, 00026; COIL 00026;
F_dam1 %M00071
 LD Block,'UMA_N': NCCON 00022, 00023; NOCON 00027;
M00069 %M00069
 LD Block,'UMA_N': NOCON 00023, 00024; COIL 00023;



M00069 %M00069
 LD Block,'UMA_N': NOCON 00023, 00024; COIL 00023;
R00142 %R00142
 LD Block,'UMA_N': ONDTR_TENTHS 00024;
M00070 %M00070
 LD Block,'UMA_N': NCCON 00022; NOCON 00025, 00144; COIL 00024;
M00074 %M00074
 LD Block,'UMA_N': NCCON 00023; NOCON 00024, 00026; COIL 00026;



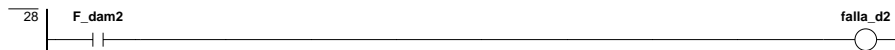
M00070 %M00070
 LD Block,'UMA_N': NCCON 00022; NOCON 00025, 00144; COIL 00024;
M00068 %M00068
 LD Block,'_MAIN': NOCON 00012, 00051, 00060, 00069;
 LD Block,'UMA_N': NOCON 00020, 00021, 00022, 00025, 00031, 00032, 00033, 00034; COIL 00002;
F_dam2 %M00072
 LD Block,'UMA_N': NCCON 00025; NOCON 00028;
M00510 %M00510
 LD Block,'UMA_N': NOCON 00030; COIL 00025;



Dam2 %Q00063
 LD Block,'UMA_N': NOCON 00026; COIL 00030;
R00145 %R00145
 LD Block,'UMA_N': ONDTR_TENTHS 00026;
M00074 %M00074
 LD Block,'UMA_N': NCCON 00023; NOCON 00024, 00026; COIL 00026;



F_dam1 %M00071
 LD Block,'UMA_N': NCCON 00022, 00023; NOCON 00027;
falla_d1 %Q00064
 LD Block,'UMA_N': NCCON 00029; NOCON 00030; COIL 00027;



F_dam2 %M00072
 LD Block,'UMA_N': NCCON 00025; NOCON 00028;
falla_d2 %Q00065
 LD Block,'UMA_N': NCCON 00030; NOCON 00029; COIL 00028;



M00509 %M00509
 LD Block,'UMA_N': NOCON 00029; COIL 00022;
falla_d1 %Q00064
 LD Block,'UMA_N': NCCON 00029; NOCON 00030; COIL 00027;
Dam1 %Q00062
 LD Block,'UMA_N': NOCON 00023; COIL 00029;

falla_d2 %Q00065

LD Block,'UMA_N': NCCON 00030; NOCON 00029; COIL 00028;



M00510 %M00510

LD Block,'UMA_N': NOCON 00030; COIL 00025;

falla_d2 %Q00065

LD Block,'UMA_N': NCCON 00030; NOCON 00029; COIL 00028;

Dam2 %Q00063

LD Block,'UMA_N': NOCON 00026; COIL 00030;

falla_d1 %Q00064

LD Block,'UMA_N': NCCON 00029; NOCON 00030; COIL 00027;



M00068 %M00068

LD Block,'_MAIN': NOCON 00012, 00051, 00060, 00069;

LD Block,'UMA_N': NOCON 00020, 00021, 00022, 00025, 00031, 00032, 00033, 00034; COIL 00002;

Sen_T1 %R00154

LD Block,'UMA_N': GT_INT 00031;

Falla_T1 %Q00066

LD Block,'UMA_N': COIL 00031;



M00068 %M00068

LD Block,'_MAIN': NOCON 00012, 00051, 00060, 00069;

LD Block,'UMA_N': NOCON 00020, 00021, 00022, 00025, 00031, 00032, 00033, 00034; COIL 00002;

Sen_T2 %R00155

LD Block,'UMA_N': GT_INT 00032;

Falla_T2 %Q00067

LD Block,'UMA_N': COIL 00032;



M00068 %M00068

LD Block,'_MAIN': NOCON 00012, 00051, 00060, 00069;

LD Block,'UMA_N': NOCON 00020, 00021, 00022, 00025, 00031, 00032, 00033, 00034; COIL 00002;

Falla_P %Q00069

LD Block,'UMA_N': COIL 00033;

Sen_pres %R00156

LD Block,'UMA_N': GE_INT 00033;



M00068 %M00068

LD Block,'_MAIN': NOCON 00012, 00051, 00060, 00069;

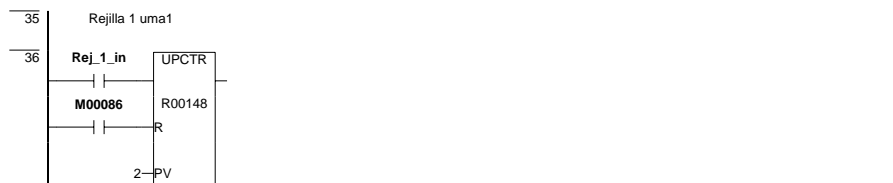
LD Block,'UMA_N': NOCON 00020, 00021, 00022, 00025, 00031, 00032, 00033, 00034; COIL 00002;

Sen_dif %R00157

LD Block,'UMA_N': GT_INT 00034;

Filtro %Q00068

LD Block,'UMA_N': COIL 00034;



Rej_1_in %M00061

LD Block,'UMA_N': NOCON 00036;

R00148 %R00148

LD Block,'UMA_N': UPCTR 00036; EQ_INT 00037, 00038;

M00086 %M00086

LD Block,'UMA_N': NOCON 00036; COIL 00038;



R00148[0] %R00148

LD Block,'UMA_N': EQ_INT 00037, 00038;

M00080 %M00080

LD Block,'UMA_N': NOCON 00002, 00039, 00040, 00043, 00044; COIL 00037;



R00148[0] %R00148

LD Block,'UMA_N': EQ_INT 00037, 00038;

M00086 %M00086

LD Block,'UMA_N': NOCON 00036; COIL 00038;



M00080 %M00080

LD Block,'UMA_N': NOCON 00002, 00039, 00040, 00043, 00044; COIL 00037;

M00094 %M00094

LD Block,'UMA_N': NCCON 00039, 00040; NOCON 00045; COIL 00045;

Q00073 %Q00073

LD Block,'UMA_N': NCCON 00039; COIL 00041;

dam1_rej1 %Q00070

LD Block,'UMA_N': NOCON 00003, 00012; COIL 00039;



M00080 %M00080

LD Block,'UMA_N': NOCON 00002, 00039, 00040, 00043, 00044; COIL 00037;

M00094 %M00094

LD Block,'UMA_N': NCCON 00039, 00040; NOCON 00045; COIL 00045;

Q00074 %Q00074

LD Block,'UMA_N': NCCON 00040; COIL 00042;

dam2_rej1 %Q00071

LD Block,'UMA_N': NOCON 00003, 00012; COIL 00040;

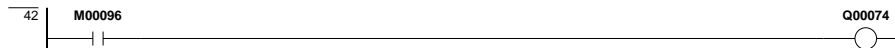


M00095 %M00095

LD Block,'UMA_N': NOCON 00041;

Q00073 %Q00073

LD Block,'UMA_N': NCCON 00039; COIL 00041;



M00096 %M00096

LD Block,'UMA_N': NOCON 00042;

Q00074 %Q00074

LD Block,'UMA_N': NCCON 00040; COIL 00042;



M00080 %M00080

LD Block,'UMA_N': NOCON 00002, 00039, 00040, 00043, 00044; COIL 00037;

Sen_rej1 %R00172

LD Block,'UMA_N': LT_INT 00044; GT_INT 00043;

M00092 %M00092

LD Block,'UMA_N': NCCON 00045; COIL 00043;

R00173 %R00173

LD Block,'UMA_N': GT_INT 00043;



M00080 %M00080

LD Block,'UMA_N': NOCON 00002, 00039, 00040, 00043, 00044; COIL 00037;

Sen_rej1 %R00172

LD Block,'UMA_N': LT_INT 00044; GT_INT 00043;

M00093 %M00093

LD Block,'UMA_N': NOCON 00045; COIL 00044;
R00174 %R00174
 LD Block,'UMA_N': LT_INT 00044;



M00093 %M00093
 LD Block,'UMA_N': NOCON 00045; COIL 00044;
M00092 %M00092
 LD Block,'UMA_N': NCCON 00045; COIL 00043;
M00094 %M00094
 LD Block,'UMA_N': NCCON 00039, 00040; NOCON 00045; COIL 00045;



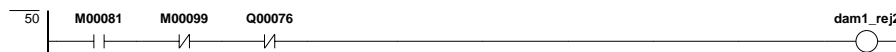
Rej_2_in %M00062
 LD Block,'UMA_N': NOCON 00047;
R00151 %R00151
 LD Block,'UMA_N': UPCTR 00047; EQ_INT 00048, 00049;
M00087 %M00087
 LD Block,'UMA_N': NOCON 00047; COIL 00049;



R00151[0] %R00151
 LD Block,'UMA_N': EQ_INT 00048, 00049;
M00081 %M00081
 LD Block,'UMA_N': NOCON 00002, 00050, 00052, 00053; COIL 00048;



R00151[0] %R00151
 LD Block,'UMA_N': EQ_INT 00048, 00049;
M00087 %M00087
 LD Block,'UMA_N': NOCON 00047; COIL 00049;



M00081 %M00081
 LD Block,'UMA_N': NOCON 00002, 00050, 00052, 00053; COIL 00048;
M00099 %M00099
 LD Block,'UMA_N': NCCON 00050; NOCON 00054; COIL 00054;
Q00076 %Q00076
 LD Block,'UMA_N': NCCON 00050; COIL 00051;
dam1_rej2 %Q00075
 LD Block,'UMA_N': NOCON 00004, 00013; COIL 00050;



M00100 %M00100
 LD Block,'UMA_N': NOCON 00051;
Q00076 %Q00076
 LD Block,'UMA_N': NCCON 00050; COIL 00051;



M00081 %M00081
 LD Block,'UMA_N': NOCON 00002, 00050, 00052, 00053; COIL 00048;
Sen_rej2 %R00175
 LD Block,'UMA_N': LT_INT 00053; GT_INT 00052;
M00097 %M00097
 LD Block,'UMA_N': NCCON 00054; COIL 00052;
R00176 %R00176
 LD Block,'UMA_N': GT_INT 00052;



M00081 %M00081
 LD Block,'UMA_N': NOCON 00002, 00050, 00052, 00053; COIL 00048;

Sen_rej2 %R00175
 LD Block,'UMA_N': LT_INT 00053; GT_INT 00052;

M00098 %M00098
 LD Block,'UMA_N': NOCON 00054; COIL 00053;

R00177 %R00177
 LD Block,'UMA_N': LT_INT 00053;



M00098 %M00098
 LD Block,'UMA_N': NOCON 00054; COIL 00053;

M00097 %M00097
 LD Block,'UMA_N': NCCON 00054; COIL 00052;

M00099 %M00099
 LD Block,'UMA_N': NCCON 00050; NOCON 00054; COIL 00054;



Rej_3 in %M00063
 LD Block,'UMA_N': NOCON 00056;

R00158 %R00158
 LD Block,'UMA_N': UPCTR 00056; EQ_INT 00057, 00058;

M00088 %M00088
 LD Block,'UMA_N': NOCON 00056; COIL 00058;



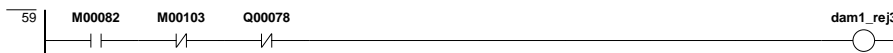
R00158[0] %R00158
 LD Block,'UMA_N': EQ_INT 00057, 00058;

M00082 %M00082
 LD Block,'UMA_N': NOCON 00002, 00059, 00061, 00062; COIL 00057;



R00158[0] %R00158
 LD Block,'UMA_N': EQ_INT 00057, 00058;

M00088 %M00088
 LD Block,'UMA_N': NOCON 00056; COIL 00058;

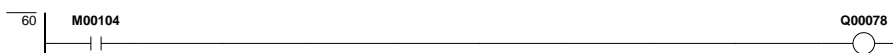


M00082 %M00082
 LD Block,'UMA_N': NOCON 00002, 00059, 00061, 00062; COIL 00057;

M00103 %M00103
 LD Block,'UMA_N': NCCON 00059; NOCON 00063; COIL 00063;

Q00078 %Q00078
 LD Block,'UMA_N': NCCON 00059; COIL 00060;

dam1_rej3 %Q00077
 LD Block,'UMA_N': NOCON 00005, 00014; COIL 00059;



M00104 %M00104
 LD Block,'UMA_N': NOCON 00060;

Q00078 %Q00078
 LD Block,'UMA_N': NCCON 00059; COIL 00060;



M00082 %M00082
LD Block,'UMA_N': NOCON 00002, 00059, 00061, 00062; COIL 00057;

Sen_rej3 %R00178
LD Block,'UMA_N': LT_INT 00062; GT_INT 00061;

M00101 %M00101
LD Block,'UMA_N': NCCON 00063; COIL 00061;

R00179 %R00179
LD Block,'UMA_N': GT_INT 00061;



M00082 %M00082
LD Block,'UMA_N': NOCON 00002, 00059, 00061, 00062; COIL 00057;

Sen_rej3 %R00178
LD Block,'UMA_N': LT_INT 00062; GT_INT 00061;

M00102 %M00102
LD Block,'UMA_N': NOCON 00063; COIL 00062;

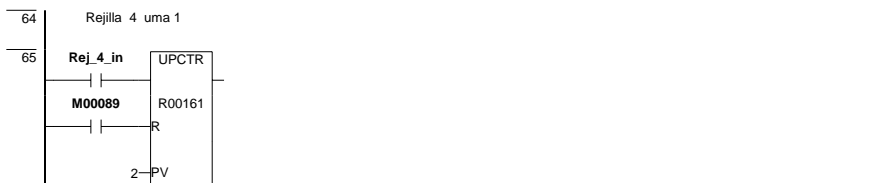
R00180 %R00180
LD Block,'UMA_N': LT_INT 00062;



M00102 %M00102
LD Block,'UMA_N': NOCON 00063; COIL 00062;

M00101 %M00101
LD Block,'UMA_N': NCCON 00063; COIL 00061;

M00103 %M00103
LD Block,'UMA_N': NCCON 00059; NOCON 00063; COIL 00063;



Rej_4_in %M00064
LD Block,'UMA_N': NOCON 00065;

R00161 %R00161
LD Block,'UMA_N': UPCTR 00065; EQ_INT 00066, 00067;

M00089 %M00089
LD Block,'UMA_N': NOCON 00065; COIL 00067;



R00161 %R00161
LD Block,'UMA_N': UPCTR 00065; EQ_INT 00066, 00067;

M00083 %M00083
LD Block,'UMA_N': NOCON 00002, 00068, 00070, 00071; COIL 00066;



R00161 %R00161
LD Block,'UMA_N': UPCTR 00065; EQ_INT 00066, 00067;

M00089 %M00089
LD Block,'UMA_N': NOCON 00065; COIL 00067;



M00083 %M00083
LD Block,'UMA_N': NOCON 00002, 00068, 00070, 00071; COIL 00066;

M00107 %M00107
LD Block,'UMA_N': NCCON 00068; NOCON 00072; COIL 00072;

Q00080 %Q00080

LD Block,'UMA_N': NCCON 00068; COIL 00069;
Dam1_rej4 %Q00079
 LD Block,'UMA_N': NOCON 00006, 00015; COIL 00068;



M00108 %M00108
 LD Block,'UMA_N': NOCON 00069;
Q00080 %Q00080
 LD Block,'UMA_N': NCCON 00068; COIL 00069;



M00083 %M00083
 LD Block,'UMA_N': NOCON 00002, 00068, 00070, 00071; COIL 00066;
Sen_rej4 %R00181
 LD Block,'UMA_N': LT_INT 00071; GT_INT 00070;
M00105 %M00105
 LD Block,'UMA_N': NCCON 00072; COIL 00070;
R00182 %R00182
 LD Block,'UMA_N': GT_INT 00070;



M00083 %M00083
 LD Block,'UMA_N': NOCON 00002, 00068, 00070, 00071; COIL 00066;
Sen_rej4 %R00181
 LD Block,'UMA_N': LT_INT 00071; GT_INT 00070;
M00106 %M00106
 LD Block,'UMA_N': NOCON 00072; COIL 00071;
R00183 %R00183
 LD Block,'UMA_N': LT_INT 00071;



M00106 %M00106
 LD Block,'UMA_N': NOCON 00072; COIL 00071;
M00105 %M00105
 LD Block,'UMA_N': NCCON 00072; COIL 00070;
M00107 %M00107
 LD Block,'UMA_N': NCCON 00068; NOCON 00072; COIL 00072;



Rej_5_in %M00065
 LD Block,'UMA_N': NOCON 00074;
R00164 %R00164
 LD Block,'UMA_N': UPCTR 00074; EQ_INT 00075, 00076;
M00090 %M00090
 LD Block,'UMA_N': NOCON 00074; COIL 00076;



R00164[0] %R00164
 LD Block,'UMA_N': EQ_INT 00075, 00076;
M00084 %M00084
 LD Block,'UMA_N': NOCON 00002, 00077, 00079, 00080; COIL 00075;



R00164[0] %R00164

LD Block,'UMA_N': EQ_INT 00075, 00076;
M00090 %M00090
 LD Block,'UMA_N': NOCON 00074; COIL 00076;



M00084 %M00084
 LD Block,'UMA_N': NOCON 00002, 00077, 00079, 00080; COIL 00075;
M00111 %M00111
 LD Block,'UMA_N': NCCON 00077; NOCON 00081; COIL 00081;
Q00082 %Q00082
 LD Block,'UMA_N': NCCON 00077; COIL 00078;
Dam1_rej5 %Q00081
 LD Block,'UMA_N': NOCON 00007, 00016; COIL 00077;



M00112 %M00112
 LD Block,'UMA_N': NOCON 00078;
Q00082 %Q00082
 LD Block,'UMA_N': NCCON 00077; COIL 00078;



M00084 %M00084
 LD Block,'UMA_N': NOCON 00002, 00077, 00079, 00080; COIL 00075;
Sen_rej5 %R00184
 LD Block,'UMA_N': LT_INT 00080; GT_INT 00079;
M00109 %M00109
 LD Block,'UMA_N': NCCON 00081; COIL 00079;
R00185 %R00185
 LD Block,'UMA_N': GT_INT 00079;



M00084 %M00084
 LD Block,'UMA_N': NOCON 00002, 00077, 00079, 00080; COIL 00075;
Sen_rej5 %R00184
 LD Block,'UMA_N': LT_INT 00080; GT_INT 00079;
M00110 %M00110
 LD Block,'UMA_N': NOCON 00081; COIL 00080;
R00186 %R00186
 LD Block,'UMA_N': LT_INT 00080;



M00110 %M00110
 LD Block,'UMA_N': NOCON 00081; COIL 00080;
M00109 %M00109
 LD Block,'UMA_N': NCCON 00081; COIL 00079;
M00111 %M00111
 LD Block,'UMA_N': NCCON 00077; NOCON 00081; COIL 00081;



Rej_6_in %M00066
 LD Block,'UMA_N': NOCON 00083;
R00167 %R00167
 LD Block,'UMA_N': UPCTR 00083; EQ_INT 00084, 00085;
M00091 %M00091
 LD Block,'UMA_N': NOCON 00083; COIL 00085;



R00167[0] %R00167

LD Block,'UMA_N': EQ_INT 00084, 00085;
M00085 %M00085
 LD Block,'UMA_N': NOCON 00002, 00086, 00088, 00089; COIL 00084;



R00167[0] %R00167
 LD Block,'UMA_N': EQ_INT 00084, 00085;
M00091 %M00091
 LD Block,'UMA_N': NOCON 00083; COIL 00085;



M00085 %M00085
 LD Block,'UMA_N': NOCON 00002, 00086, 00088, 00089; COIL 00084;
M00115 %M00115
 LD Block,'UMA_N': NCCON 00086; NOCON 00090; COIL 00090;
Q00084 %Q00084
 LD Block,'UMA_N': NCCON 00086; COIL 00087;
dam1_rej6 %Q00083
 LD Block,'UMA_N': NOCON 00008, 00017; COIL 00086;



M00116 %M00116
 LD Block,'UMA_N': NOCON 00087;
Q00084 %Q00084
 LD Block,'UMA_N': NCCON 00086; COIL 00087;



M00085 %M00085
 LD Block,'UMA_N': NOCON 00002, 00086, 00088, 00089; COIL 00084;
Sen_rej6 %R00187
 LD Block,'UMA_N': LT_INT 00089; GT_INT 00088;
M00113 %M00113
 LD Block,'UMA_N': NCCON 00090; COIL 00088;
R00188 %R00188
 LD Block,'UMA_N': GT_INT 00088;



M00085 %M00085
 LD Block,'UMA_N': NOCON 00002, 00086, 00088, 00089; COIL 00084;
Sen_rej6 %R00187
 LD Block,'UMA_N': LT_INT 00089; GT_INT 00088;
M00114 %M00114
 LD Block,'UMA_N': NOCON 00090; COIL 00089;
R00189 %R00189
 LD Block,'UMA_N': LT_INT 00089;



M00114 %M00114
 LD Block,'UMA_N': NOCON 00090; COIL 00089;
M00113 %M00113
 LD Block,'UMA_N': NCCON 00090; COIL 00088;
M00115 %M00115
 LD Block,'UMA_N': NCCON 00086; NOCON 00090; COIL 00090;





M00150 %M00150

LD Block,'UMA_N': NOCON 00092, 00131, 00133, 00134; COIL 00129;

M00142 %M00142

LD Block,'_MAIN': NOCON 00012, 00080, 00092, 00100;

LD Block,'UMA_N': NOCON 00112, 00113, 00114, 00117, 00123, 00124, 00125, 00126; COIL 00092;

M00151 %M00151

LD Block,'UMA_N': NOCON 00092, 00140, 00142, 00143; COIL 00138;

M00152 %M00152

LD Block,'UMA_N': NOCON 00092, 00149, 00151, 00152; COIL 00147;

M00153 %M00153

LD Block,'UMA_N': NOCON 00092, 00158, 00160, 00161; COIL 00156;

M00154 %M00154

LD Block,'UMA_N': NOCON 00092, 00167, 00169, 00170; COIL 00165;

M00155 %M00155

LD Block,'UMA_N': NOCON 00092, 00176, 00178, 00179; COIL 00174;

LD Block,'incend': NCCON 00029;

M00167 %M00167

LD Block,'UMA_N': NOCON 00092, 00185, 00187, 00188; COIL 00183;



Q00153 %Q00153

LD Block,'UMA_N': NOCON 00093, 00103; COIL 00131;

M00800 %M00800

LD Block,'UMA_N': POSCOIL 00093; NOCON 00100;

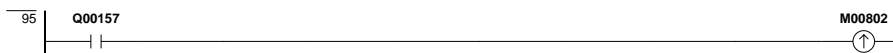


Q00155 %Q00155

LD Block,'UMA_N': NOCON 00094, 00104; COIL 00140;

M00801 %M00801

LD Block,'UMA_N': POSCOIL 00094; NOCON 00100;



Q00157 %Q00157

LD Block,'UMA_N': NOCON 00095, 00105; COIL 00149;

M00802 %M00802

LD Block,'UMA_N': POSCOIL 00095; NOCON 00100;



Q00159 %Q00159

LD Block,'UMA_N': NOCON 00096, 00106; COIL 00158;

M00803 %M00803

LD Block,'UMA_N': POSCOIL 00096; NOCON 00100;



Q00161 %Q00161

LD Block,'UMA_N': NOCON 00097, 00107; COIL 00167;

M00804 %M00804

LD Block,'UMA_N': POSCOIL 00097; NOCON 00100;



Q00163 %Q00163

LD Block,'UMA_N': NOCON 00098, 00108; COIL 00176;

M00805 %M00805

LD Block,'UMA_N': POSCOIL 00098; NOCON 00100;



Q00165 %Q00165

LD Block,'UMA_N': NOCON 00099, 00109; COIL 00185;

M00806 %M00806

LD Block,'UMA_N': POSCOIL 00099; NOCON 00100;



M00800 %M00800
 LD Block,'UMA_N': POSCOIL 00093; NOCON 00100;
M00807 %M00807
 LD Block,'UMA_N': NOCON 00101; COIL 00100;
M00801 %M00801
 LD Block,'UMA_N': POSCOIL 00094; NOCON 00100;
M00802 %M00802
 LD Block,'UMA_N': POSCOIL 00095; NOCON 00100;
M00803 %M00803
 LD Block,'UMA_N': POSCOIL 00096; NOCON 00100;
M00804 %M00804
 LD Block,'UMA_N': POSCOIL 00097; NOCON 00100;
M00805 %M00805
 LD Block,'UMA_N': POSCOIL 00098; NOCON 00100;
M00806 %M00806
 LD Block,'UMA_N': POSCOIL 00099; NOCON 00100;



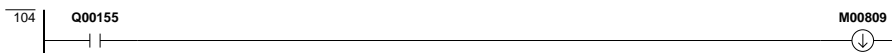
M00807 %M00807
 LD Block,'UMA_N': NOCON 00101; COIL 00100;
R00025[1] %R00026
 LD Block,'UMA_N': SUB_INT 00111, 00111; ADD_INT 00101, 00101; MOVE_INT 00102;



#ALW_ON %S00007
 LD Block,'Reloj': NOCON 00001, 00002, 00003, 00004, 00005, 00006, 00007, 00008;
 LD Block,'flujo_1': NOCON 00003, 00006;
 LD Block,'UMA_N': NOCON 00011, 00102, 00198, 00273, 00395, 00510, 00616;
 LD Block,'N_T_ch': NOCON 00001, 00002, 00003, 00004, 00005, 00006, 00007;
 LD Block,'N_cist': NOCON 00002, 00003, 00004, 00005, 00006, 00007, 00008, 00010, 00011, 00012, 00013, 00014, 00015, 00016;
 LD Block,'cister': NOCON 00020;
 LD Block,'genee': NOCON 00011;
 LD Block,'incend': NOCON 00026, 00032, 00033, 00034, 00036, 00039, 00040, 00041, 00042, 00043, 00044, 00045;
 LD Block,'agua': NOCON 00001, 00002, 00003, 00006, 00007, 00008, 00009, 00010, 00011, 00012, 00020, 00021, 00022, 00031, 00032, 00033;
 LD Block,'_MAIN': NOCON 00001, 00002, 00003, 00004, 00005, 00006, 00007, 00008, 00009, 00010, 00011, 00026, 00030, 00033, 00034, 00035, 00049, 00067, 00068, 00078, 00095, 00096, 00121, 00126, 00128, 00129;
R00025[1] %R00026
 LD Block,'UMA_N': SUB_INT 00111, 00111; ADD_INT 00101, 00101; MOVE_INT 00102;
AQ0006 %AQ0006
 LD Block,'UMA_N': MOVE_INT 00102;



Q00153 %Q00153
 LD Block,'UMA_N': NOCON 00093, 00103; COIL 00131;
M00808 %M00808
 LD Block,'UMA_N': NEGCOIL 00103; NOCON 00110;



Q00155 %Q00155
 LD Block,'UMA_N': NOCON 00094, 00104; COIL 00140;
M00809 %M00809
 LD Block,'UMA_N': NEGCOIL 00104; NOCON 00110;



Q00157 %Q00157
 LD Block,'UMA_N': NOCON 00095, 00105; COIL 00149;
M00810 %M00810

LD Block,'UMA_N': NEGCOIL 00105; NOCON 00110;



Q00159 %Q00159

LD Block,'UMA_N': NOCON 00096, 00106; COIL 00158;

M00811 %M00811

LD Block,'UMA_N': NEGCOIL 00106; NOCON 00110;



Q00161 %Q00161

LD Block,'UMA_N': NOCON 00097, 00107; COIL 00167;

M00812 %M00812

LD Block,'UMA_N': NEGCOIL 00107; NOCON 00110;



Q00163 %Q00163

LD Block,'UMA_N': NOCON 00098, 00108; COIL 00176;

M00813 %M00813

LD Block,'UMA_N': NEGCOIL 00108; NOCON 00110;



Q00165 %Q00165

LD Block,'UMA_N': NOCON 00099, 00109; COIL 00185;

M00814 %M00814

LD Block,'UMA_N': NEGCOIL 00109; NOCON 00110;



M00808 %M00808

LD Block,'UMA_N': NEGCOIL 00103; NOCON 00110;

M00815 %M00815

LD Block,'UMA_N': NOCON 00111; COIL 00110;

M00809 %M00809

LD Block,'UMA_N': NEGCOIL 00104; NOCON 00110;

M00810 %M00810

LD Block,'UMA_N': NEGCOIL 00105; NOCON 00110;

M00811 %M00811

LD Block,'UMA_N': NEGCOIL 00106; NOCON 00110;

M00812 %M00812

LD Block,'UMA_N': NEGCOIL 00107; NOCON 00110;

M00813 %M00813

LD Block,'UMA_N': NEGCOIL 00108; NOCON 00110;

M00814 %M00814

LD Block,'UMA_N': NEGCOIL 00109; NOCON 00110;



M00815 %M00815

LD Block,'UMA_N': NOCON 00111; COIL 00110;

R00025[1] %R00026

LD Block,'UMA_N': SUB_INT 00111, 00111; ADD_INT 00101, 00101; MOVE_INT 00102;



M00142 %M00142

LD Block,'_MAIN': NOCON 00012, 00080, 00092, 00100;

LD Block,'UMA_N': NOCON 00112, 00113, 00114, 00117, 00123, 00124, 00125, 00126; COIL 00092;

M00149 %M00149

LD Block,'UMA_N': NCCON 00112;

M00147 %M00147

LD Block,'UMA_N': NCCON 00112;

Q00143 %Q00143

LD Block,'UMA_N': COIL 00112;



M00142 %M00142

LD Block,'UMA_N': COIL 00112;

M00142 %M00142

LD Block,'_MAIN': NOCON 00012, 00080, 00092, 00100;
LD Block,'UMA_N': NOCON 00112, 00113, 00114, 00117, 00123, 00124, 00125, 00126; COIL 00092;

Q00144 %Q00144

LD Block,'UMA_N': COIL 00113;



M00142 %M00142

LD Block,'_MAIN': NOCON 00012, 00080, 00092, 00100;
LD Block,'UMA_N': NOCON 00112, 00113, 00114, 00117, 00123, 00124, 00125, 00126; COIL 00092;

M00144 %M00144

LD Block,'UMA_N': NCCON 00114; NOCON 00117; COIL 00116;

M00145 %M00145

LD Block,'UMA_N': NCCON 00114, 00115; NOCON 00119;

M00156 %M00156

LD Block,'UMA_N': NOCON 00121; COIL 00114;



Q00145 %Q00145

LD Block,'UMA_N': NOCON 00115; COIL 00121;

M00148 %M00148

LD Block,'UMA_N': NCCON 00115; NOCON 00116, 00118; COIL 00118;

M00145 %M00145

LD Block,'UMA_N': NCCON 00114, 00115; NOCON 00119;

M00143 %M00143

LD Block,'UMA_N': NOCON 00115, 00116; COIL 00115;



M00143 %M00143

LD Block,'UMA_N': NOCON 00115, 00116; COIL 00115;

R00510 %R00510

LD Block,'UMA_N': ONDTR_TENTHS 00116;

M00144 %M00144

LD Block,'UMA_N': NCCON 00114; NOCON 00117; COIL 00116;

M00148 %M00148

LD Block,'UMA_N': NCCON 00115; NOCON 00116, 00118; COIL 00118;



M00144 %M00144

LD Block,'UMA_N': NCCON 00114; NOCON 00117; COIL 00116;

M00142 %M00142

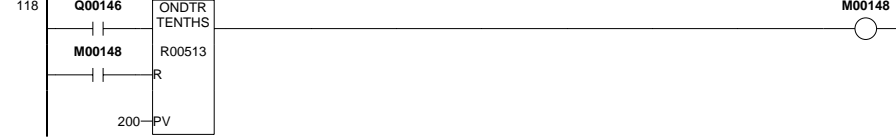
LD Block,'_MAIN': NOCON 00012, 00080, 00092, 00100;
LD Block,'UMA_N': NOCON 00112, 00113, 00114, 00117, 00123, 00124, 00125, 00126; COIL 00092;

M00146 %M00146

LD Block,'UMA_N': NCCON 00117; NOCON 00120;

M00157 %M00157

LD Block,'UMA_N': NOCON 00122; COIL 00117;



Q00146 %Q00146

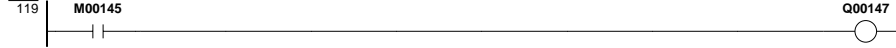
LD Block,'UMA_N': NOCON 00118; COIL 00122;

R00513 %R00513

LD Block,'UMA_N': ONDTR_TENTHS 00118;

M00148 %M00148

LD Block,'UMA_N': NCCON 00115; NOCON 00116, 00118; COIL 00118;



M00145 %M00145

LD Block,'UMA_N': NCCON 00114, 00115; NOCON 00119;

Q00147 %Q00147

LD Block,'UMA_N': NCCON 00121; NOCON 00122; COIL 00119;



M00146 %M00146

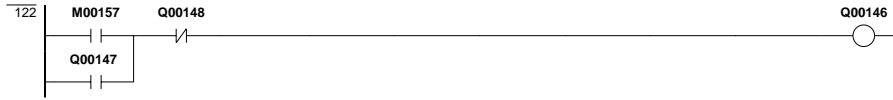
LD Block,'UMA_N': NCCON 00117; NOCON 00120;

Q00148 %Q00148

LD Block,'UMA_N': NCCON 00122; NOCON 00121; COIL 00120;



M00156 %M00156
 LD Block,'UMA_N': NOCON 00121; COIL 00114;
Q00147 %Q00147
 LD Block,'UMA_N': NCCON 00121; NOCON 00122; COIL 00119;
Q00145 %Q00145
 LD Block,'UMA_N': NOCON 00115; COIL 00121;
Q00148 %Q00148
 LD Block,'UMA_N': NCCON 00122; NOCON 00121; COIL 00120;



M00157 %M00157
 LD Block,'UMA_N': NOCON 00122; COIL 00117;
Q00148 %Q00148
 LD Block,'UMA_N': NCCON 00122; NOCON 00121; COIL 00120;
Q00146 %Q00146
 LD Block,'UMA_N': NOCON 00118; COIL 00122;
Q00147 %Q00147
 LD Block,'UMA_N': NCCON 00121; NOCON 00122; COIL 00119;



M00142 %M00142
 LD Block,'_MAIN': NOCON 00012, 00080, 00092, 00100;
 LD Block,'UMA_N': NOCON 00112, 00113, 00114, 00117, 00123, 00124, 00125, 00126; COIL 00092;
R01000 %R01000
 LD Block,'UMA_N': GT_INT 00123;
Q00149 %Q00149
 LD Block,'UMA_N': COIL 00123;



M00142 %M00142
 LD Block,'_MAIN': NOCON 00012, 00080, 00092, 00100;
 LD Block,'UMA_N': NOCON 00112, 00113, 00114, 00117, 00123, 00124, 00125, 00126; COIL 00092;
R01001 %R01001
 LD Block,'UMA_N': GT_INT 00124;
Q00150 %Q00150
 LD Block,'UMA_N': COIL 00124;



M00142 %M00142
 LD Block,'_MAIN': NOCON 00012, 00080, 00092, 00100;
 LD Block,'UMA_N': NOCON 00112, 00113, 00114, 00117, 00123, 00124, 00125, 00126; COIL 00092;
Q00152 %Q00152
 LD Block,'UMA_N': COIL 00125;
R01002 %R01002
 LD Block,'UMA_N': GE_INT 00125;



M00142 %M00142
 LD Block,'_MAIN': NOCON 00012, 00080, 00092, 00100;
 LD Block,'UMA_N': NOCON 00112, 00113, 00114, 00117, 00123, 00124, 00125, 00126; COIL 00092;
R01003 %R01003
 LD Block,'UMA_N': GT_INT 00126;
Q00151 %Q00151
 LD Block,'UMA_N': COIL 00126;

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M00158 %M00158
LD Block,'UMA_N': NOCON 00128;

R00517 %R00517
LD Block,'UMA_N': UPCTR 00128; EQ_INT 00129, 00130;

M00159 %M00159
LD Block,'UMA_N': NOCON 00128; COIL 00130;



R00517 %R00517
LD Block,'UMA_N': UPCTR 00128; EQ_INT 00129, 00130;

M00150 %M00150
LD Block,'UMA_N': NOCON 00092, 00131, 00133, 00134; COIL 00129;



R00517 %R00517
LD Block,'UMA_N': UPCTR 00128; EQ_INT 00129, 00130;

M00159 %M00159
LD Block,'UMA_N': NOCON 00128; COIL 00130;



M00150 %M00150
LD Block,'UMA_N': NOCON 00092, 00131, 00133, 00134; COIL 00129;

M00163 %M00163
LD Block,'UMA_N': NCCON 00131; NOCON 00135; COIL 00135;

Q00154 %Q00154
LD Block,'UMA_N': NCCON 00131; COIL 00132;

Q00153 %Q00153
LD Block,'UMA_N': NOCON 00093, 00103; COIL 00131;



M00164 %M00164
LD Block,'UMA_N': NOCON 00132;

Q00154 %Q00154
LD Block,'UMA_N': NCCON 00131; COIL 00132;



M00150 %M00150
LD Block,'UMA_N': NOCON 00092, 00131, 00133, 00134; COIL 00129;

R00520 %R00520
LD Block,'UMA_N': LT_INT 00134; GT_INT 00133;

M00161 %M00161
LD Block,'UMA_N': NCCON 00135; COIL 00133;

R00521 %R00521
LD Block,'UMA_N': GT_INT 00133;

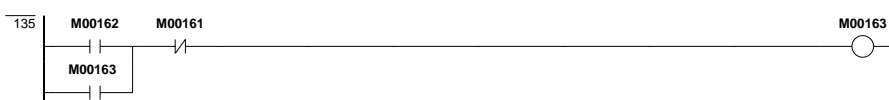


M00150 %M00150
LD Block,'UMA_N': NOCON 00092, 00131, 00133, 00134; COIL 00129;

R00520 %R00520
LD Block,'UMA_N': LT_INT 00134; GT_INT 00133;

M00162 %M00162
LD Block,'UMA_N': NOCON 00135; COIL 00134;

R00522 %R00522
LD Block,'UMA_N': LT_INT 00134;



M00162 %M00162

LD Block,'UMA_N': NOCON 00135; COIL 00134;

M00161 %M00161

LD Block,'UMA_N': NCCON 00135; COIL 00133;

M00163 %M00163

LD Block,'UMA_N': NCCON 00131; NOCON 00135; COIL 00135;



M00165 %M00165

LD Block,'UMA_N': NOCON 00137;

R00523 %R00523

LD Block,'UMA_N': UPCTR 00137; EQ_INT 00138, 00139;

M00166 %M00166

LD Block,'UMA_N': NOCON 00137; COIL 00139;



R00523[0] %R00523

LD Block,'UMA_N': EQ_INT 00138, 00139;

M00151 %M00151

LD Block,'UMA_N': NOCON 00092, 00140, 00142, 00143; COIL 00138;



R00523[0] %R00523

LD Block,'UMA_N': EQ_INT 00138, 00139;

M00166 %M00166

LD Block,'UMA_N': NOCON 00137; COIL 00139;



M00151 %M00151

LD Block,'UMA_N': NOCON 00092, 00140, 00142, 00143; COIL 00138;

M00170 %M00170

LD Block,'UMA_N': NCCON 00140; COIL 00144;

Q00156 %Q00156

LD Block,'UMA_N': NCCON 00140; COIL 00141;

Q00155 %Q00155

LD Block,'UMA_N': NOCON 00094, 00104; COIL 00140;



M00171 %M00171

LD Block,'UMA_N': NOCON 00141;

Q00156 %Q00156

LD Block,'UMA_N': NCCON 00140; COIL 00141;



M00151 %M00151

LD Block,'UMA_N': NOCON 00092, 00140, 00142, 00143; COIL 00138;

R00526 %R00526

LD Block,'UMA_N': LT_INT 00143; GT_INT 00142;

M00168 %M00168

LD Block,'UMA_N': NCCON 00144; COIL 00142;

R00527 %R00527

LD Block,'UMA_N': GT_INT 00142;



M00151 %M00151

LD Block,'UMA_N': NOCON 00092, 00140, 00142, 00143; COIL 00138;

R00526 %R00526

LD Block,'UMA_N': LT_INT 00143; GT_INT 00142;

M00169 %M00169

LD Block,'UMA_N': NOCON 00144; COIL 00143;

R00528 %R00528

LD Block,'UMA_N': LT_INT 00143;



M00169 %M00169

LD Block,'UMA_N': NOCON 00144; COIL 00143;

M00168 %M00168

LD Block,'UMA_N': NCCON 00144; COIL 00142;

M00170 %M00170

LD Block,'UMA_N': NCCON 00140; COIL 00144;

M00070 %M00070

LD Block,'UMA_N': NCCON 00022; NOCON 00025, 00144; COIL 00024;



M00172 %M00172

LD Block,'UMA_N': NOCON 00146;

R00529 %R00529

LD Block,'UMA_N': UPCTR 00146; EQ_INT 00147, 00148;

M00173 %M00173

LD Block,'UMA_N': NOCON 00146; COIL 00148;



R00529[0] %R00529

LD Block,'UMA_N': EQ_INT 00147, 00148;

M00152 %M00152

LD Block,'UMA_N': NOCON 00092, 00149, 00151, 00152; COIL 00147;



R00529[0] %R00529

LD Block,'UMA_N': EQ_INT 00147, 00148;

M00173 %M00173

LD Block,'UMA_N': NOCON 00146; COIL 00148;



M00152 %M00152

LD Block,'UMA_N': NOCON 00092, 00149, 00151, 00152; COIL 00147;

M00177 %M00177

LD Block,'UMA_N': NCCON 00149; NOCON 00153; COIL 00153;

Q00158 %Q00158

LD Block,'UMA_N': NCCON 00149; COIL 00150;

Q00157 %Q00157

LD Block,'UMA_N': NOCON 00095, 00105; COIL 00149;



M00178 %M00178

LD Block,'UMA_N': NOCON 00150;

Q00158 %Q00158

LD Block,'UMA_N': NCCON 00149; COIL 00150;



M00152 %M00152

LD Block,'UMA_N': NOCON 00092, 00149, 00151, 00152; COIL 00147;

R00532 %R00532

LD Block,'UMA_N': LT_INT 00152; GT_INT 00151;

M00175 %M00175

LD Block,'UMA_N': NCCON 00153; COIL 00151;

R00533 %R00533
 LD Block,'UMA_N': GT_INT 00151;



M00152 %M00152
 LD Block,'UMA_N': NOCON 00092, 00149, 00151, 00152; COIL 00147;

R00532 %R00532
 LD Block,'UMA_N': LT_INT 00152; GT_INT 00151;

M00176 %M00176
 LD Block,'UMA_N': NOCON 00153; COIL 00152;

R00534 %R00534
 LD Block,'UMA_N': LT_INT 00152;



M00176 %M00176
 LD Block,'UMA_N': NOCON 00153; COIL 00152;

M00175 %M00175
 LD Block,'UMA_N': NCCON 00153; COIL 00151;

M00177 %M00177
 LD Block,'UMA_N': NCCON 00149; NOCON 00153; COIL 00153;



M00179 %M00179
 LD Block,'UMA_N': NOCON 00155;

R00535 %R00535
 LD Block,'UMA_N': UPCTR 00155; EQ_INT 00156, 00157;

M00180 %M00180
 LD Block,'UMA_N': NOCON 00155; COIL 00157;



R00535[0] %R00535
 LD Block,'UMA_N': EQ_INT 00156, 00157;

M00153 %M00153
 LD Block,'UMA_N': NOCON 00092, 00158, 00160, 00161; COIL 00156;



R00535[0] %R00535
 LD Block,'UMA_N': EQ_INT 00156, 00157;

M00180 %M00180
 LD Block,'UMA_N': NOCON 00155; COIL 00157;



M00153 %M00153
 LD Block,'UMA_N': NOCON 00092, 00158, 00160, 00161; COIL 00156;

M00184 %M00184
 LD Block,'UMA_N': NCCON 00158; NOCON 00162; COIL 00162;

Q00160 %Q00160
 LD Block,'UMA_N': NCCON 00158; COIL 00159;

Q00159 %Q00159
 LD Block,'UMA_N': NOCON 00096, 00106; COIL 00158;



M00185 %M00185
 LD Block,'UMA_N': NOCON 00159;

Q00160 %Q00160
 LD Block,'UMA_N': NCCON 00158; COIL 00159;



M00153 %M00153
LD Block,'UMA_N': NOCON 00092, 00158, 00160, 00161; COIL 00156;

R00538 %R00538
LD Block,'UMA_N': LT_INT 00161; GT_INT 00160;

M00182 %M00182
LD Block,'UMA_N': NCCON 00162; COIL 00160;

R00539 %R00539
LD Block,'UMA_N': GT_INT 00160;



M00153 %M00153
LD Block,'UMA_N': NOCON 00092, 00158, 00160, 00161; COIL 00156;

R00538 %R00538
LD Block,'UMA_N': LT_INT 00161; GT_INT 00160;

M00183 %M00183
LD Block,'UMA_N': NOCON 00162; COIL 00161;

R00540 %R00540
LD Block,'UMA_N': LT_INT 00161;



M00183 %M00183
LD Block,'UMA_N': NOCON 00162; COIL 00161;

M00182 %M00182
LD Block,'UMA_N': NCCON 00162; COIL 00160;

M00184 %M00184
LD Block,'UMA_N': NCCON 00158; NOCON 00162; COIL 00162;



M00186 %M00186
LD Block,'UMA_N': NOCON 00164;

R00541 %R00541
LD Block,'UMA_N': UPCTR 00164; EQ_INT 00165, 00166;

M00187 %M00187
LD Block,'UMA_N': NOCON 00164; COIL 00166;



R00541[0] %R00541
LD Block,'UMA_N': EQ_INT 00165, 00166;

M00154 %M00154
LD Block,'UMA_N': NOCON 00092, 00167, 00169, 00170; COIL 00165;



R00541[0] %R00541
LD Block,'UMA_N': EQ_INT 00165, 00166;

M00187 %M00187
LD Block,'UMA_N': NOCON 00164; COIL 00166;



M00154 %M00154
LD Block,'UMA_N': NOCON 00092, 00167, 00169, 00170; COIL 00165;

M00191 %M00191
LD Block,'UMA_N': NCCON 00167; NOCON 00171; COIL 00171;

Q00162 %Q00162

LD Block,'UMA_N': NCCON 00167; COIL 00168;
Q00161 %Q00161
 LD Block,'UMA_N': NOCON 00097, 00107; COIL 00167;



M00192 %M00192
 LD Block,'UMA_N': NOCON 00168;
Q00162 %Q00162
 LD Block,'UMA_N': NCCON 00167; COIL 00168;



M00154 %M00154
 LD Block,'UMA_N': NOCON 00092, 00167, 00169, 00170; COIL 00165;
R00544 %R00544
 LD Block,'UMA_N': LT_INT 00170; GT_INT 00169;
M00189 %M00189
 LD Block,'UMA_N': NCCON 00171; COIL 00169;
R00545 %R00545
 LD Block,'UMA_N': GT_INT 00169;



M00154 %M00154
 LD Block,'UMA_N': NOCON 00092, 00167, 00169, 00170; COIL 00165;
R00544 %R00544
 LD Block,'UMA_N': LT_INT 00170; GT_INT 00169;
M00190 %M00190
 LD Block,'UMA_N': NOCON 00171; COIL 00170;
R00546 %R00546
 LD Block,'UMA_N': LT_INT 00170;



M00190 %M00190
 LD Block,'UMA_N': NOCON 00171; COIL 00170;
M00189 %M00189
 LD Block,'UMA_N': NCCON 00171; COIL 00169;
M00191 %M00191
 LD Block,'UMA_N': NCCON 00167; NOCON 00171; COIL 00171;



M00193 %M00193
 LD Block,'UMA_N': NOCON 00173;
R00547 %R00547
 LD Block,'UMA_N': UPCTR 00173; EQ_INT 00174, 00175;
M00194 %M00194
 LD Block,'UMA_N': NOCON 00173; COIL 00175;



R00547[0] %R00547
 LD Block,'UMA_N': EQ_INT 00174, 00175;
M00155 %M00155
 LD Block,'UMA_N': NOCON 00092, 00176, 00178, 00179; COIL 00174;
 LD Block,'incend': NCCON 00029;



R00547[0] %R00547

LD Block,'UMA_N': EQ_INT 00174, 00175;

M00194 %M00194

LD Block,'UMA_N': NOCON 00173; COIL 00175;



M00155 %M00155

LD Block,'UMA_N': NOCON 00092, 00176, 00178, 00179; COIL 00174;
LD Block,'incend': NCCON 00029;

M00198 %M00198

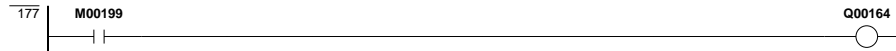
LD Block,'UMA_N': NCCON 00176; NOCON 00180; COIL 00180;

Q00164 %Q00164

LD Block,'UMA_N': NCCON 00176; COIL 00177;

Q00163 %Q00163

LD Block,'UMA_N': NOCON 00098, 00108; COIL 00176;



M00199 %M00199

LD Block,'UMA_N': NOCON 00177;

Q00164 %Q00164

LD Block,'UMA_N': NCCON 00176; COIL 00177;



M00155 %M00155

LD Block,'UMA_N': NOCON 00092, 00176, 00178, 00179; COIL 00174;
LD Block,'incend': NCCON 00029;

R00550 %R00550

LD Block,'UMA_N': LT_INT 00179; GT_INT 00178;

M00196 %M00196

LD Block,'UMA_N': NCCON 00180; COIL 00178;

R00551 %R00551

LD Block,'UMA_N': GT_INT 00178;



M00155 %M00155

LD Block,'UMA_N': NOCON 00092, 00176, 00178, 00179; COIL 00174;
LD Block,'incend': NCCON 00029;

R00550 %R00550

LD Block,'UMA_N': LT_INT 00179; GT_INT 00178;

M00197 %M00197

LD Block,'UMA_N': NOCON 00180; COIL 00179;

R00552 %R00552

LD Block,'UMA_N': LT_INT 00179;



M00197 %M00197

LD Block,'UMA_N': NOCON 00180; COIL 00179;

M00196 %M00196

LD Block,'UMA_N': NCCON 00180; COIL 00178;

M00198 %M00198

LD Block,'UMA_N': NCCON 00176; NOCON 00180; COIL 00180;



M00200 %M00200

LD Block,'UMA_N': NOCON 00182;

R00553 %R00553

LD Block,'UMA_N': UPCTR 00182; EQ_INT 00183, 00184;

M00201 %M00201

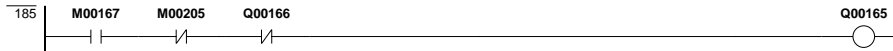
LD Block,'UMA_N': NOCON 00182; COIL 00184;



R00553[0] %R00553
 LD Block,'UMA_N': EQ_INT 00183, 00184;
M00167 %M00167
 LD Block,'UMA_N': NOCON 00092, 00185, 00187, 00188; COIL 00183;



R00553[0] %R00553
 LD Block,'UMA_N': EQ_INT 00183, 00184;
M00201 %M00201
 LD Block,'UMA_N': NOCON 00182; COIL 00184;



M00167 %M00167
 LD Block,'UMA_N': NOCON 00092, 00185, 00187, 00188; COIL 00183;
M00205 %M00205
 LD Block,'UMA_N': NCCON 00185; NOCON 00189; COIL 00189;
Q00166 %Q00166
 LD Block,'UMA_N': NCCON 00185; COIL 00186;
Q00165 %Q00165
 LD Block,'UMA_N': NOCON 00099, 00109; COIL 00185;



M00206 %M00206
 LD Block,'UMA_N': NOCON 00186;
Q00166 %Q00166
 LD Block,'UMA_N': NCCON 00185; COIL 00186;



M00167 %M00167
 LD Block,'UMA_N': NOCON 00092, 00185, 00187, 00188; COIL 00183;
R00556 %R00556
 LD Block,'UMA_N': LT_INT 00188; GT_INT 00187;
M00203 %M00203
 LD Block,'UMA_N': NCCON 00189; COIL 00187;
R00557 %R00557
 LD Block,'UMA_N': GT_INT 00187;



M00167 %M00167
 LD Block,'UMA_N': NOCON 00092, 00185, 00187, 00188; COIL 00183;
R00556 %R00556
 LD Block,'UMA_N': LT_INT 00188; GT_INT 00187;
M00204 %M00204
 LD Block,'UMA_N': NOCON 00189; COIL 00188;
R00558 %R00558
 LD Block,'UMA_N': LT_INT 00188;



M00204 %M00204
 LD Block,'UMA_N': NOCON 00189; COIL 00188;
M00203 %M00203
 LD Block,'UMA_N': NCCON 00189; COIL 00187;
M00205 %M00205
 LD Block,'UMA_N': NCCON 00185; NOCON 00189; COIL 00189;

190 UMA 3



M00215 %M00215
LD Block,'UMA_N': NOCON 00191, 00224, 00226, 00227; COIL 00222;

M00207 %M00207
LD Block,'_MAIN': NOCON 00012, 00051, 00062, 00071;
LD Block,'UMA_N': NOCON 00205, 00206, 00207, 00210, 00216, 00217, 00218, 00219; COIL 00191;

M00216 %M00216
LD Block,'UMA_N': NOCON 00191, 00233, 00235, 00236; COIL 00231;

M00217 %M00217
LD Block,'UMA_N': NOCON 00191, 00242, 00243, 00246, 00247; COIL 00240;

M00218 %M00218
LD Block,'UMA_N': NOCON 00191, 00253, 00254, 00257, 00258; COIL 00251;



Q00184 %Q00184
LD Block,'UMA_N': NOCON 00192, 00199; COIL 00224;

M00816 %M00816
LD Block,'UMA_N': POSCOIL 00192; NOCON 00196;



Q00186 %Q00186
LD Block,'UMA_N': NOCON 00193, 00200; COIL 00233;

M00817 %M00817
LD Block,'UMA_N': POSCOIL 00193; NOCON 00196;



Q00188 %Q00188
LD Block,'UMA_N': NOCON 00194, 00201; COIL 00242;

M00818 %M00818
LD Block,'UMA_N': POSCOIL 00194; NOCON 00196;

Q00189 %Q00189
LD Block,'UMA_N': NOCON 00194, 00201; COIL 00243;



Q00192 %Q00192
LD Block,'UMA_N': NOCON 00195, 00202; COIL 00253;

M00819 %M00819
LD Block,'UMA_N': POSCOIL 00195; NOCON 00196;

Q00193 %Q00193
LD Block,'UMA_N': NOCON 00195, 00202; COIL 00254;



M00816 %M00816
LD Block,'UMA_N': POSCOIL 00192; NOCON 00196;

M00820 %M00820
LD Block,'UMA_N': NOCON 00197; COIL 00196;

M00817 %M00817
LD Block,'UMA_N': POSCOIL 00193; NOCON 00196;

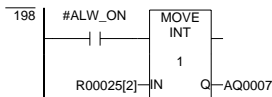
M00818 %M00818
LD Block,'UMA_N': POSCOIL 00194; NOCON 00196;

M00819 %M00819
LD Block,'UMA_N': POSCOIL 00195; NOCON 00196;



M00820 %M00820
LD Block,'UMA_N': NOCON 00197; COIL 00196;

R00025[2] %R00027
LD Block,'UMA_N': SUB_INT 00204, 00204; ADD_INT 00197, 00197; MOVE_INT 00198;



#ALW_ON %S00007

LD Block,'Reloj': NOCON 00001, 00002, 00003, 00004, 00005, 00006, 00007, 00008;
 LD Block,'flujo_1': NOCON 00003, 00006;
 LD Block,'UMA_N': NOCON 00011, 00102, 00198, 00273, 00395, 00510, 00616;
 LD Block,'N_T_ch': NOCON 00001, 00002, 00003, 00004, 00005, 00006, 00007;
 LD Block,'N_cist': NOCON 00002, 00003, 00004, 00005, 00006, 00007, 00008, 00010, 00011, 00012, 00013, 00014, 00015, 00016;
 LD Block,'cister': NOCON 00020;
 LD Block,'genee': NOCON 00011;
 LD Block,'incend': NOCON 00026, 00032, 00033, 00034, 00036, 00039, 00040, 00041, 00042, 00043, 00044, 00045;
 LD Block,'agua': NOCON 00001, 00002, 00003, 00006, 00007, 00008, 00009, 00010, 00011, 00012, 00020, 00021, 00022, 00031, 00032, 00033;
 LD Block,'_MAIN': NOCON 00001, 00002, 00003, 00004, 00005, 00006, 00007, 00008, 00009, 00010, 00011, 00026, 00030, 00033, 00034, 00035, 00049, 00067, 00068, 00078, 00095, 00096, 00121, 00126, 00128, 00129;

R00025[2] %R00027

LD Block,'UMA_N': SUB_INT 00204, 00204; ADD_INT 00197, 00197; MOVE_INT 00198;

AQ0007 %AQ0007

LD Block,'UMA_N': MOVE_INT 00198;

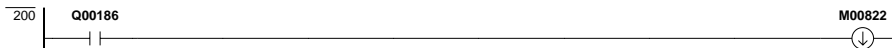


Q00184 %Q00184

LD Block,'UMA_N': NOCON 00192, 00199; COIL 00224;

M00821 %M00821

LD Block,'UMA_N': NEGCOIL 00199; NOCON 00203;



Q00186 %Q00186

LD Block,'UMA_N': NOCON 00193, 00200; COIL 00233;

M00822 %M00822

LD Block,'UMA_N': NEGCOIL 00200; NOCON 00203;



Q00188 %Q00188

LD Block,'UMA_N': NOCON 00194, 00201; COIL 00242;

M00823 %M00823

LD Block,'UMA_N': NEGCOIL 00201; NOCON 00203;

Q00189 %Q00189

LD Block,'UMA_N': NOCON 00194, 00201; COIL 00243;



Q00192 %Q00192

LD Block,'UMA_N': NOCON 00195, 00202; COIL 00253;

M00824 %M00824

LD Block,'UMA_N': NEGCOIL 00202; NOCON 00203;

Q00193 %Q00193

LD Block,'UMA_N': NOCON 00195, 00202; COIL 00254;



M00821 %M00821

LD Block,'UMA_N': NEGCOIL 00199; NOCON 00203;

M00825 %M00825

LD Block,'UMA_N': NOCON 00204; COIL 00203;

M00822 %M00822

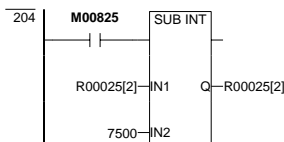
LD Block,'UMA_N': NEGCOIL 00200; NOCON 00203;

M00823 %M00823

LD Block,'UMA_N': NEGCOIL 00201; NOCON 00203;

M00824 %M00824

LD Block,'UMA_N': NEGCOIL 00202; NOCON 00203;



M00825 %M00825

LD Block,'UMA_N': NOCON 00204; COIL 00203;

R00025[2] %R00027

LD Block,'UMA_N': SUB_INT 00204, 00204; ADD_INT 00197, 00197; MOVE_INT 00198;



M00207 %M00207

LD Block,'_MAIN': NOCON 00012, 00051, 00062, 00071;

LD Block,'UMA_N': NOCON 00205, 00206, 00207, 00210, 00216, 00217, 00218, 00219; COIL 00191;

M00214 %M00214

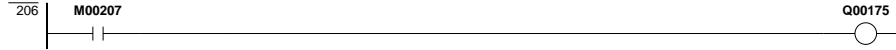
LD Block,'UMA_N': NCCON 00205;

M00212 %M00212

LD Block,'UMA_N': NCCON 00205;

Q00174 %Q00174

LD Block,'UMA_N': COIL 00205;



M00207 %M00207

LD Block,'_MAIN': NOCON 00012, 00051, 00062, 00071;

LD Block,'UMA_N': NOCON 00205, 00206, 00207, 00210, 00216, 00217, 00218, 00219; COIL 00191;

Q00175 %Q00175

LD Block,'UMA_N': COIL 00206;



M00207 %M00207

LD Block,'_MAIN': NOCON 00012, 00051, 00062, 00071;

LD Block,'UMA_N': NOCON 00205, 00206, 00207, 00210, 00216, 00217, 00218, 00219; COIL 00191;

M00209 %M00209

LD Block,'UMA_N': NCCON 00207; NOCON 00210; COIL 00209;

M00210 %M00210

LD Block,'UMA_N': NCCON 00207, 00208; NOCON 00212;

M00221 %M00221

LD Block,'UMA_N': NOCON 00214; COIL 00207;



Q00176 %Q00176

LD Block,'UMA_N': NOCON 00208; COIL 00214;

M00213 %M00213

LD Block,'UMA_N': NCCON 00208; NOCON 00209, 00211; COIL 00211;

M00210 %M00210

LD Block,'UMA_N': NCCON 00207, 00208; NOCON 00212;

M00208 %M00208

LD Block,'UMA_N': NOCON 00208, 00209; COIL 00208;



M00208 %M00208

LD Block,'UMA_N': NOCON 00208, 00209; COIL 00208;

R00559 %R00559

LD Block,'UMA_N': ONDTR_TENTHS 00209;

M00209 %M00209

LD Block,'UMA_N': NCCON 00207; NOCON 00210; COIL 00209;

M00213 %M00213

LD Block,'UMA_N': NCCON 00208; NOCON 00209, 00211; COIL 00211;



M00209 %M00209

LD Block,'UMA_N': NCCON 00207; NOCON 00210; COIL 00209;

M00207 %M00207

LD Block,'_MAIN': NOCON 00012, 00051, 00062, 00071;

LD Block,'UMA_N': NOCON 00205, 00206, 00207, 00210, 00216, 00217, 00218, 00219; COIL 00191;

M00211 %M00211

LD Block,'UMA_N': NCCON 00210; NOCON 00213;

M00222 %M00222

LD Block,'UMA_N': NOCON 00215; COIL 00210;



Q00177 %Q00177

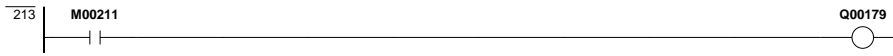
LD Block,'UMA_N': NOCON 00211; COIL 00215;

R00562 %R00562

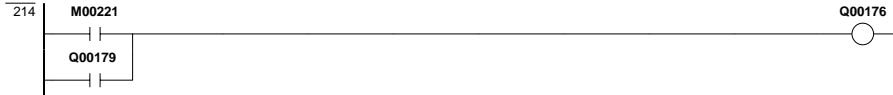
LD Block,'UMA_N': ONDTR_TENTHS 00211;
M00213 %M00213
 LD Block,'UMA_N': NCCON 00208; NOCON 00209, 00211; COIL 00211;



M00210 %M00210
 LD Block,'UMA_N': NCCON 00207, 00208; NOCON 00212;
Q00178 %Q00178
 LD Block,'UMA_N': NOCON 00215; COIL 00212;



M00211 %M00211
 LD Block,'UMA_N': NCCON 00210; NOCON 00213;
Q00179 %Q00179
 LD Block,'UMA_N': NOCON 00214; COIL 00213;



M00221 %M00221
 LD Block,'UMA_N': NOCON 00214; COIL 00207;
Q00176 %Q00176
 LD Block,'UMA_N': NOCON 00208; COIL 00214;
Q00179 %Q00179
 LD Block,'UMA_N': NOCON 00214; COIL 00213;



M00222 %M00222
 LD Block,'UMA_N': NOCON 00215; COIL 00210;
Q00177 %Q00177
 LD Block,'UMA_N': NOCON 00211; COIL 00215;
Q00178 %Q00178
 LD Block,'UMA_N': NOCON 00215; COIL 00212;



M00207 %M00207
 LD Block,'_MAIN': NOCON 00012, 00051, 00062, 00071;
 LD Block,'UMA_N': NOCON 00205, 00206, 00207, 00210, 00216, 00217, 00218, 00219; COIL 00191;
R00566 %R00566
 LD Block,'UMA_N': GT_INT 00216;
Q00180 %Q00180
 LD Block,'UMA_N': COIL 00216;



M00207 %M00207
 LD Block,'_MAIN': NOCON 00012, 00051, 00062, 00071;
 LD Block,'UMA_N': NOCON 00205, 00206, 00207, 00210, 00216, 00217, 00218, 00219; COIL 00191;
R00567 %R00567
 LD Block,'UMA_N': GT_INT 00217;
Q00181 %Q00181
 LD Block,'UMA_N': COIL 00217;



M00207 %M00207
 LD Block,'_MAIN': NOCON 00012, 00051, 00062, 00071;
 LD Block,'UMA_N': NOCON 00205, 00206, 00207, 00210, 00216, 00217, 00218, 00219; COIL 00191;
Q00183 %Q00183
 LD Block,'UMA_N': COIL 00218;
R00568 %R00568
 LD Block,'UMA_N': GE_INT 00218;



M00207 %M00207
 LD Block,'UMA_N': NOCON 00012, 00051, 00062, 00071;
 LD Block,'UMA_N': NOCON 00205, 00206, 00207, 00210, 00216, 00217, 00218, 00219; COIL 00191;
R00569 %R00569
 LD Block,'UMA_N': GT_INT 00219;
Q00182 %Q00182
 LD Block,'UMA_N': COIL 00219;



M00223 %M00223
 LD Block,'UMA_N': NOCON 00221;
R00570 %R00570
 LD Block,'UMA_N': UPCTR 00221; EQ_INT 00222, 00223;
M00224 %M00224
 LD Block,'UMA_N': NOCON 00221; COIL 00223;



R00570[0] %R00570
 LD Block,'UMA_N': EQ_INT 00222, 00223;
M00215 %M00215
 LD Block,'UMA_N': NOCON 00191, 00224, 00226, 00227; COIL 00222;



R00570[0] %R00570
 LD Block,'UMA_N': EQ_INT 00222, 00223;
M00224 %M00224
 LD Block,'UMA_N': NOCON 00221; COIL 00223;



M00215 %M00215
 LD Block,'UMA_N': NOCON 00191, 00224, 00226, 00227; COIL 00222;
M00228 %M00228
 LD Block,'UMA_N': NCCON 00224; NOCON 00228; COIL 00228;
Q00185 %Q00185
 LD Block,'UMA_N': NCCON 00224; COIL 00225;
Q00184 %Q00184
 LD Block,'UMA_N': NOCON 00192, 00199; COIL 00224;



M00229 %M00229
 LD Block,'UMA_N': NOCON 00225;
Q00185 %Q00185
 LD Block,'UMA_N': NCCON 00224; COIL 00225;



M00215 %M00215
 LD Block,'UMA_N': NOCON 00191, 00224, 00226, 00227; COIL 00222;
R00573 %R00573
 LD Block,'UMA_N': LT_INT 00227; GT_INT 00226;
M00226 %M00226
 LD Block,'UMA_N': NCCON 00228; COIL 00226;
R00574 %R00574
 LD Block,'UMA_N': GT_INT 00226;



M00215 %M00215
LD Block,'UMA_N': NOCON 00191, 00224, 00226, 00227; COIL 00222;

R00573 %R00573
LD Block,'UMA_N': LT_INT 00227; GT_INT 00226;

M00227 %M00227
LD Block,'UMA_N': NOCON 00228; COIL 00227;

R00575 %R00575
LD Block,'UMA_N': LT_INT 00227;



M00227 %M00227
LD Block,'UMA_N': NOCON 00228; COIL 00227;

M00226 %M00226
LD Block,'UMA_N': NCCON 00228; COIL 00226;

M00228 %M00228
LD Block,'UMA_N': NCCON 00224; NOCON 00228; COIL 00228;



M00230 %M00230
LD Block,'UMA_N': NOCON 00230;

R00576 %R00576
LD Block,'UMA_N': UPCTR 00230; EQ_INT 00231, 00232;

M00231 %M00231
LD Block,'UMA_N': NOCON 00230; COIL 00232;



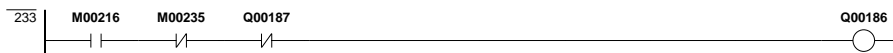
R00576[0] %R00576
LD Block,'UMA_N': EQ_INT 00231, 00232;

M00216 %M00216
LD Block,'UMA_N': NOCON 00191, 00233, 00235, 00236; COIL 00231;



R00576[0] %R00576
LD Block,'UMA_N': EQ_INT 00231, 00232;

M00231 %M00231
LD Block,'UMA_N': NOCON 00230; COIL 00232;



M00216 %M00216
LD Block,'UMA_N': NOCON 00191, 00233, 00235, 00236; COIL 00231;

M00235 %M00235
LD Block,'UMA_N': NCCON 00233; NOCON 00237; COIL 00237;

Q00187 %Q00187
LD Block,'UMA_N': NCCON 00233; COIL 00234;

Q00186 %Q00186
LD Block,'UMA_N': NOCON 00193, 00200; COIL 00233;



M00236 %M00236
LD Block,'UMA_N': NOCON 00234;

Q00187 %Q00187
LD Block,'UMA_N': NCCON 00233; COIL 00234;



M00216 %M00216
LD Block,'UMA_N': NOCON 00191, 00233, 00235, 00236; COIL 00231;
R00579 %R00579
LD Block,'UMA_N': LT_INT 00236; GT_INT 00235;
M00233 %M00233
LD Block,'UMA_N': NCCON 00237; COIL 00235;
R00580 %R00580
LD Block,'UMA_N': GT_INT 00235;



M00216 %M00216
LD Block,'UMA_N': NOCON 00191, 00233, 00235, 00236; COIL 00231;
R00579 %R00579
LD Block,'UMA_N': LT_INT 00236; GT_INT 00235;
M00234 %M00234
LD Block,'UMA_N': NOCON 00237; COIL 00236;
R00581 %R00581
LD Block,'UMA_N': LT_INT 00236;



M00234 %M00234
LD Block,'UMA_N': NOCON 00237; COIL 00236;
M00233 %M00233
LD Block,'UMA_N': NCCON 00237; COIL 00235;
M00235 %M00235
LD Block,'UMA_N': NCCON 00233; NOCON 00237; COIL 00237;



M00237 %M00237
LD Block,'UMA_N': NOCON 00239;
R00582 %R00582
LD Block,'UMA_N': UPCTR 00239; EQ_INT 00240, 00241;
M00238 %M00238
LD Block,'UMA_N': NOCON 00239; COIL 00241;



R00582[0] %R00582
LD Block,'UMA_N': EQ_INT 00240, 00241;
M00217 %M00217
LD Block,'UMA_N': NOCON 00191, 00242, 00243, 00246, 00247; COIL 00240;



R00582[0] %R00582
LD Block,'UMA_N': EQ_INT 00240, 00241;
M00238 %M00238
LD Block,'UMA_N': NOCON 00239; COIL 00241;



M00217 %M00217
LD Block,'UMA_N': NOCON 00191, 00242, 00243, 00246, 00247; COIL 00240;
M00242 %M00242
LD Block,'UMA_N': NCCON 00242, 00243; NOCON 00248; COIL 00248;
Q00190 %Q00190

LD Block,'UMA_N': NCCON 00242; COIL 00244;
Q00188 %Q00188
 LD Block,'UMA_N': NOCON 00194, 00201; COIL 00242;



M00217 %M00217
 LD Block,'UMA_N': NOCON 00191, 00242, 00243, 00246, 00247; COIL 00240;
M00242 %M00242
 LD Block,'UMA_N': NCCON 00242, 00243; NOCON 00248; COIL 00248;
Q00191 %Q00191
 LD Block,'UMA_N': NCCON 00243; COIL 00245;
Q00189 %Q00189
 LD Block,'UMA_N': NOCON 00194, 00201; COIL 00243;



M00243 %M00243
 LD Block,'UMA_N': NOCON 00244;
Q00190 %Q00190
 LD Block,'UMA_N': NCCON 00242; COIL 00244;



M00244 %M00244
 LD Block,'UMA_N': NOCON 00245;
Q00191 %Q00191
 LD Block,'UMA_N': NCCON 00243; COIL 00245;



M00217 %M00217
 LD Block,'UMA_N': NOCON 00191, 00242, 00243, 00246, 00247; COIL 00240;
R00585 %R00585
 LD Block,'UMA_N': LT_INT 00247; GT_INT 00246;
M00240 %M00240
 LD Block,'UMA_N': NCCON 00248; COIL 00246;
R00586 %R00586
 LD Block,'UMA_N': GT_INT 00246;



M00217 %M00217
 LD Block,'UMA_N': NOCON 00191, 00242, 00243, 00246, 00247; COIL 00240;
R00585 %R00585
 LD Block,'UMA_N': LT_INT 00247; GT_INT 00246;
M00241 %M00241
 LD Block,'UMA_N': NOCON 00248; COIL 00247;
R00587 %R00587
 LD Block,'UMA_N': LT_INT 00247;



M00241 %M00241
 LD Block,'UMA_N': NOCON 00248; COIL 00247;
M00240 %M00240
 LD Block,'UMA_N': NCCON 00248; COIL 00246;
M00242 %M00242
 LD Block,'UMA_N': NCCON 00242, 00243; NOCON 00248; COIL 00248;



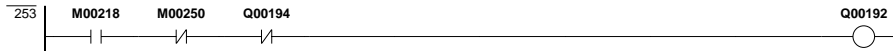
M00245 %M00245
 LD Block,'UMA_N': NOCON 00250;
R00588 %R00588
 LD Block,'UMA_N': UPCTR 00250; EQ_INT 00251, 00252;
M00246 %M00246
 LD Block,'UMA_N': NOCON 00250; COIL 00252;



R00588[0] %R00588
 LD Block,'UMA_N': EQ_INT 00251, 00252;
M00218 %M00218
 LD Block,'UMA_N': NOCON 00191, 00253, 00254, 00257, 00258; COIL 00251;



R00588[0] %R00588
 LD Block,'UMA_N': EQ_INT 00251, 00252;
M00246 %M00246
 LD Block,'UMA_N': NOCON 00250; COIL 00252;



M00218 %M00218
 LD Block,'UMA_N': NOCON 00191, 00253, 00254, 00257, 00258; COIL 00251;
M00250 %M00250
 LD Block,'UMA_N': NCCON 00253, 00254; NOCON 00259; COIL 00259;
Q00194 %Q00194
 LD Block,'UMA_N': NCCON 00253; COIL 00255;
Q00192 %Q00192
 LD Block,'UMA_N': NOCON 00195, 00202; COIL 00253;



M00218 %M00218
 LD Block,'UMA_N': NOCON 00191, 00253, 00254, 00257, 00258; COIL 00251;
M00250 %M00250
 LD Block,'UMA_N': NCCON 00253, 00254; NOCON 00259; COIL 00259;
Q00195 %Q00195
 LD Block,'UMA_N': NCCON 00254; COIL 00256;
Q00193 %Q00193
 LD Block,'UMA_N': NOCON 00195, 00202; COIL 00254;



M00251 %M00251
 LD Block,'UMA_N': NOCON 00255;
Q00194 %Q00194
 LD Block,'UMA_N': NCCON 00253; COIL 00255;



M00252 %M00252
 LD Block,'UMA_N': NOCON 00256;
Q00195 %Q00195
 LD Block,'UMA_N': NCCON 00254; COIL 00256;



M00218 %M00218
 LD Block,'UMA_N': NOCON 00191, 00253, 00254, 00257, 00258; COIL 00251;
R00591 %R00591
 LD Block,'UMA_N': LT_INT 00258; GT_INT 00257;
M00248 %M00248
 LD Block,'UMA_N': NCCON 00259; COIL 00257;
R00592 %R00592
 LD Block,'UMA_N': GT_INT 00257;



M00218 %M00218
 LD Block,'UMA_N': NOCON 00191, 00253, 00254, 00257, 00258; COIL 00251;
R00591 %R00591
 LD Block,'UMA_N': LT_INT 00258; GT_INT 00257;
M00249 %M00249
 LD Block,'UMA_N': NOCON 00259; COIL 00258;
R00593 %R00593

LD Block,'UMA_N': LT_INT 00258;



M00249 %M00249

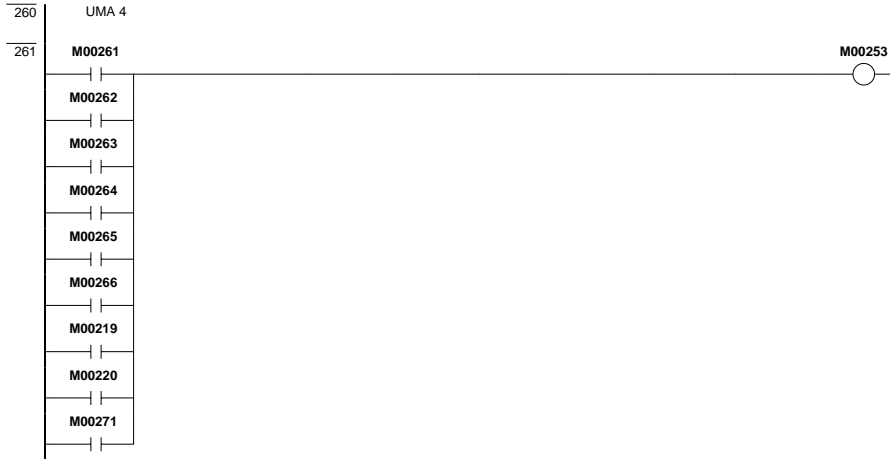
LD Block,'UMA_N': NOCON 00259; COIL 00258;

M00248 %M00248

LD Block,'UMA_N': NCCON 00259; COIL 00257;

M00250 %M00250

LD Block,'UMA_N': NCCON 00253, 00254; NOCON 00259; COIL 00259;



M00261 %M00261

LD Block,'UMA_N': NOCON 00261, 00304, 00306, 00307; COIL 00302;

M00253 %M00253

LD Block,'_MAIN': NOCON 00012, 00051, 00063, 00072;

LD Block,'UMA_N': NOCON 00285, 00286, 00287, 00290, 00296, 00297, 00298, 00299; COIL 00261;

M00262 %M00262

LD Block,'UMA_N': NOCON 00261, 00313, 00315, 00316; COIL 00311;

M00263 %M00263

LD Block,'UMA_N': NOCON 00261, 00322, 00324, 00325; COIL 00320;

M00264 %M00264

LD Block,'UMA_N': NOCON 00261, 00331, 00333, 00334; COIL 00329;

M00265 %M00265

LD Block,'UMA_N': NOCON 00261, 00340, 00341, 00344, 00345; COIL 00338;

M00266 %M00266

LD Block,'UMA_N': NOCON 00261, 00351, 00353, 00354; COIL 00349;

M00219 %M00219

LD Block,'UMA_N': NOCON 00261, 00360, 00362, 00363; COIL 00358;

M00220 %M00220

LD Block,'UMA_N': NOCON 00261, 00369, 00371, 00372; COIL 00367;

M00271 %M00271

LD Block,'UMA_N': NOCON 00261, 00378, 00380, 00381; COIL 00376;



Q00210 %Q00210

LD Block,'UMA_N': NOCON 00262, 00274; COIL 00304;

M00826 %M00826

LD Block,'UMA_N': POSCOIL 00262; NOCON 00271;



Q00212 %Q00212

LD Block,'UMA_N': NOCON 00263, 00275; COIL 00313;

M00827 %M00827

LD Block,'UMA_N': POSCOIL 00263; NOCON 00271;

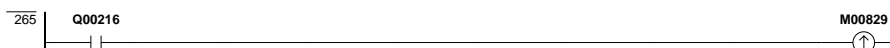


Q00214 %Q00214

LD Block,'UMA_N': NOCON 00264, 00276; COIL 00322;

M00828 %M00828

LD Block,'UMA_N': POSCOIL 00264; NOCON 00271;



Q00216 %Q00216

LD Block,'UMA_N': NOCON 00265, 00277; COIL 00331;

M00829 %M00829

LD Block,'UMA_N': POSCOIL 00265; NOCON 00271;



Q00218 %Q00218
 LD Block,'UMA_N': NOCON 00266, 00278; COIL 00340;
M00830 %M00830
 LD Block,'UMA_N': POSCOIL 00266; NOCON 00271;
Q00219 %Q00219
 LD Block,'UMA_N': NOCON 00266, 00278; COIL 00341;



Q00222 %Q00222
 LD Block,'UMA_N': NOCON 00267, 00279; COIL 00351;
M00831 %M00831
 LD Block,'UMA_N': POSCOIL 00267; NOCON 00271;



Q00224 %Q00224
 LD Block,'UMA_N': NOCON 00268, 00280; COIL 00360;
M00832 %M00832
 LD Block,'UMA_N': POSCOIL 00268; NOCON 00271;



Q00226 %Q00226
 LD Block,'UMA_N': NOCON 00269, 00281; COIL 00369;
M00833 %M00833
 LD Block,'UMA_N': POSCOIL 00269; NOCON 00271;



Q00228 %Q00228
 LD Block,'UMA_N': NOCON 00270, 00282; COIL 00378;
M00834 %M00834
 LD Block,'UMA_N': POSCOIL 00270; NOCON 00271;

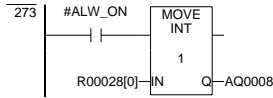


M00826 %M00826
 LD Block,'UMA_N': POSCOIL 00262; NOCON 00271;
M00835 %M00835
 LD Block,'UMA_N': NOCON 00272; COIL 00271;
M00827 %M00827
 LD Block,'UMA_N': POSCOIL 00263; NOCON 00271;
M00828 %M00828
 LD Block,'UMA_N': POSCOIL 00264; NOCON 00271;
M00829 %M00829
 LD Block,'UMA_N': POSCOIL 00265; NOCON 00271;
M00830 %M00830
 LD Block,'UMA_N': POSCOIL 00266; NOCON 00271;
M00831 %M00831
 LD Block,'UMA_N': POSCOIL 00267; NOCON 00271;
M00832 %M00832
 LD Block,'UMA_N': POSCOIL 00268; NOCON 00271;
M00833 %M00833
 LD Block,'UMA_N': POSCOIL 00269; NOCON 00271;
M00834 %M00834
 LD Block,'UMA_N': POSCOIL 00270; NOCON 00271;



M00835 %M00835
 LD Block,'UMA_N': NOCON 00272; COIL 00271;
R00028[0] %R00028

LD Block,'UMA_N': SUB_INT 00284, 00284; ADD_INT 00272, 00272; MOVE_INT 00273;



#ALW_ON %S0007

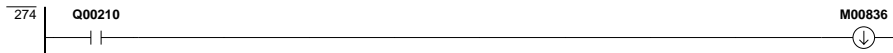
LD Block,'Reloj': NOCON 00001, 00002, 00003, 00004, 00005, 00006, 00007, 00008;
LD Block,'flujo_1': NOCON 00003, 00006;
LD Block,'UMA_N': NOCON 00011, 00102, 00198, 00273, 00395, 00510, 00616;
LD Block,'N_T_ch': NOCON 00001, 00002, 00003, 00004, 00005, 00006, 00007;
LD Block,'N_cist': NOCON 00002, 00003, 00004, 00005, 00006, 00007, 00008, 00010, 00011, 00012, 00013, 00014, 00015, 00016;
LD Block,'cister': NOCON 00020;
LD Block,'genee': NOCON 00011;
LD Block,'incend': NOCON 00026, 00032, 00033, 00034, 00036, 00039, 00040, 00041, 00042, 00043, 00044, 00045;
LD Block,'agua': NOCON 00001, 00002, 00003, 00006, 00007, 00008, 00009, 00010, 00011, 00012, 00020, 00021, 00022, 00031, 00032, 00033;
LD Block,'_MAIN': NOCON 00001, 00002, 00003, 00004, 00005, 00006, 00007, 00008, 00009, 00010, 00011, 00026, 00030, 00033, 00034, 00035, 00049, 00067, 00068, 00078, 00095, 00096, 00121, 00126, 00128, 00129;

R00028[0] %R00028

LD Block,'UMA_N': SUB_INT 00284, 00284; ADD_INT 00272, 00272; MOVE_INT 00273;

AQ0008 %AQ0008

LD Block,'UMA_N': MOVE_INT 00273;



Q00210 %Q00210

LD Block,'UMA_N': NOCON 00262, 00274; COIL 00304;

M00836 %M00836

LD Block,'UMA_N': NEGCOIL 00274; NOCON 00283;

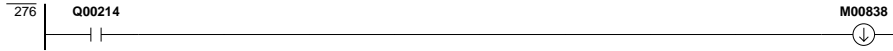


Q00212 %Q00212

LD Block,'UMA_N': NOCON 00263, 00275; COIL 00313;

M00837 %M00837

LD Block,'UMA_N': NEGCOIL 00275; NOCON 00283;



Q00214 %Q00214

LD Block,'UMA_N': NOCON 00264, 00276; COIL 00322;

M00838 %M00838

LD Block,'UMA_N': NEGCOIL 00276; NOCON 00283;



Q00216 %Q00216

LD Block,'UMA_N': NOCON 00265, 00277; COIL 00331;

M00839 %M00839

LD Block,'UMA_N': NEGCOIL 00277; NOCON 00283;



Q00218 %Q00218

LD Block,'UMA_N': NOCON 00266, 00278; COIL 00340;

M00840 %M00840

LD Block,'UMA_N': NEGCOIL 00278; NOCON 00283;

Q00219 %Q00219

LD Block,'UMA_N': NOCON 00266, 00278; COIL 00341;

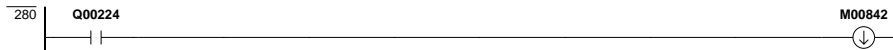


Q00222 %Q00222

LD Block,'UMA_N': NOCON 00267, 00279; COIL 00351;

M00841 %M00841

LD Block,'UMA_N': NEGCOIL 00279; NOCON 00283;



Q00224 %Q00224

LD Block,'UMA_N': NOCON 00268, 00280; COIL 00360;

M00842 %M00842

LD Block,'UMA_N': NEGCOIL 00280; NOCON 00283;



Q00226 %Q00226

LD Block,'UMA_N': NOCON 00269, 00281; COIL 00369;

M00843 %M00843

LD Block,'UMA_N': NEGCOIL 00281; NOCON 00283;



Q00228 %Q00228
 LD Block,'UMA_N': NOCON 00270, 00282; COIL 00378;
M00844 %M00844
 LD Block,'UMA_N': NEGCOIL 00282; NOCON 00283;



M00836 %M00836
 LD Block,'UMA_N': NEGCOIL 00274; NOCON 00283;
M00845 %M00845
 LD Block,'UMA_N': NOCON 00284; COIL 00283;
M00837 %M00837
 LD Block,'UMA_N': NEGCOIL 00275; NOCON 00283;
M00838 %M00838
 LD Block,'UMA_N': NEGCOIL 00276; NOCON 00283;
M00839 %M00839
 LD Block,'UMA_N': NEGCOIL 00277; NOCON 00283;
M00840 %M00840
 LD Block,'UMA_N': NEGCOIL 00278; NOCON 00283;
M00841 %M00841
 LD Block,'UMA_N': NEGCOIL 00279; NOCON 00283;
M00842 %M00842
 LD Block,'UMA_N': NEGCOIL 00280; NOCON 00283;
M00843 %M00843
 LD Block,'UMA_N': NEGCOIL 00281; NOCON 00283;
M00844 %M00844
 LD Block,'UMA_N': NEGCOIL 00282; NOCON 00283;



M00845 %M00845
 LD Block,'UMA_N': NOCON 00284; COIL 00283;
R00028[0] %R00028
 LD Block,'UMA_N': SUB_INT 00284, 00284; ADD_INT 00272, 00272; MOVE_INT 00273;



M00253 %M00253
 LD Block,'_MAIN': NOCON 00012, 00051, 00063, 00072;
 LD Block,'UMA_N': NOCON 00285, 00286, 00287, 00290, 00296, 00297, 00298, 00299; COIL 00261;
M00260 %M00260
 LD Block,'UMA_N': NCCON 00285;
M00258 %M00258
 LD Block,'UMA_N': NCCON 00285, 00288;
Q00200 %Q00200
 LD Block,'UMA_N': COIL 00285;



M00253 %M00253
 LD Block,'_MAIN': NOCON 00012, 00051, 00063, 00072;
 LD Block,'UMA_N': NOCON 00285, 00286, 00287, 00290, 00296, 00297, 00298, 00299; COIL 00261;
Q00201 %Q00201
 LD Block,'UMA_N': COIL 00286;



M00253 %M00253
 LD Block,'_MAIN': NOCON 00012, 00051, 00063, 00072;
 LD Block,'UMA_N': NOCON 00285, 00286, 00287, 00290, 00296, 00297, 00298, 00299; COIL 00261;
M00255 %M00255
 LD Block,'UMA_N': NCCON 00287; NOCON 00290; COIL 00289;
M00256 %M00256
 LD Block,'UMA_N': NCCON 00287, 00288; NOCON 00292;

M00267 %M00267

LD Block,'UMA_N': NOCON 00294; COIL 00287;



Q00202 %Q00202

LD Block,'UMA_N': NOCON 00288; COIL 00294;

M00258 %M00258

LD Block,'UMA_N': NCCON 00285, 00288;

M00256 %M00256

LD Block,'UMA_N': NCCON 00287, 00288; NOCON 00292;

M00254 %M00254

LD Block,'UMA_N': NOCON 00288, 00289; COIL 00288;



M00254 %M00254

LD Block,'UMA_N': NOCON 00288, 00289; COIL 00288;

R00594 %R00594

LD Block,'UMA_N': ONDTR_TENTHS 00289;

M00255 %M00255

LD Block,'UMA_N': NCCON 00287; NOCON 00290; COIL 00289;

M00259 %M00259

LD Block,'UMA_N': NOCON 00289, 00291; COIL 00291;



M00255 %M00255

LD Block,'UMA_N': NCCON 00287; NOCON 00290; COIL 00289;

M00253 %M00253

LD Block,'_MAIN': NOCON 00012, 00051, 00063, 00072;

LD Block,'UMA_N': NOCON 00285, 00286, 00287, 00290, 00296, 00297, 00298, 00299; COIL 00261;

M00257 %M00257

LD Block,'UMA_N': NCCON 00290; NOCON 00293;

M00268 %M00268

LD Block,'UMA_N': NOCON 00295; COIL 00290;



Q00203 %Q00203

LD Block,'UMA_N': NOCON 00291; COIL 00295;

R00597 %R00597

LD Block,'UMA_N': ONDTR_TENTHS 00291;

M00259 %M00259

LD Block,'UMA_N': NOCON 00289, 00291; COIL 00291;

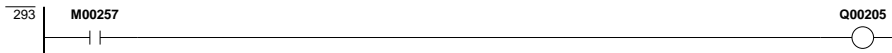


M00256 %M00256

LD Block,'UMA_N': NCCON 00287, 00288; NOCON 00292;

Q00204 %Q00204

LD Block,'UMA_N': NOCON 00295; COIL 00292;



M00257 %M00257

LD Block,'UMA_N': NCCON 00290; NOCON 00293;

Q00205 %Q00205

LD Block,'UMA_N': NOCON 00294; COIL 00293;



M00267 %M00267

LD Block,'UMA_N': NOCON 00294; COIL 00287;

Q00202 %Q00202

LD Block,'UMA_N': NOCON 00288; COIL 00294;

Q00205 %Q00205

LD Block,'UMA_N': NOCON 00294; COIL 00293;



M00268 %M00268

LD Block,'UMA_N': NOCON 00295; COIL 00290;
Q00203 %Q00203
 LD Block,'UMA_N': NOCON 00291; COIL 00295;
Q00204 %Q00204
 LD Block,'UMA_N': NOCON 00295; COIL 00292;



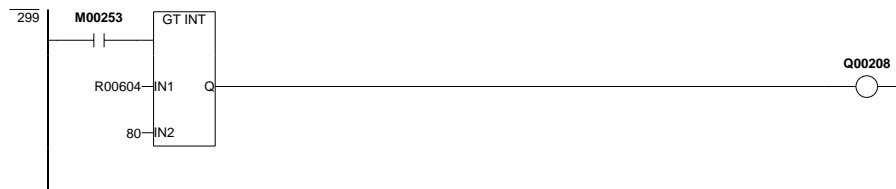
M00253 %M00253
 LD Block,'_MAIN': NOCON 00012, 00051, 00063, 00072;
 LD Block,'UMA_N': NOCON 00285, 00286, 00287, 00290, 00296, 00297, 00298, 00299; COIL 00261;
R00601 %R00601
 LD Block,'UMA_N': GT_INT 00296;
Q00206 %Q00206
 LD Block,'UMA_N': COIL 00296;



M00253 %M00253
 LD Block,'_MAIN': NOCON 00012, 00051, 00063, 00072;
 LD Block,'UMA_N': NOCON 00285, 00286, 00287, 00290, 00296, 00297, 00298, 00299; COIL 00261;
R00602 %R00602
 LD Block,'UMA_N': GT_INT 00297;
Q00207 %Q00207
 LD Block,'UMA_N': COIL 00297;



M00253 %M00253
 LD Block,'_MAIN': NOCON 00012, 00051, 00063, 00072;
 LD Block,'UMA_N': NOCON 00285, 00286, 00287, 00290, 00296, 00297, 00298, 00299; COIL 00261;
Q00209 %Q00209
 LD Block,'UMA_N': COIL 00298;
R00603 %R00603
 LD Block,'UMA_N': GE_INT 00298;



M00253 %M00253
 LD Block,'_MAIN': NOCON 00012, 00051, 00063, 00072;
 LD Block,'UMA_N': NOCON 00285, 00286, 00287, 00290, 00296, 00297, 00298, 00299; COIL 00261;
R00604 %R00604
 LD Block,'UMA_N': GT_INT 00299;
Q00208 %Q00208
 LD Block,'UMA_N': COIL 00299;



M00269 %M00269
 LD Block,'UMA_N': NOCON 00301;
R00605 %R00605
 LD Block,'UMA_N': UPCTR 00301; EQ_INT 00302, 00303;
M00270 %M00270
 LD Block,'UMA_N': NOCON 00301; COIL 00303;



R00605[0] %R00605
 LD Block,'UMA_N': EQ_INT 00302, 00303;

M00261 %M00261

LD Block,'UMA_N': NOCON 00261, 00304, 00306, 00307; COIL 00302;



R00605[0] %R00605

LD Block,'UMA_N': EQ_INT 00302, 00303;

M00270 %M00270

LD Block,'UMA_N': NOCON 00301; COIL 00303;



M00261 %M00261

LD Block,'UMA_N': NOCON 00261, 00304, 00306, 00307; COIL 00302;

M00274 %M00274

LD Block,'UMA_N': NCCON 00304; NOCON 00308; COIL 00308;

Q00211 %Q00211

LD Block,'UMA_N': NCCON 00304; COIL 00305;

Q00210 %Q00210

LD Block,'UMA_N': NOCON 00262, 00274; COIL 00304;



M00275 %M00275

LD Block,'UMA_N': NOCON 00305;

Q00211 %Q00211

LD Block,'UMA_N': NCCON 00304; COIL 00305;



M00261 %M00261

LD Block,'UMA_N': NOCON 00261, 00304, 00306, 00307; COIL 00302;

R00608 %R00608

LD Block,'UMA_N': LT_INT 00307; GT_INT 00306;

M00272 %M00272

LD Block,'UMA_N': NCCON 00308; COIL 00306;

R00609 %R00609

LD Block,'UMA_N': GT_INT 00306;



M00261 %M00261

LD Block,'UMA_N': NOCON 00261, 00304, 00306, 00307; COIL 00302;

R00608 %R00608

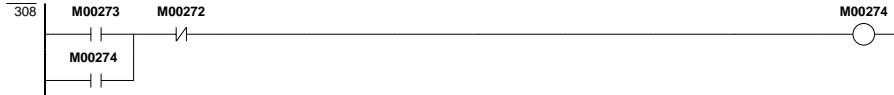
LD Block,'UMA_N': LT_INT 00307; GT_INT 00306;

M00273 %M00273

LD Block,'UMA_N': NOCON 00308; COIL 00307;

R00610 %R00610

LD Block,'UMA_N': LT_INT 00307;



M00273 %M00273

LD Block,'UMA_N': NOCON 00308; COIL 00307;

M00272 %M00272

LD Block,'UMA_N': NCCON 00308; COIL 00306;

M00274 %M00274

LD Block,'UMA_N': NCCON 00304; NOCON 00308; COIL 00308;



M00276 %M00276

LD Block,'UMA_N': NOCON 00310;

R00611 %R00611

LD Block,'UMA_N': UPCTR 00310; EQ_INT 00311, 00312;

M00277 %M00277

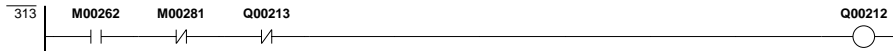
LD Block,'UMA_N': NOCON 00310; COIL 00312;



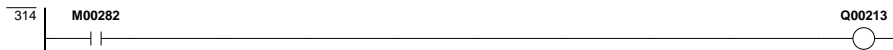
R00611[0] %R00611
 LD Block,'UMA_N': EQ_INT 00311, 00312;
M00262 %M00262
 LD Block,'UMA_N': NOCON 00261, 00313, 00315, 00316; COIL 00311;



R00611[0] %R00611
 LD Block,'UMA_N': EQ_INT 00311, 00312;
M00277 %M00277
 LD Block,'UMA_N': NOCON 00310; COIL 00312;



M00262 %M00262
 LD Block,'UMA_N': NOCON 00261, 00313, 00315, 00316; COIL 00311;
M00281 %M00281
 LD Block,'UMA_N': NCCON 00313; NOCON 00317; COIL 00317;
Q00213 %Q00213
 LD Block,'UMA_N': NCCON 00313; COIL 00314;
Q00212 %Q00212
 LD Block,'UMA_N': NOCON 00263, 00275; COIL 00313;



M00282 %M00282
 LD Block,'UMA_N': NOCON 00314;
Q00213 %Q00213
 LD Block,'UMA_N': NCCON 00313; COIL 00314;



M00262 %M00262
 LD Block,'UMA_N': NOCON 00261, 00313, 00315, 00316; COIL 00311;
R00614 %R00614
 LD Block,'UMA_N': LT_INT 00316; GT_INT 00315;
M00279 %M00279
 LD Block,'UMA_N': NCCON 00317; COIL 00315;
R00615 %R00615
 LD Block,'UMA_N': GT_INT 00315;



M00262 %M00262
 LD Block,'UMA_N': NOCON 00261, 00313, 00315, 00316; COIL 00311;
R00614 %R00614
 LD Block,'UMA_N': LT_INT 00316; GT_INT 00315;
M00280 %M00280
 LD Block,'UMA_N': NOCON 00317; COIL 00316;
R00616 %R00616
 LD Block,'UMA_N': LT_INT 00316;



M00280 %M00280
 LD Block,'UMA_N': NOCON 00317; COIL 00316;
M00279 %M00279
 LD Block,'UMA_N': NCCON 00317; COIL 00315;
M00281 %M00281
 LD Block,'UMA_N': NCCON 00313; NOCON 00317; COIL 00317;

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M00283 %M00283
LD Block,'UMA_N': NOCON 00319;

R00617 %R00617
LD Block,'UMA_N': UPCTR 00319; EQ_INT 00320, 00321;

M00284 %M00284
LD Block,'UMA_N': NOCON 00319; COIL 00321;



R00617[0] %R00617
LD Block,'UMA_N': EQ_INT 00320, 00321;

M00263 %M00263
LD Block,'UMA_N': NOCON 00261, 00322, 00324, 00325; COIL 00320;



R00617[0] %R00617
LD Block,'UMA_N': EQ_INT 00320, 00321;

M00284 %M00284
LD Block,'UMA_N': NOCON 00319; COIL 00321;



M00263 %M00263
LD Block,'UMA_N': NOCON 00261, 00322, 00324, 00325; COIL 00320;

M00288 %M00288
LD Block,'UMA_N': NCCON 00322; NOCON 00326; COIL 00326;

Q00215 %Q00215
LD Block,'UMA_N': NCCON 00322; COIL 00323;

Q00214 %Q00214
LD Block,'UMA_N': NOCON 00264, 00276; COIL 00322;



M00289 %M00289
LD Block,'UMA_N': NOCON 00323;

Q00215 %Q00215
LD Block,'UMA_N': NCCON 00322; COIL 00323;



M00263 %M00263
LD Block,'UMA_N': NOCON 00261, 00322, 00324, 00325; COIL 00320;

R00620 %R00620
LD Block,'UMA_N': LT_INT 00325; GT_INT 00324;

M00286 %M00286
LD Block,'UMA_N': NCCON 00326; COIL 00324;

R00621 %R00621
LD Block,'UMA_N': GT_INT 00324;

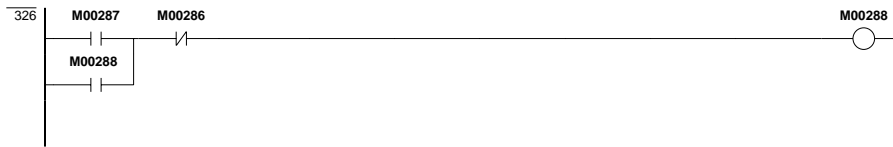


M00263 %M00263
LD Block,'UMA_N': NOCON 00261, 00322, 00324, 00325; COIL 00320;

R00620 %R00620
LD Block,'UMA_N': LT_INT 00325; GT_INT 00324;

M00287 %M00287
LD Block,'UMA_N': NOCON 00326; COIL 00325;

R00622 %R00622
LD Block,'UMA_N': LT_INT 00325;



M00287 %M00287
LD Block,'UMA_N': NOCON 00326; COIL 00325;

M00286 %M00286
LD Block,'UMA_N': NCCON 00326; COIL 00324;

M00288 %M00288
LD Block,'UMA_N': NCCON 00322; NOCON 00326; COIL 00326;



M00290 %M00290
LD Block,'UMA_N': NOCON 00328;

R00623 %R00623
LD Block,'UMA_N': UPCTR 00328; EQ_INT 00329, 00330;

M00291 %M00291
LD Block,'UMA_N': NOCON 00328; COIL 00330;



R00623[0] %R00623
LD Block,'UMA_N': EQ_INT 00329, 00330;

M00264 %M00264
LD Block,'UMA_N': NOCON 00261, 00331, 00333, 00334; COIL 00329;



R00623[0] %R00623
LD Block,'UMA_N': EQ_INT 00329, 00330;

M00291 %M00291
LD Block,'UMA_N': NOCON 00328; COIL 00330;



M00264 %M00264
LD Block,'UMA_N': NOCON 00261, 00331, 00333, 00334; COIL 00329;

M00295 %M00295
LD Block,'UMA_N': NCCON 00331; NOCON 00335; COIL 00335;

Q00217 %Q00217
LD Block,'UMA_N': NCCON 00331; COIL 00332;

Q00216 %Q00216
LD Block,'UMA_N': NOCON 00265, 00277; COIL 00331;



M00296 %M00296
LD Block,'UMA_N': NOCON 00332;

Q00217 %Q00217
LD Block,'UMA_N': NCCON 00331; COIL 00332;



M00264 %M00264
LD Block,'UMA_N': NOCON 00261, 00331, 00333, 00334; COIL 00329;

R00626 %R00626
LD Block,'UMA_N': LT_INT 00334; GT_INT 00333;

M00293 %M00293
LD Block,'UMA_N': NCCON 00335; COIL 00333;

R00627 %R00627
LD Block,'UMA_N': GT_INT 00333;



M00264 %M00264
LD Block,'UMA_N': NOCON 00261, 00331, 00333, 00334; COIL 00329;

R00626 %R00626
LD Block,'UMA_N': LT_INT 00334; GT_INT 00333;

M00294 %M00294
LD Block,'UMA_N': NOCON 00335; COIL 00334;

R00628 %R00628
LD Block,'UMA_N': LT_INT 00334;



M00294 %M00294
LD Block,'UMA_N': NOCON 00335; COIL 00334;

M00293 %M00293
LD Block,'UMA_N': NCCON 00335; COIL 00333;

M00295 %M00295
LD Block,'UMA_N': NCCON 00331; NOCON 00335; COIL 00335;



M00297 %M00297
LD Block,'UMA_N': NOCON 00337;

R00629 %R00629
LD Block,'UMA_N': UPCTR 00337; EQ_INT 00338, 00339;

M00298 %M00298
LD Block,'UMA_N': NOCON 00337; COIL 00339;



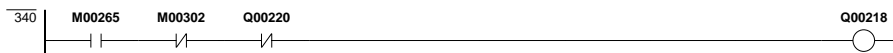
R00629[0] %R00629
LD Block,'UMA_N': EQ_INT 00338, 00339;

M00265 %M00265
LD Block,'UMA_N': NOCON 00261, 00340, 00341, 00344, 00345; COIL 00338;



R00629[0] %R00629
LD Block,'UMA_N': EQ_INT 00338, 00339;

M00298 %M00298
LD Block,'UMA_N': NOCON 00337; COIL 00339;



M00265 %M00265
LD Block,'UMA_N': NOCON 00261, 00340, 00341, 00344, 00345; COIL 00338;

M00302 %M00302
LD Block,'UMA_N': NCCON 00340, 00341; NOCON 00346; COIL 00346;

Q00220 %Q00220
LD Block,'UMA_N': NCCON 00340; COIL 00342;

Q00218 %Q00218
LD Block,'UMA_N': NOCON 00266, 00278; COIL 00340;



M00265 %M00265
LD Block,'UMA_N': NOCON 00261, 00340, 00341, 00344, 00345; COIL 00338;

M00302 %M00302
LD Block,'UMA_N': NCCON 00340, 00341; NOCON 00346; COIL 00346;

Q00221 %Q00221
LD Block,'UMA_N': NCCON 00341; COIL 00343;

Q00219 %Q00219
LD Block,'UMA_N': NOCON 00266, 00278; COIL 00341;



M00303 %M00303
 LD Block,'UMA_N': NOCON 00342;
Q00220 %Q00220
 LD Block,'UMA_N': NCCON 00340; COIL 00342;



M00304 %M00304
 LD Block,'UMA_N': NOCON 00343;
Q00221 %Q00221
 LD Block,'UMA_N': NCCON 00341; COIL 00343;



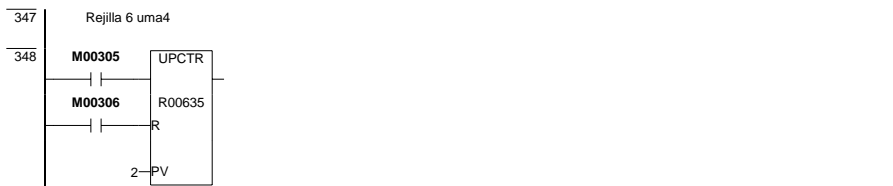
M00265 %M00265
 LD Block,'UMA_N': NOCON 00261, 00340, 00341, 00344, 00345; COIL 00338;
R00632 %R00632
 LD Block,'UMA_N': LT_INT 00345; GT_INT 00344;
M00300 %M00300
 LD Block,'UMA_N': NCCON 00346; COIL 00344;
R00633 %R00633
 LD Block,'UMA_N': GT_INT 00344;



M00265 %M00265
 LD Block,'UMA_N': NOCON 00261, 00340, 00341, 00344, 00345; COIL 00338;
R00632 %R00632
 LD Block,'UMA_N': LT_INT 00345; GT_INT 00344;
M00301 %M00301
 LD Block,'UMA_N': NOCON 00346; COIL 00345;
R00634 %R00634
 LD Block,'UMA_N': LT_INT 00345;



M00301 %M00301
 LD Block,'UMA_N': NOCON 00346; COIL 00345;
M00300 %M00300
 LD Block,'UMA_N': NCCON 00346; COIL 00344;
M00302 %M00302
 LD Block,'UMA_N': NCCON 00340, 00341; NOCON 00346; COIL 00346;



M00305 %M00305
 LD Block,'UMA_N': NOCON 00348;
R00635 %R00635
 LD Block,'UMA_N': UPCTR 00348; EQ_INT 00349, 00350;
M00306 %M00306
 LD Block,'UMA_N': NOCON 00348; COIL 00350;



R00635[0] %R00635
 LD Block,'UMA_N': EQ_INT 00349, 00350;
M00266 %M00266
 LD Block,'UMA_N': NOCON 00261, 00351, 00353, 00354; COIL 00349;



R00635[0] %R00635
 LD Block,'UMA_N': EQ_INT 00349, 00350;
M00306 %M00306
 LD Block,'UMA_N': NOCON 00348; COIL 00350;



M00266 %M00266
 LD Block,'UMA_N': NOCON 00261, 00351, 00353, 00354; COIL 00349;
M00310 %M00310
 LD Block,'UMA_N': NCCON 00351; NOCON 00355; COIL 00355;
Q00223 %Q00223
 LD Block,'UMA_N': NCCON 00351; COIL 00352;
Q00222 %Q00222
 LD Block,'UMA_N': NOCON 00267, 00279; COIL 00351;



M00311 %M00311
 LD Block,'UMA_N': NOCON 00352;
Q00223 %Q00223
 LD Block,'UMA_N': NCCON 00351; COIL 00352;



M00266 %M00266
 LD Block,'UMA_N': NOCON 00261, 00351, 00353, 00354; COIL 00349;
R00638 %R00638
 LD Block,'UMA_N': LT_INT 00354; GT_INT 00353;
M00308 %M00308
 LD Block,'UMA_N': NCCON 00355; COIL 00353;
R00639 %R00639
 LD Block,'UMA_N': GT_INT 00353;



M00266 %M00266
 LD Block,'UMA_N': NOCON 00261, 00351, 00353, 00354; COIL 00349;
R00638 %R00638
 LD Block,'UMA_N': LT_INT 00354; GT_INT 00353;
M00309 %M00309
 LD Block,'UMA_N': NOCON 00355; COIL 00354;
R00640 %R00640
 LD Block,'UMA_N': LT_INT 00354;



M00309 %M00309
 LD Block,'UMA_N': NOCON 00355; COIL 00354;
M00308 %M00308
 LD Block,'UMA_N': NCCON 00355; COIL 00353;
M00310 %M00310
 LD Block,'UMA_N': NCCON 00351; NOCON 00355; COIL 00355;



M00312 %M00312
 LD Block,'UMA_N': NOCON 00357;
R00641 %R00641
 LD Block,'UMA_N': UPCTR 00357; EQ_INT 00358, 00359;
M00313 %M00313
 LD Block,'UMA_N': NOCON 00357; COIL 00359;



R00641[0] %R00641
 LD Block,'UMA_N': EQ_INT 00358, 00359;
M00219 %M00219
 LD Block,'UMA_N': NOCON 00261, 00360, 00362, 00363; COIL 00358;



R00641[0] %R00641
 LD Block,'UMA_N': EQ_INT 00358, 00359;
M00313 %M00313
 LD Block,'UMA_N': NOCON 00357; COIL 00359;



M00219 %M00219
 LD Block,'UMA_N': NOCON 00261, 00360, 00362, 00363; COIL 00358;
M00317 %M00317
 LD Block,'UMA_N': NCCON 00360; NOCON 00364; COIL 00364;
Q00225 %Q00225
 LD Block,'UMA_N': NCCON 00360; COIL 00361;
Q00224 %Q00224
 LD Block,'UMA_N': NOCON 00268, 00280; COIL 00360;



M00318 %M00318
 LD Block,'UMA_N': NOCON 00361;
Q00225 %Q00225
 LD Block,'UMA_N': NCCON 00360; COIL 00361;



M00219 %M00219
 LD Block,'UMA_N': NOCON 00261, 00360, 00362, 00363; COIL 00358;
R00644 %R00644
 LD Block,'UMA_N': LT_INT 00363; GT_INT 00362;
M00315 %M00315
 LD Block,'UMA_N': NCCON 00364; COIL 00362;
R00645 %R00645
 LD Block,'UMA_N': GT_INT 00362;



M00219 %M00219
 LD Block,'UMA_N': NOCON 00261, 00360, 00362, 00363; COIL 00358;
R00644 %R00644
 LD Block,'UMA_N': LT_INT 00363; GT_INT 00362;
M00316 %M00316
 LD Block,'UMA_N': NOCON 00364; COIL 00363;
R00646 %R00646
 LD Block,'UMA_N': LT_INT 00363;



M00316 %M00316
 LD Block,'UMA_N': NOCON 00364; COIL 00363;
M00315 %M00315
 LD Block,'UMA_N': NCCON 00364; COIL 00362;
M00317 %M00317
 LD Block,'UMA_N': NCCON 00360; NOCON 00364; COIL 00364;

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M00319 %M00319
LD Block,'UMA_N': NOCON 00366;

R00647 %R00647
LD Block,'UMA_N': UPCTR 00366; EQ_INT 00367, 00368;

M00320 %M00320
LD Block,'UMA_N': NOCON 00366; COIL 00368;



R00647[0] %R00647
LD Block,'UMA_N': EQ_INT 00367, 00368;

M00220 %M00220
LD Block,'UMA_N': NOCON 00261, 00369, 00371, 00372; COIL 00367;



R00647[0] %R00647
LD Block,'UMA_N': EQ_INT 00367, 00368;

M00320 %M00320
LD Block,'UMA_N': NOCON 00366; COIL 00368;



M00220 %M00220
LD Block,'UMA_N': NOCON 00261, 00369, 00371, 00372; COIL 00367;

M00324 %M00324
LD Block,'UMA_N': NCCON 00369; NOCON 00373; COIL 00373;

Q00227 %Q00227
LD Block,'UMA_N': NCCON 00369; COIL 00370;

Q00226 %Q00226
LD Block,'UMA_N': NOCON 00269, 00281; COIL 00369;



M00325 %M00325
LD Block,'UMA_N': NOCON 00370;

Q00227 %Q00227
LD Block,'UMA_N': NCCON 00369; COIL 00370;



M00220 %M00220
LD Block,'UMA_N': NOCON 00261, 00369, 00371, 00372; COIL 00367;

R00650 %R00650
LD Block,'UMA_N': LT_INT 00372; GT_INT 00371;

M00322 %M00322
LD Block,'UMA_N': NCCON 00373; COIL 00371;

R00651 %R00651
LD Block,'UMA_N': GT_INT 00371;



M00220 %M00220
LD Block,'UMA_N': NOCON 00261, 00369, 00371, 00372; COIL 00367;

R00650 %R00650
LD Block,'UMA_N': LT_INT 00372; GT_INT 00371;

M00323 %M00323
LD Block,'UMA_N': NOCON 00373; COIL 00372;

R00652 %R00652
LD Block,'UMA_N': LT_INT 00372;



M00323 %M00323
LD Block,'UMA_N': NOCON 00373; COIL 00372;
M00322 %M00322
LD Block,'UMA_N': NCCON 00373; COIL 00371;
M00324 %M00324
LD Block,'UMA_N': NCCON 00369; NOCON 00373; COIL 00373;



M00326 %M00326
LD Block,'UMA_N': NOCON 00375;
R00653 %R00653
LD Block,'UMA_N': UPCTR 00375; EQ_INT 00376, 00377;
M00327 %M00327
LD Block,'UMA_N': NOCON 00375; COIL 00377;



R00653[0] %R00653
LD Block,'UMA_N': EQ_INT 00376, 00377;
M00271 %M00271
LD Block,'UMA_N': NOCON 00261, 00378, 00380, 00381; COIL 00376;



R00653[0] %R00653
LD Block,'UMA_N': EQ_INT 00376, 00377;
M00327 %M00327
LD Block,'UMA_N': NOCON 00375; COIL 00377;



M00271 %M00271
LD Block,'UMA_N': NOCON 00261, 00378, 00380, 00381; COIL 00376;
M00331 %M00331
LD Block,'UMA_N': NCCON 00378; NOCON 00382; COIL 00382;
Q00229 %Q00229
LD Block,'UMA_N': NCCON 00378; COIL 00379;
Q00228 %Q00228
LD Block,'UMA_N': NOCON 00270, 00282; COIL 00378;



M00332 %M00332
LD Block,'UMA_N': NOCON 00379;
Q00229 %Q00229
LD Block,'UMA_N': NCCON 00378; COIL 00379;



M00271 %M00271
LD Block,'UMA_N': NOCON 00261, 00378, 00380, 00381; COIL 00376;
R00656 %R00656
LD Block,'UMA_N': LT_INT 00381; GT_INT 00380;
M00329 %M00329
LD Block,'UMA_N': NCCON 00382; COIL 00380;
R00657 %R00657
LD Block,'UMA_N': GT_INT 00380;



M00271 %M00271
LD Block,'UMA_N': NOCON 00261, 00378, 00380, 00381; COIL 00376;

R00656 %R00656
LD Block,'UMA_N': LT_INT 00381; GT_INT 00380;

M00330 %M00330
LD Block,'UMA_N': NOCON 00382; COIL 00381;

R00658 %R00658
LD Block,'UMA_N': LT_INT 00381;



M00330 %M00330
LD Block,'UMA_N': NOCON 00382; COIL 00381;

M00329 %M00329
LD Block,'UMA_N': NCCON 00382; COIL 00380;

M00331 %M00331
LD Block,'UMA_N': NCCON 00378; NOCON 00382; COIL 00382;



M00341 %M00341
LD Block,'UMA_N': NOCON 00384, 00425, 00427, 00428; COIL 00423;

M00333 %M00333
LD Block,'_MAIN': NOCON 00012, 00051, 00064, 00073;
LD Block,'UMA_N': NOCON 00406, 00407, 00408, 00411, 00417, 00418, 00419, 00420; COIL 00384;

M00342 %M00342
LD Block,'UMA_N': NOCON 00384, 00434, 00436, 00437; COIL 00432;

M00343 %M00343
LD Block,'UMA_N': NOCON 00384, 00443, 00445, 00446; COIL 00441;

M00344 %M00344
LD Block,'UMA_N': NOCON 00384, 00452, 00454, 00455; COIL 00450;

M00345 %M00345
LD Block,'UMA_N': NOCON 00384, 00461, 00463, 00464; COIL 00459;

M00346 %M00346
LD Block,'UMA_N': NOCON 00384, 00470, 00472, 00473; COIL 00468;

M00351 %M00351
LD Block,'UMA_N': NOCON 00384, 00479, 00480, 00483, 00484; COIL 00477;

M00358 %M00358
LD Block,'UMA_N': NOCON 00384, 00490, 00491, 00492, 00496, 00497; COIL 00488;
LD Block,'genee': NOCON 00013;



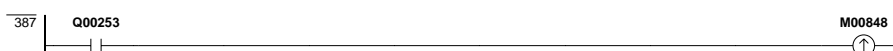
Q00249 %Q00249
LD Block,'UMA_N': NOCON 00385, 00396; COIL 00425;

M00846 %M00846
LD Block,'UMA_N': POSCOIL 00385; NOCON 00393;



Q00251 %Q00251
LD Block,'UMA_N': NOCON 00386, 00397; COIL 00434;

M00847 %M00847
LD Block,'UMA_N': POSCOIL 00386; NOCON 00393;



Q00253 %Q00253
LD Block,'UMA_N': NOCON 00387, 00398; COIL 00443;

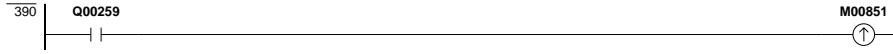
M00848 %M00848
LD Block,'UMA_N': POSCOIL 00387; NOCON 00393;



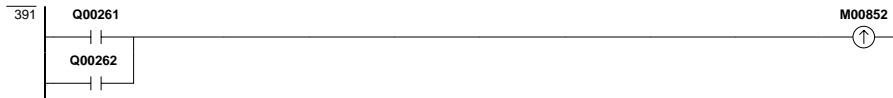
Q00255 %Q00255
 LD Block,'UMA_N': NOCON 00388, 00399; COIL 00452;
M00849 %M00849
 LD Block,'UMA_N': POSCOIL 00388; NOCON 00393;



Q00257 %Q00257
 LD Block,'UMA_N': NOCON 00389, 00400; COIL 00461;
M00850 %M00850
 LD Block,'UMA_N': POSCOIL 00389; NOCON 00393;



Q00259 %Q00259
 LD Block,'UMA_N': NOCON 00390, 00401; COIL 00470;
M00851 %M00851
 LD Block,'UMA_N': POSCOIL 00390; NOCON 00393;



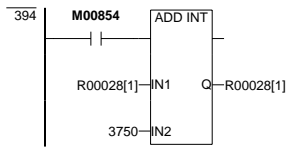
Q00261 %Q00261
 LD Block,'UMA_N': NOCON 00391, 00402; COIL 00479;
M00852 %M00852
 LD Block,'UMA_N': POSCOIL 00391; NOCON 00393;
Q00262 %Q00262
 LD Block,'UMA_N': NOCON 00391, 00402; COIL 00480;



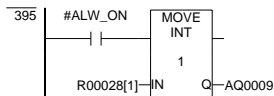
Q00265 %Q00265
 LD Block,'UMA_N': NOCON 00392, 00403; COIL 00490;
M00853 %M00853
 LD Block,'UMA_N': POSCOIL 00392; NOCON 00393;
Q00266 %Q00266
 LD Block,'UMA_N': NOCON 00392, 00403; COIL 00491;
Q00267 %Q00267
 LD Block,'UMA_N': NOCON 00392, 00403; COIL 00492;



M00846 %M00846
 LD Block,'UMA_N': POSCOIL 00385; NOCON 00393;
M00854 %M00854
 LD Block,'UMA_N': NOCON 00394; COIL 00393;
M00847 %M00847
 LD Block,'UMA_N': POSCOIL 00386; NOCON 00393;
M00848 %M00848
 LD Block,'UMA_N': POSCOIL 00387; NOCON 00393;
M00849 %M00849
 LD Block,'UMA_N': POSCOIL 00388; NOCON 00393;
M00850 %M00850
 LD Block,'UMA_N': POSCOIL 00389; NOCON 00393;
M00851 %M00851
 LD Block,'UMA_N': POSCOIL 00390; NOCON 00393;
M00852 %M00852
 LD Block,'UMA_N': POSCOIL 00391; NOCON 00393;
M00853 %M00853
 LD Block,'UMA_N': POSCOIL 00392; NOCON 00393;



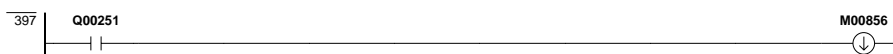
M00854 %M00854
 LD Block,'UMA_N': NOCON 00394; COIL 00393;
R00028[1] %R00029
 LD Block,'UMA_N': SUB_INT 00405, 00405; ADD_INT 00394, 00394; MOVE_INT 00395;



#ALW_ON %S00007
 LD Block,'Reloj': NOCON 00001, 00002, 00003, 00004, 00005, 00006, 00007, 00008;
 LD Block,'flujos_1': NOCON 00003, 00006;
 LD Block,'UMA_N': NOCON 00011, 00102, 00198, 00273, 00395, 00510, 00616;
 LD Block,'N_T_ch': NOCON 00001, 00002, 00003, 00004, 00005, 00006, 00007;
 LD Block,'N_cist': NOCON 00002, 00003, 00004, 00005, 00006, 00007, 00008, 00010, 00011, 00012, 00013, 00014, 00015, 00016;
 LD Block,'cister': NOCON 00020;
 LD Block,'genee': NOCON 00011;
 LD Block,'incend': NOCON 00026, 00032, 00033, 00034, 00036, 00039, 00040, 00041, 00042, 00043, 00044, 00045;
 LD Block,'agua': NOCON 00001, 00002, 00003, 00006, 00007, 00008, 00009, 00010, 00011, 00012, 00020, 00021, 00022, 00031, 00032, 00033;
 LD Block,'_MAIN': NOCON 00001, 00002, 00003, 00004, 00005, 00006, 00007, 00008, 00009, 00010, 00011, 00026, 00030, 00033, 00034, 00035, 00049, 00067, 00068, 00078, 00095, 00096, 00121, 00126, 00128, 00129;
R00028[1] %R00029
 LD Block,'UMA_N': SUB_INT 00405, 00405; ADD_INT 00394, 00394; MOVE_INT 00395;
AQ0009 %AQ0009
 LD Block,'UMA_N': MOVE_INT 00395;



Q00249 %Q00249
 LD Block,'UMA_N': NOCON 00385, 00396; COIL 00425;
M00855 %M00855
 LD Block,'UMA_N': NEGCOIL 00396; NOCON 00404;



Q00251 %Q00251
 LD Block,'UMA_N': NOCON 00386, 00397; COIL 00434;
M00856 %M00856
 LD Block,'UMA_N': NEGCOIL 00397; NOCON 00404;



Q00253 %Q00253
 LD Block,'UMA_N': NOCON 00387, 00398; COIL 00443;
M00857 %M00857
 LD Block,'UMA_N': NEGCOIL 00398; NOCON 00404;



Q00255 %Q00255
 LD Block,'UMA_N': NOCON 00388, 00399; COIL 00452;
M00858 %M00858
 LD Block,'UMA_N': NEGCOIL 00399; NOCON 00404;



Q00257 %Q00257
 LD Block,'UMA_N': NOCON 00389, 00400; COIL 00461;
M00859 %M00859
 LD Block,'UMA_N': NEGCOIL 00400; NOCON 00404;



Q00259 %Q00259
 LD Block,'UMA_N': NOCON 00390, 00401; COIL 00470;
M00860 %M00860
 LD Block,'UMA_N': NEGCOIL 00401; NOCON 00404;



Q00261 %Q00261
 LD Block,'UMA_N': NOCON 00391, 00402; COIL 00479;
M00861 %M00861
 LD Block,'UMA_N': NEGCOIL 00402; NOCON 00404;

Q00262 %Q00262
 LD Block,'UMA_N': NOCON 00391, 00402; COIL 00480;



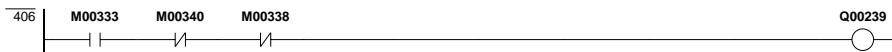
Q00265 %Q00265
 LD Block,'UMA_N': NOCON 00392, 00403; COIL 00490;
M00862 %M00862
 LD Block,'UMA_N': NEGCOIL 00403; NOCON 00404;
Q00266 %Q00266
 LD Block,'UMA_N': NOCON 00392, 00403; COIL 00491;
Q00267 %Q00267
 LD Block,'UMA_N': NOCON 00392, 00403; COIL 00492;



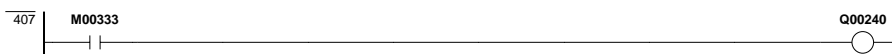
M00855 %M00855
 LD Block,'UMA_N': NEGCOIL 00396; NOCON 00404;
M00863 %M00863
 LD Block,'UMA_N': NOCON 00405; COIL 00404;
M00856 %M00856
 LD Block,'UMA_N': NEGCOIL 00397; NOCON 00404;
M00857 %M00857
 LD Block,'UMA_N': NEGCOIL 00398; NOCON 00404;
M00858 %M00858
 LD Block,'UMA_N': NEGCOIL 00399; NOCON 00404;
M00859 %M00859
 LD Block,'UMA_N': NEGCOIL 00400; NOCON 00404;
M00860 %M00860
 LD Block,'UMA_N': NEGCOIL 00401; NOCON 00404;
M00861 %M00861
 LD Block,'UMA_N': NEGCOIL 00402; NOCON 00404;
M00862 %M00862
 LD Block,'UMA_N': NEGCOIL 00403; NOCON 00404;



M00863 %M00863
 LD Block,'UMA_N': NOCON 00405; COIL 00404;
R00028[1] %R00029
 LD Block,'UMA_N': SUB_INT 00405, 00405; ADD_INT 00394, 00394; MOVE_INT 00395;



M00333 %M00333
 LD Block,'_MAIN': NOCON 00012, 00051, 00064, 00073;
 LD Block,'UMA_N': NOCON 00406, 00407, 00408, 00411, 00417, 00418, 00419, 00420; COIL 00384;
M00340 %M00340
 LD Block,'UMA_N': NCCON 00406;
M00338 %M00338
 LD Block,'UMA_N': NCCON 00406, 00409;
Q00239 %Q00239
 LD Block,'UMA_N': COIL 00406;



M00333 %M00333
 LD Block,'_MAIN': NOCON 00012, 00051, 00064, 00073;
 LD Block,'UMA_N': NOCON 00406, 00407, 00408, 00411, 00417, 00418, 00419, 00420; COIL 00384;
Q00240 %Q00240
 LD Block,'UMA_N': COIL 00407;



M00333 %M00333

LD Block,'_MAIN': NOCON 00012, 00051, 00064, 00073;
 LD Block,'UMA_N': NOCON 00406, 00407, 00408, 00411, 00417, 00418, 00419, 00420; COIL 00384;
M00335 %M00335
 LD Block,'UMA_N': NCCON 00408; NOCON 00411; COIL 00410;
M00336 %M00336
 LD Block,'UMA_N': NCCON 00408, 00409; NOCON 00413;
M00347 %M00347
 LD Block,'UMA_N': NOCON 00415; COIL 00408;



Q00241 %Q00241
 LD Block,'UMA_N': NOCON 00409; COIL 00415;
M00338 %M00338
 LD Block,'UMA_N': NCCON 00406, 00409;
M00336 %M00336
 LD Block,'UMA_N': NCCON 00408, 00409; NOCON 00413;
M00334 %M00334
 LD Block,'UMA_N': NOCON 00409, 00410; COIL 00409;



M00334 %M00334
 LD Block,'UMA_N': NOCON 00409, 00410; COIL 00409;
R00659 %R00659
 LD Block,'UMA_N': ONDTR_TENTHS 00410;
M00335 %M00335
 LD Block,'UMA_N': NCCON 00408; NOCON 00411; COIL 00410;
M00339 %M00339
 LD Block,'UMA_N': NOCON 00410, 00412; COIL 00412;



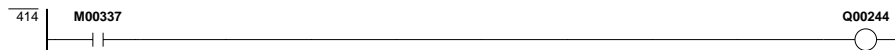
M00335 %M00335
 LD Block,'UMA_N': NCCON 00408; NOCON 00411; COIL 00410;
M00333 %M00333
 LD Block,'_MAIN': NOCON 00012, 00051, 00064, 00073;
 LD Block,'UMA_N': NOCON 00406, 00407, 00408, 00411, 00417, 00418, 00419, 00420; COIL 00384;
M00337 %M00337
 LD Block,'UMA_N': NCCON 00411; NOCON 00414;
M00348 %M00348
 LD Block,'UMA_N': NOCON 00416; COIL 00411;



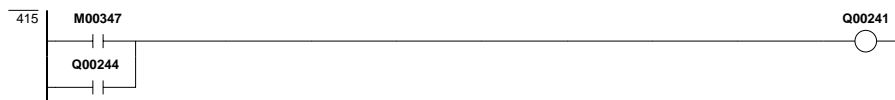
Q00242 %Q00242
 LD Block,'UMA_N': NOCON 00412; COIL 00416;
R00662 %R00662
 LD Block,'UMA_N': ONDTR_TENTHS 00412;
M00339 %M00339
 LD Block,'UMA_N': NOCON 00410, 00412; COIL 00412;



M00336 %M00336
 LD Block,'UMA_N': NCCON 00408, 00409; NOCON 00413;
Q00243 %Q00243
 LD Block,'UMA_N': NOCON 00416; COIL 00413;



M00337 %M00337
 LD Block,'UMA_N': NCCON 00411; NOCON 00414;
Q00244 %Q00244
 LD Block,'UMA_N': NOCON 00415; COIL 00414;



M00347 %M00347
 LD Block,'UMA_N': NOCON 00415; COIL 00408;
Q00241 %Q00241
 LD Block,'UMA_N': NOCON 00409; COIL 00415;
Q00244 %Q00244
 LD Block,'UMA_N': NOCON 00415; COIL 00414;



M00348 %M00348
 LD Block,'UMA_N': NOCON 00416; COIL 00411;
Q00242 %Q00242
 LD Block,'UMA_N': NOCON 00412; COIL 00416;
Q00243 %Q00243
 LD Block,'UMA_N': NOCON 00416; COIL 00413;



M00333 %M00333
 LD Block,'_MAIN': NOCON 00012, 00051, 00064, 00073;
 LD Block,'UMA_N': NOCON 00406, 00407, 00408, 00411, 00417, 00418, 00419, 00420; COIL 00384;
R00666 %R00666
 LD Block,'UMA_N': GT_INT 00417;
Q00245 %Q00245
 LD Block,'UMA_N': COIL 00417;



M00333 %M00333
 LD Block,'_MAIN': NOCON 00012, 00051, 00064, 00073;
 LD Block,'UMA_N': NOCON 00406, 00407, 00408, 00411, 00417, 00418, 00419, 00420; COIL 00384;
R00667 %R00667
 LD Block,'UMA_N': GT_INT 00418;
Q00246 %Q00246
 LD Block,'UMA_N': COIL 00418;



M00333 %M00333
 LD Block,'_MAIN': NOCON 00012, 00051, 00064, 00073;
 LD Block,'UMA_N': NOCON 00406, 00407, 00408, 00411, 00417, 00418, 00419, 00420; COIL 00384;
Q00248 %Q00248
 LD Block,'UMA_N': COIL 00419;
R00668 %R00668
 LD Block,'UMA_N': GE_INT 00419;



M00333 %M00333
 LD Block,'_MAIN': NOCON 00012, 00051, 00064, 00073;
 LD Block,'UMA_N': NOCON 00406, 00407, 00408, 00411, 00417, 00418, 00419, 00420; COIL 00384;
R00669 %R00669
 LD Block,'UMA_N': GT_INT 00420;
Q00247 %Q00247
 LD Block,'UMA_N': COIL 00420;



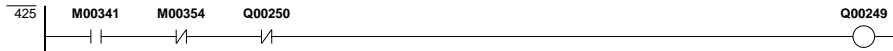
M00349 %M00349
 LD Block,'UMA_N': NOCON 00422;
R00670 %R00670
 LD Block,'UMA_N': UPCTR 00422; EQ_INT 00423, 00424;
M00350 %M00350
 LD Block,'UMA_N': NOCON 00422; COIL 00424;



R00670[0] %R00670
 LD Block,'UMA_N': EQ_INT 00423, 00424;
M00341 %M00341
 LD Block,'UMA_N': NOCON 00384, 00425, 00427, 00428; COIL 00423;



R00670[0] %R00670
 LD Block,'UMA_N': EQ_INT 00423, 00424;
M00350 %M00350
 LD Block,'UMA_N': NOCON 00422; COIL 00424;



M00341 %M00341
 LD Block,'UMA_N': NOCON 00384, 00425, 00427, 00428; COIL 00423;
M00354 %M00354
 LD Block,'UMA_N': NCCON 00425; NOCON 00429; COIL 00429;
Q00250 %Q00250
 LD Block,'UMA_N': NCCON 00425; COIL 00426;
Q00249 %Q00249
 LD Block,'UMA_N': NOCON 00385, 00396; COIL 00425;



M00355 %M00355
 LD Block,'UMA_N': NOCON 00426;
Q00250 %Q00250
 LD Block,'UMA_N': NCCON 00425; COIL 00426;



M00341 %M00341
 LD Block,'UMA_N': NOCON 00384, 00425, 00427, 00428; COIL 00423;
R00673 %R00673
 LD Block,'UMA_N': LT_INT 00428; GT_INT 00427;
M00352 %M00352
 LD Block,'UMA_N': NCCON 00429; COIL 00427;
R00674 %R00674
 LD Block,'UMA_N': GT_INT 00427;



M00341 %M00341
 LD Block,'UMA_N': NOCON 00384, 00425, 00427, 00428; COIL 00423;
R00673 %R00673
 LD Block,'UMA_N': LT_INT 00428; GT_INT 00427;
M00353 %M00353
 LD Block,'UMA_N': NOCON 00429; COIL 00428;
R00675 %R00675
 LD Block,'UMA_N': LT_INT 00428;



M00353 %M00353
 LD Block,'UMA_N': NOCON 00429; COIL 00428;
M00352 %M00352
 LD Block,'UMA_N': NCCON 00429; COIL 00427;
M00354 %M00354
 LD Block,'UMA_N': NCCON 00425; NOCON 00429; COIL 00429;

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M00356 %M00356
LD Block,'UMA_N': NOCON 00431;
R00676 %R00676
LD Block,'UMA_N': UPCTR 00431; EQ_INT 00432, 00433;
M00357 %M00357
LD Block,'UMA_N': NOCON 00431; COIL 00433;



R00676[0] %R00676
LD Block,'UMA_N': EQ_INT 00432, 00433;
M00342 %M00342
LD Block,'UMA_N': NOCON 00384, 00434, 00436, 00437; COIL 00432;



R00676[0] %R00676
LD Block,'UMA_N': EQ_INT 00432, 00433;
M00357 %M00357
LD Block,'UMA_N': NOCON 00431; COIL 00433;



M00342 %M00342
LD Block,'UMA_N': NOCON 00384, 00434, 00436, 00437; COIL 00432;
M00361 %M00361
LD Block,'UMA_N': NCCON 00434; NOCON 00438; COIL 00438;
Q00252 %Q00252
LD Block,'UMA_N': NCCON 00434; COIL 00435;
Q00251 %Q00251
LD Block,'UMA_N': NOCON 00386, 00397; COIL 00434;



M00362 %M00362
LD Block,'UMA_N': NOCON 00435;
Q00252 %Q00252
LD Block,'UMA_N': NCCON 00434; COIL 00435;



M00342 %M00342
LD Block,'UMA_N': NOCON 00384, 00434, 00436, 00437; COIL 00432;
R00679 %R00679
LD Block,'UMA_N': LT_INT 00437; GT_INT 00436;
M00359 %M00359
LD Block,'UMA_N': NCCON 00438; COIL 00436;
R00680 %R00680
LD Block,'UMA_N': GT_INT 00436;



M00342 %M00342
LD Block,'UMA_N': NOCON 00384, 00434, 00436, 00437; COIL 00432;
R00679 %R00679
LD Block,'UMA_N': LT_INT 00437; GT_INT 00436;
M00360 %M00360
LD Block,'UMA_N': NOCON 00438; COIL 00437;
R00681 %R00681
LD Block,'UMA_N': LT_INT 00437;



M00360 %M00360
LD Block,'UMA_N': NOCON 00438; COIL 00437;

M00359 %M00359
LD Block,'UMA_N': NCCON 00438; COIL 00436;

M00361 %M00361
LD Block,'UMA_N': NCCON 00434; NOCON 00438; COIL 00438;



M00363 %M00363
LD Block,'UMA_N': NOCON 00440;

R00682 %R00682
LD Block,'UMA_N': UPCTR 00440; EQ_INT 00441, 00442;

M00364 %M00364
LD Block,'UMA_N': NOCON 00440; COIL 00442;



R00682[0] %R00682
LD Block,'UMA_N': EQ_INT 00441, 00442;

M00343 %M00343
LD Block,'UMA_N': NOCON 00384, 00443, 00445, 00446; COIL 00441;



R00682[0] %R00682
LD Block,'UMA_N': EQ_INT 00441, 00442;

M00364 %M00364
LD Block,'UMA_N': NOCON 00440; COIL 00442;



M00343 %M00343
LD Block,'UMA_N': NOCON 00384, 00443, 00445, 00446; COIL 00441;

M00368 %M00368
LD Block,'UMA_N': NCCON 00443; NOCON 00447; COIL 00447;

Q00254 %Q00254
LD Block,'UMA_N': NCCON 00443; COIL 00444;

Q00253 %Q00253
LD Block,'UMA_N': NOCON 00387, 00398; COIL 00443;



M00369 %M00369
LD Block,'UMA_N': NOCON 00444;

Q00254 %Q00254
LD Block,'UMA_N': NCCON 00443; COIL 00444;



M00343 %M00343
LD Block,'UMA_N': NOCON 00384, 00443, 00445, 00446; COIL 00441;

R00685 %R00685
LD Block,'UMA_N': LT_INT 00446; GT_INT 00445;

M00366 %M00366
LD Block,'UMA_N': NCCON 00447; COIL 00445;

R00686 %R00686
LD Block,'UMA_N': GT_INT 00445;



M00343 %M00343
LD Block,'UMA_N': NOCON 00384, 00443, 00445, 00446; COIL 00441;
R00685 %R00685
LD Block,'UMA_N': LT_INT 00446; GT_INT 00445;
M00367 %M00367
LD Block,'UMA_N': NOCON 00447; COIL 00446;
R00687 %R00687
LD Block,'UMA_N': LT_INT 00446;



M00367 %M00367
LD Block,'UMA_N': NOCON 00447; COIL 00446;
M00366 %M00366
LD Block,'UMA_N': NCCON 00447; COIL 00445;
M00368 %M00368
LD Block,'UMA_N': NCCON 00443; NOCON 00447; COIL 00447;



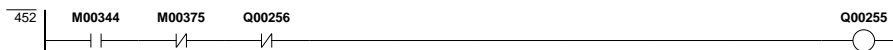
M00370 %M00370
LD Block,'UMA_N': NOCON 00449;
R00688 %R00688
LD Block,'UMA_N': UPCTR 00449; EQ_INT 00450, 00451;
M00371 %M00371
LD Block,'UMA_N': NOCON 00449; COIL 00451;



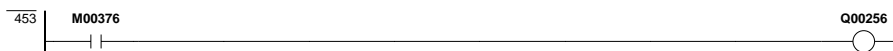
R00688[0] %R00688
LD Block,'UMA_N': EQ_INT 00450, 00451;
M00344 %M00344
LD Block,'UMA_N': NOCON 00384, 00452, 00454, 00455; COIL 00450;



R00688[0] %R00688
LD Block,'UMA_N': EQ_INT 00450, 00451;
M00371 %M00371
LD Block,'UMA_N': NOCON 00449; COIL 00451;



M00344 %M00344
LD Block,'UMA_N': NOCON 00384, 00452, 00454, 00455; COIL 00450;
M00375 %M00375
LD Block,'UMA_N': NCCON 00452; NOCON 00456; COIL 00456;
Q00256 %Q00256
LD Block,'UMA_N': NCCON 00452; COIL 00453;
Q00255 %Q00255
LD Block,'UMA_N': NOCON 00388, 00399; COIL 00452;



M00376 %M00376
LD Block,'UMA_N': NOCON 00453;
Q00256 %Q00256
LD Block,'UMA_N': NCCON 00452; COIL 00453;



M00344 %M00344
LD Block,'UMA_N': NOCON 00384, 00452, 00454, 00455; COIL 00450;
R00691 %R00691
LD Block,'UMA_N': LT_INT 00455; GT_INT 00454;
M00373 %M00373
LD Block,'UMA_N': NCCON 00456; COIL 00454;
R00692 %R00692
LD Block,'UMA_N': GT_INT 00454;



M00344 %M00344
LD Block,'UMA_N': NOCON 00384, 00452, 00454, 00455; COIL 00450;
R00691 %R00691
LD Block,'UMA_N': LT_INT 00455; GT_INT 00454;
M00374 %M00374
LD Block,'UMA_N': NOCON 00456; COIL 00455;
R00693 %R00693
LD Block,'UMA_N': LT_INT 00455;



M00374 %M00374
LD Block,'UMA_N': NOCON 00456; COIL 00455;
M00373 %M00373
LD Block,'UMA_N': NCCON 00456; COIL 00454;
M00375 %M00375
LD Block,'UMA_N': NCCON 00452; NOCON 00456; COIL 00456;



M00377 %M00377
LD Block,'UMA_N': NOCON 00458;
R00694 %R00694
LD Block,'UMA_N': UPCTR 00458; EQ_INT 00459, 00460;
M00378 %M00378
LD Block,'UMA_N': NOCON 00458; COIL 00460;



R00694[0] %R00694
LD Block,'UMA_N': EQ_INT 00459, 00460;
M00345 %M00345
LD Block,'UMA_N': NOCON 00384, 00461, 00463, 00464; COIL 00459;



R00694[0] %R00694
LD Block,'UMA_N': EQ_INT 00459, 00460;
M00378 %M00378
LD Block,'UMA_N': NOCON 00458; COIL 00460;



M00345 %M00345
LD Block,'UMA_N': NOCON 00384, 00461, 00463, 00464; COIL 00459;
M00382 %M00382
LD Block,'UMA_N': NCCON 00461; NOCON 00465; COIL 00465;
Q00258 %Q00258

LD Block,'UMA_N': NCCON 00461; COIL 00462;
Q00257 %Q00257
 LD Block,'UMA_N': NOCON 00389, 00400; COIL 00461;



M00383 %M00383
 LD Block,'UMA_N': NOCON 00462;
Q00258 %Q00258
 LD Block,'UMA_N': NCCON 00461; COIL 00462;



M00345 %M00345
 LD Block,'UMA_N': NOCON 00384, 00461, 00463, 00464; COIL 00459;
R00697 %R00697
 LD Block,'UMA_N': LT_INT 00464; GT_INT 00463;
M00380 %M00380
 LD Block,'UMA_N': NCCON 00465; COIL 00463;
R00698 %R00698
 LD Block,'UMA_N': GT_INT 00463;



M00345 %M00345
 LD Block,'UMA_N': NOCON 00384, 00461, 00463, 00464; COIL 00459;
R00697 %R00697
 LD Block,'UMA_N': LT_INT 00464; GT_INT 00463;
M00381 %M00381
 LD Block,'UMA_N': NOCON 00465; COIL 00464;
R00699 %R00699
 LD Block,'UMA_N': LT_INT 00464;



M00381 %M00381
 LD Block,'UMA_N': NOCON 00465; COIL 00464;
M00380 %M00380
 LD Block,'UMA_N': NCCON 00465; COIL 00463;
M00382 %M00382
 LD Block,'UMA_N': NCCON 00461; NOCON 00465; COIL 00465;



M00384 %M00384
 LD Block,'UMA_N': NOCON 00467;
R00700 %R00700
 LD Block,'UMA_N': UPCTR 00467; EQ_INT 00468, 00469;
M00385 %M00385
 LD Block,'UMA_N': NOCON 00467; COIL 00469;



R00700[0] %R00700
 LD Block,'UMA_N': EQ_INT 00468, 00469;
M00346 %M00346
 LD Block,'UMA_N': NOCON 00384, 00470, 00472, 00473; COIL 00468;



R00700[0] %R00700

LD Block,'UMA_N': EQ_INT 00468, 00469;
M00385 %M00385
 LD Block,'UMA_N': NOCON 00467; COIL 00469;



M00346 %M00346
 LD Block,'UMA_N': NOCON 00384, 00470, 00472, 00473; COIL 00468;
M00389 %M00389
 LD Block,'UMA_N': NCCON 00470; NOCON 00474; COIL 00474;
Q00260 %Q00260
 LD Block,'UMA_N': NCCON 00470; COIL 00471;
Q00259 %Q00259
 LD Block,'UMA_N': NOCON 00390, 00401; COIL 00470;



M00390 %M00390
 LD Block,'UMA_N': NOCON 00471;
Q00260 %Q00260
 LD Block,'UMA_N': NCCON 00470; COIL 00471;



M00346 %M00346
 LD Block,'UMA_N': NOCON 00384, 00470, 00472, 00473; COIL 00468;
R00703 %R00703
 LD Block,'UMA_N': LT_INT 00473; GT_INT 00472;
M00387 %M00387
 LD Block,'UMA_N': NCCON 00474; COIL 00472;
R00704 %R00704
 LD Block,'UMA_N': GT_INT 00472;



M00346 %M00346
 LD Block,'UMA_N': NOCON 00384, 00470, 00472, 00473; COIL 00468;
R00703 %R00703
 LD Block,'UMA_N': LT_INT 00473; GT_INT 00472;
M00388 %M00388
 LD Block,'UMA_N': NOCON 00474; COIL 00473;
R00705 %R00705
 LD Block,'UMA_N': LT_INT 00473;



M00388 %M00388
 LD Block,'UMA_N': NOCON 00474; COIL 00473;
M00387 %M00387
 LD Block,'UMA_N': NCCON 00474; COIL 00472;
M00389 %M00389
 LD Block,'UMA_N': NCCON 00470; NOCON 00474; COIL 00474;



M00391 %M00391
 LD Block,'UMA_N': NOCON 00476;
R00706 %R00706
 LD Block,'UMA_N': UPCTR 00476; EQ_INT 00477, 00478;
M00392 %M00392
 LD Block,'UMA_N': NOCON 00476; COIL 00478;



R00706[0] %R00706

LD Block,'UMA_N': EQ_INT 00477, 00478;
M00351 %M00351
 LD Block,'UMA_N': NOCON 00384, 00479, 00480, 00483, 00484; COIL 00477;



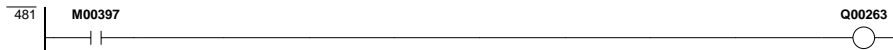
R00706[0] %R00706
 LD Block,'UMA_N': EQ_INT 00477, 00478;
M00392 %M00392
 LD Block,'UMA_N': NOCON 00476; COIL 00478;



M00351 %M00351
 LD Block,'UMA_N': NOCON 00384, 00479, 00480, 00483, 00484; COIL 00477;
M00396 %M00396
 LD Block,'UMA_N': NCCON 00479, 00480; NOCON 00485; COIL 00485;
Q00263 %Q00263
 LD Block,'UMA_N': NCCON 00479; COIL 00481;
Q00261 %Q00261
 LD Block,'UMA_N': NOCON 00391, 00402; COIL 00479;



M00351 %M00351
 LD Block,'UMA_N': NOCON 00384, 00479, 00480, 00483, 00484; COIL 00477;
M00396 %M00396
 LD Block,'UMA_N': NCCON 00479, 00480; NOCON 00485; COIL 00485;
Q00264 %Q00264
 LD Block,'UMA_N': NCCON 00480; COIL 00482;
Q00262 %Q00262
 LD Block,'UMA_N': NOCON 00391, 00402; COIL 00480;



M00397 %M00397
 LD Block,'UMA_N': NOCON 00481;
Q00263 %Q00263
 LD Block,'UMA_N': NCCON 00479; COIL 00481;



M00398 %M00398
 LD Block,'UMA_N': NOCON 00482;
Q00264 %Q00264
 LD Block,'UMA_N': NCCON 00480; COIL 00482;



M00351 %M00351
 LD Block,'UMA_N': NOCON 00384, 00479, 00480, 00483, 00484; COIL 00477;
R00709 %R00709
 LD Block,'UMA_N': LT_INT 00484; GT_INT 00483;
M00394 %M00394
 LD Block,'UMA_N': NCCON 00485; COIL 00483;
R00710 %R00710
 LD Block,'UMA_N': GT_INT 00483;



M00351 %M00351
 LD Block,'UMA_N': NOCON 00384, 00479, 00480, 00483, 00484; COIL 00477;
R00709 %R00709
 LD Block,'UMA_N': LT_INT 00484; GT_INT 00483;
M00395 %M00395
 LD Block,'UMA_N': NOCON 00485; COIL 00484;
R00711 %R00711
 LD Block,'UMA_N': LT_INT 00484;



M00395 %M00395
LD Block,'UMA_N': NOCON 00485; COIL 00484;

M00394 %M00394
LD Block,'UMA_N': NCCON 00485; COIL 00483;

M00396 %M00396
LD Block,'UMA_N': NCCON 00479, 00480; NOCON 00485; COIL 00485;



M00399 %M00399
LD Block,'UMA_N': NOCON 00487;

R00712 %R00712
LD Block,'UMA_N': UPCTR 00487; EQ_INT 00488, 00489;

M00400 %M00400
LD Block,'UMA_N': NOCON 00487; COIL 00489;



R00712[0] %R00712
LD Block,'UMA_N': EQ_INT 00488, 00489;

M00358 %M00358
LD Block,'UMA_N': NOCON 00384, 00490, 00491, 00492, 00496, 00497; COIL 00488;
LD Block,'genee': NOCON 00013;



R00712[0] %R00712
LD Block,'UMA_N': EQ_INT 00488, 00489;

M00400 %M00400
LD Block,'UMA_N': NOCON 00487; COIL 00489;



M00358 %M00358
LD Block,'UMA_N': NOCON 00384, 00490, 00491, 00492, 00496, 00497; COIL 00488;
LD Block,'genee': NOCON 00013;

M00404 %M00404
LD Block,'UMA_N': NCCON 00490, 00491, 00492; NOCON 00498; COIL 00498;

Q00268 %Q00268
LD Block,'UMA_N': NCCON 00490; COIL 00493;

Q00265 %Q00265
LD Block,'UMA_N': NOCON 00392, 00403; COIL 00490;

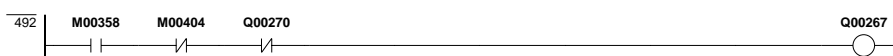


M00358 %M00358
LD Block,'UMA_N': NOCON 00384, 00490, 00491, 00492, 00496, 00497; COIL 00488;
LD Block,'genee': NOCON 00013;

M00404 %M00404
LD Block,'UMA_N': NCCON 00490, 00491, 00492; NOCON 00498; COIL 00498;

Q00269 %Q00269
LD Block,'UMA_N': NCCON 00491; COIL 00494;

Q00266 %Q00266
LD Block,'UMA_N': NOCON 00392, 00403; COIL 00491;



M00358 %M00358
LD Block,'UMA_N': NOCON 00384, 00490, 00491, 00492, 00496, 00497; COIL 00488;
LD Block,'genee': NOCON 00013;

M00404 %M00404
LD Block,'UMA_N': NCCON 00490, 00491, 00492; NOCON 00498; COIL 00498;

Q00270 %Q00270
LD Block,'UMA_N': NCCON 00492; COIL 00495;

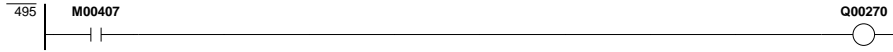
Q00267 %Q00267
LD Block,'UMA_N': NOCON 00392, 00403; COIL 00492;



M00405 %M00405
 LD Block,'UMA_N': NOCON 00493;
Q00268 %Q00268
 LD Block,'UMA_N': NCCON 00490; COIL 00493;



M00406 %M00406
 LD Block,'UMA_N': NOCON 00494;
Q00269 %Q00269
 LD Block,'UMA_N': NCCON 00491; COIL 00494;



M00407 %M00407
 LD Block,'UMA_N': NOCON 00495;
Q00270 %Q00270
 LD Block,'UMA_N': NCCON 00492; COIL 00495;



M00358 %M00358
 LD Block,'UMA_N': NOCON 00384, 00490, 00491, 00492, 00496, 00497; COIL 00488;
 LD Block,'genee': NOCON 00013;
R00715 %R00715
 LD Block,'UMA_N': LT_INT 00497; GT_INT 00496;
M00402 %M00402
 LD Block,'UMA_N': NCCON 00498; COIL 00496;
R00716 %R00716
 LD Block,'UMA_N': GT_INT 00496;



M00358 %M00358
 LD Block,'UMA_N': NOCON 00384, 00490, 00491, 00492, 00496, 00497; COIL 00488;
 LD Block,'genee': NOCON 00013;
R00715 %R00715
 LD Block,'UMA_N': LT_INT 00497; GT_INT 00496;
M00403 %M00403
 LD Block,'UMA_N': NOCON 00498; COIL 00497;
R00717 %R00717
 LD Block,'UMA_N': LT_INT 00497;



M00403 %M00403
 LD Block,'UMA_N': NOCON 00498; COIL 00497;
M00402 %M00402
 LD Block,'UMA_N': NCCON 00498; COIL 00496;
M00404 %M00404
 LD Block,'UMA_N': NCCON 00490, 00491, 00492; NOCON 00498; COIL 00498;



M00416 %M00416
 LD Block,'UMA_N': NOCON 00500, 00539, 00541, 00542; COIL 00537;
M00408 %M00408
 LD Block,'_MAIN': NOCON 00012, 00080, 00089, 00097;
 LD Block,'UMA_N': NOCON 00520, 00521, 00522, 00525, 00531, 00532, 00533, 00534; COIL 00500;

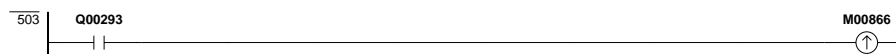
M00417 %M00417
 LD Block,'UMA_N': NOCON 00500, 00548, 00550, 00551; COIL 00546;
M00418 %M00418
 LD Block,'UMA_N': NOCON 00500, 00557, 00559, 00560; COIL 00555;
M00419 %M00419
 LD Block,'UMA_N': NOCON 00500, 00566, 00567, 00570, 00571; COIL 00564;
M00420 %M00420
 LD Block,'UMA_N': NOCON 00500, 00577, 00578, 00581, 00582; COIL 00575;
M00421 %M00421
 LD Block,'UMA_N': NOCON 00500, 00588, 00589, 00592, 00593; COIL 00586;
M00426 %M00426
 LD Block,'UMA_N': NOCON 00500, 00599, 00600, 00601, 00602, 00607, 00608; COIL 00597;



Q00289 %Q00289
 LD Block,'UMA_N': NOCON 00501, 00511; COIL 00539;
M00864 %M00864
 LD Block,'UMA_N': POSCOIL 00501; NOCON 00508;



Q00291 %Q00291
 LD Block,'UMA_N': NOCON 00502, 00512; COIL 00548;
M00865 %M00865
 LD Block,'UMA_N': POSCOIL 00502; NOCON 00508;



Q00293 %Q00293
 LD Block,'UMA_N': NOCON 00503, 00513; COIL 00557;
M00866 %M00866
 LD Block,'UMA_N': POSCOIL 00503; NOCON 00508;



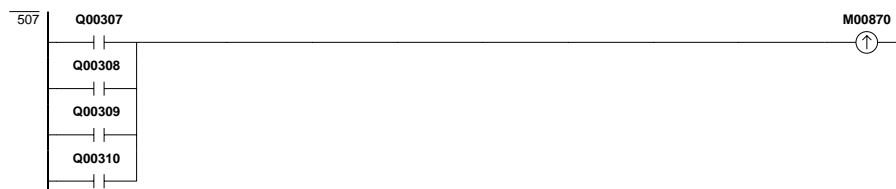
Q00295 %Q00295
 LD Block,'UMA_N': NOCON 00504, 00514; COIL 00566;
M00867 %M00867
 LD Block,'UMA_N': POSCOIL 00504; NOCON 00508;
Q00296 %Q00296
 LD Block,'UMA_N': NOCON 00504, 00514; COIL 00567;



Q00299 %Q00299
 LD Block,'UMA_N': NOCON 00505, 00515; COIL 00577;
M00868 %M00868
 LD Block,'UMA_N': POSCOIL 00505; NOCON 00508;
Q00300 %Q00300
 LD Block,'UMA_N': NOCON 00505, 00515; COIL 00578;



Q00303 %Q00303
 LD Block,'UMA_N': NOCON 00506, 00516; COIL 00588;
M00869 %M00869
 LD Block,'UMA_N': POSCOIL 00506; NOCON 00508;
Q00304 %Q00304
 LD Block,'UMA_N': NOCON 00506, 00516; COIL 00589;



Q00307 %Q00307
 LD Block,'UMA_N': NOCON 00507, 00517; COIL 00599;
M00870 %M00870
 LD Block,'UMA_N': POSCOIL 00507; NOCON 00508;
Q00308 %Q00308
 LD Block,'UMA_N': NOCON 00507, 00517; COIL 00600;
Q00309 %Q00309
 LD Block,'UMA_N': NOCON 00507, 00517; COIL 00601;
Q00310 %Q00310
 LD Block,'UMA_N': NOCON 00507, 00517; COIL 00602;



M00864 %M00864
 LD Block,'UMA_N': POSCOIL 00501; NOCON 00508;
M00871 %M00871
 LD Block,'UMA_N': NOCON 00509; COIL 00508;
M00865 %M00865
 LD Block,'UMA_N': POSCOIL 00502; NOCON 00508;
M00866 %M00866
 LD Block,'UMA_N': POSCOIL 00503; NOCON 00508;
M00867 %M00867
 LD Block,'UMA_N': POSCOIL 00504; NOCON 00508;
M00868 %M00868
 LD Block,'UMA_N': POSCOIL 00505; NOCON 00508;
M00869 %M00869
 LD Block,'UMA_N': POSCOIL 00506; NOCON 00508;
M00870 %M00870
 LD Block,'UMA_N': POSCOIL 00507; NOCON 00508;



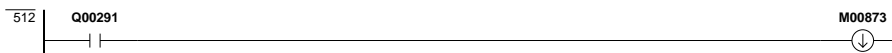
M00871 %M00871
 LD Block,'UMA_N': NOCON 00509; COIL 00508;
R00028[2] %R00030
 LD Block,'UMA_N': SUB_INT 00519, 00519; ADD_INT 00509, 00509; MOVE_INT 00510;



#ALW_ON %S00007
 LD Block,'Reloj': NOCON 00001, 00002, 00003, 00004, 00005, 00006, 00007, 00008;
 LD Block,'flujo_1': NOCON 00003, 00006;
 LD Block,'UMA_N': NOCON 00011, 00102, 00198, 00273, 00395, 00510, 00616;
 LD Block,'N_T_ch': NOCON 00001, 00002, 00003, 00004, 00005, 00006, 00007;
 LD Block,'N_cist': NOCON 00002, 00003, 00004, 00005, 00006, 00007, 00008, 00010, 00011, 00012, 00013, 00014, 00015, 00016;
 LD Block,'cister': NOCON 00020;
 LD Block,'genee': NOCON 00011;
 LD Block,'incend': NOCON 00026, 00032, 00033, 00034, 00036, 00039, 00040, 00041, 00042, 00043, 00044, 00045;
 LD Block,'agua': NOCON 00001, 00002, 00003, 00006, 00007, 00008, 00009, 00010, 00011, 00012, 00020, 00021, 00022, 00031, 00032, 00033;
 LD Block,'_MAIN': NOCON 00001, 00002, 00003, 00004, 00005, 00006, 00007, 00008, 00009, 00010, 00011, 00026, 00030, 00033, 00034, 00035, 00049, 00067, 00068, 00078, 00095, 00096, 00121, 00126, 00128, 00129;
R00028[2] %R00030
 LD Block,'UMA_N': SUB_INT 00519, 00519; ADD_INT 00509, 00509; MOVE_INT 00510;
AQ0010 %AQ0010
 LD Block,'UMA_N': MOVE_INT 00510;



Q00289 %Q00289
 LD Block,'UMA_N': NOCON 00501, 00511; COIL 00539;
M00872 %M00872
 LD Block,'UMA_N': NEGCOIL 00511; NOCON 00518;



Q00291 %Q00291
 LD Block,'UMA_N': NOCON 00502, 00512; COIL 00548;
M00873 %M00873
 LD Block,'UMA_N': NEGCOIL 00512; NOCON 00518;



Q00293 %Q00293
 LD Block,'UMA_N': NOCON 00503, 00513; COIL 00557;
M00874 %M00874

LD Block,'UMA_N': NEGCOIL 00513; NOCON 00518;



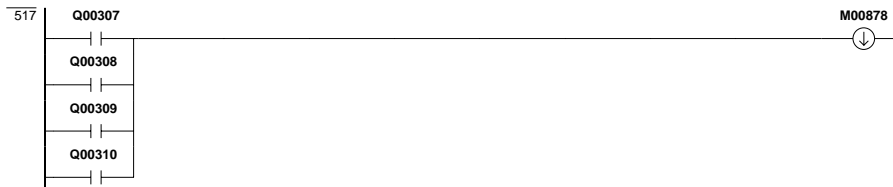
Q00295 %Q00295
LD Block,'UMA_N': NOCON 00504, 00514; COIL 00566;
M00875 %M00875
LD Block,'UMA_N': NEGCOIL 00514; NOCON 00518;
Q00296 %Q00296
LD Block,'UMA_N': NOCON 00504, 00514; COIL 00567;



Q00299 %Q00299
LD Block,'UMA_N': NOCON 00505, 00515; COIL 00577;
M00876 %M00876
LD Block,'UMA_N': NEGCOIL 00515; NOCON 00518;
Q00300 %Q00300
LD Block,'UMA_N': NOCON 00505, 00515; COIL 00578;



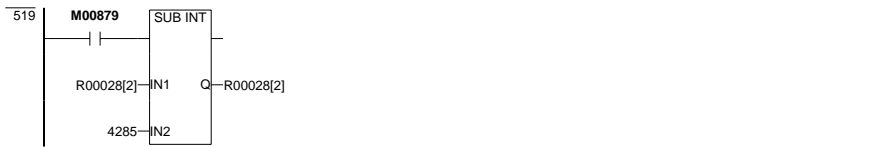
Q00303 %Q00303
LD Block,'UMA_N': NOCON 00506, 00516; COIL 00588;
M00877 %M00877
LD Block,'UMA_N': NEGCOIL 00516; NOCON 00518;
Q00304 %Q00304
LD Block,'UMA_N': NOCON 00506, 00516; COIL 00589;



Q00307 %Q00307
LD Block,'UMA_N': NOCON 00507, 00517; COIL 00599;
M00878 %M00878
LD Block,'UMA_N': NEGCOIL 00517; NOCON 00518;
Q00308 %Q00308
LD Block,'UMA_N': NOCON 00507, 00517; COIL 00600;
Q00309 %Q00309
LD Block,'UMA_N': NOCON 00507, 00517; COIL 00601;
Q00310 %Q00310
LD Block,'UMA_N': NOCON 00507, 00517; COIL 00602;



M00872 %M00872
LD Block,'UMA_N': NEGCOIL 00511; NOCON 00518;
M00879 %M00879
LD Block,'UMA_N': NOCON 00519; COIL 00518;
M00873 %M00873
LD Block,'UMA_N': NEGCOIL 00512; NOCON 00518;
M00874 %M00874
LD Block,'UMA_N': NEGCOIL 00513; NOCON 00518;
M00875 %M00875
LD Block,'UMA_N': NEGCOIL 00514; NOCON 00518;
M00876 %M00876
LD Block,'UMA_N': NEGCOIL 00515; NOCON 00518;
M00877 %M00877
LD Block,'UMA_N': NEGCOIL 00516; NOCON 00518;
M00878 %M00878
LD Block,'UMA_N': NEGCOIL 00517; NOCON 00518;

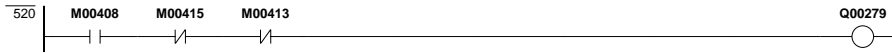


M00879 %M00879

LD Block,'UMA_N': NOCON 00519; COIL 00518;

R00028[2] %R00030

LD Block,'UMA_N': SUB_INT 00519, 00519; ADD_INT 00509, 00509; MOVE_INT 00510;



M00408 %M00408

LD Block,'_MAIN': NOCON 00012, 00080, 00089, 00097;

LD Block,'UMA_N': NOCON 00520, 00521, 00522, 00525, 00531, 00532, 00533, 00534; COIL 00500;

M00415 %M00415

LD Block,'UMA_N': NCCON 00520;

M00413 %M00413

LD Block,'UMA_N': NCCON 00520, 00523;

Q00279 %Q00279

LD Block,'UMA_N': COIL 00520;



M00408 %M00408

LD Block,'_MAIN': NOCON 00012, 00080, 00089, 00097;

LD Block,'UMA_N': NOCON 00520, 00521, 00522, 00525, 00531, 00532, 00533, 00534; COIL 00500;

Q00280 %Q00280

LD Block,'UMA_N': COIL 00521;



M00408 %M00408

LD Block,'_MAIN': NOCON 00012, 00080, 00089, 00097;

LD Block,'UMA_N': NOCON 00520, 00521, 00522, 00525, 00531, 00532, 00533, 00534; COIL 00500;

M00410 %M00410

LD Block,'UMA_N': NCCON 00522; NOCON 00525; COIL 00524;

M00411 %M00411

LD Block,'UMA_N': NCCON 00522, 00523; NOCON 00527;

M00422 %M00422

LD Block,'UMA_N': NOCON 00529; COIL 00522;



Q00281 %Q00281

LD Block,'UMA_N': NOCON 00523; COIL 00529;

M00413 %M00413

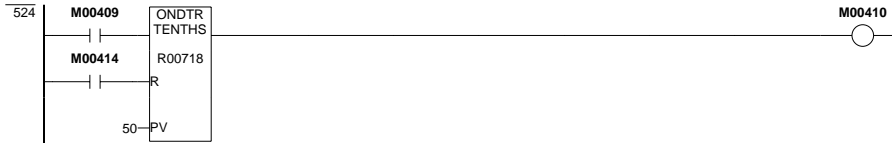
LD Block,'UMA_N': NCCON 00520, 00523;

M00411 %M00411

LD Block,'UMA_N': NCCON 00522, 00523; NOCON 00527;

M00409 %M00409

LD Block,'UMA_N': NOCON 00523, 00524; COIL 00523;



M00409 %M00409

LD Block,'UMA_N': NOCON 00523, 00524; COIL 00523;

R00718 %R00718

LD Block,'UMA_N': ONDTR_TENTHS 00524;

M00410 %M00410

LD Block,'UMA_N': NCCON 00522; NOCON 00525; COIL 00524;

M00414 %M00414

LD Block,'UMA_N': NOCON 00524, 00526; COIL 00526;



M00410 %M00410

LD Block,'UMA_N': NCCON 00522; NOCON 00525; COIL 00524;

M00408 %M00408

LD Block,'_MAIN': NOCON 00012, 00080, 00089, 00097;

LD Block,'UMA_N': NOCON 00520, 00521, 00522, 00525, 00531, 00532, 00533, 00534; COIL 00500;

M00412 %M00412

LD Block,'UMA_N': NCCON 00525; NOCON 00528;

M00423 %M00423

LD Block,'UMA_N': NOCON 00530; COIL 00525;



Q00282 %Q00282
LD Block,'UMA_N': NOCON 00526; COIL 00530;
R00721 %R00721
LD Block,'UMA_N': ONDTR_TENTHS 00526;
M00414 %M00414
LD Block,'UMA_N': NOCON 00524, 00526; COIL 00526;



M00411 %M00411
LD Block,'UMA_N': NCCON 00522, 00523; NOCON 00527;
Q00283 %Q00283
LD Block,'UMA_N': NOCON 00530; COIL 00527;



M00412 %M00412
LD Block,'UMA_N': NCCON 00525; NOCON 00528;
Q00284 %Q00284
LD Block,'UMA_N': NOCON 00529; COIL 00528;



M00422 %M00422
LD Block,'UMA_N': NOCON 00529; COIL 00522;
Q00281 %Q00281
LD Block,'UMA_N': NOCON 00523; COIL 00529;
Q00284 %Q00284
LD Block,'UMA_N': NOCON 00529; COIL 00528;



M00423 %M00423
LD Block,'UMA_N': NOCON 00530; COIL 00525;
Q00282 %Q00282
LD Block,'UMA_N': NOCON 00526; COIL 00530;
Q00283 %Q00283
LD Block,'UMA_N': NOCON 00530; COIL 00527;



M00408 %M00408
LD Block,'_MAIN': NOCON 00012, 00080, 00089, 00097;
LD Block,'UMA_N': NOCON 00520, 00521, 00522, 00525, 00531, 00532, 00533, 00534; COIL 00500;
R00725 %R00725
LD Block,'UMA_N': GT_INT 00531;
Q00285 %Q00285
LD Block,'UMA_N': COIL 00531;



M00408 %M00408
LD Block,'_MAIN': NOCON 00012, 00080, 00089, 00097;
LD Block,'UMA_N': NOCON 00520, 00521, 00522, 00525, 00531, 00532, 00533, 00534; COIL 00500;
R00726 %R00726
LD Block,'UMA_N': GT_INT 00532;
Q00286 %Q00286
LD Block,'UMA_N': COIL 00532;



M00408 %M00408
LD Block,'_MAIN': NOCON 00012, 00080, 00089, 00097;
LD Block,'UMA_N': NOCON 00520, 00521, 00522, 00525, 00531, 00532, 00533, 00534; COIL 00500;
Q00288 %Q00288

LD Block,'UMA_N': COIL 00533;
R00727 %R00727
 LD Block,'UMA_N': GE_INT 00533;



M00408 %M00408
 LD Block,'_MAIN': NOCON 00012, 00080, 00089, 00097;
 LD Block,'UMA_N': NOCON 00520, 00521, 00522, 00525, 00531, 00532, 00533, 00534; COIL 00500;
R00728 %R00728
 LD Block,'UMA_N': GT_INT 00534;
Q00287 %Q00287
 LD Block,'UMA_N': COIL 00534;



M00424 %M00424
 LD Block,'UMA_N': NOCON 00536;
R00729 %R00729
 LD Block,'UMA_N': UPCTR 00536; EQ_INT 00537, 00538;
M00425 %M00425
 LD Block,'UMA_N': NOCON 00536; COIL 00538;



R00729[0] %R00729
 LD Block,'UMA_N': EQ_INT 00537, 00538;
M00416 %M00416
 LD Block,'UMA_N': NOCON 00500, 00539, 00541, 00542; COIL 00537;



R00729[0] %R00729
 LD Block,'UMA_N': EQ_INT 00537, 00538;
M00425 %M00425
 LD Block,'UMA_N': NOCON 00536; COIL 00538;



M00416 %M00416
 LD Block,'UMA_N': NOCON 00500, 00539, 00541, 00542; COIL 00537;
M00429 %M00429
 LD Block,'UMA_N': NCCON 00539; NOCON 00543; COIL 00543;
Q00290 %Q00290
 LD Block,'UMA_N': NCCON 00539; COIL 00540;
Q00289 %Q00289
 LD Block,'UMA_N': NOCON 00501, 00511; COIL 00539;



M00430 %M00430
 LD Block,'UMA_N': NOCON 00540;
Q00290 %Q00290
 LD Block,'UMA_N': NCCON 00539; COIL 00540;



M00416 %M00416
 LD Block,'UMA_N': NOCON 00500, 00539, 00541, 00542; COIL 00537;
R00732 %R00732
 LD Block,'UMA_N': LT_INT 00542; GT_INT 00541;
M00427 %M00427
 LD Block,'UMA_N': NCCON 00543; COIL 00541;

R00733 %R00733
LD Block,'UMA_N': GT_INT 00541;



M00416 %M00416
LD Block,'UMA_N': NOCON 00500, 00539, 00541, 00542; COIL 00537;

R00732 %R00732
LD Block,'UMA_N': LT_INT 00542; GT_INT 00541;

M00428 %M00428
LD Block,'UMA_N': NOCON 00543; COIL 00542;

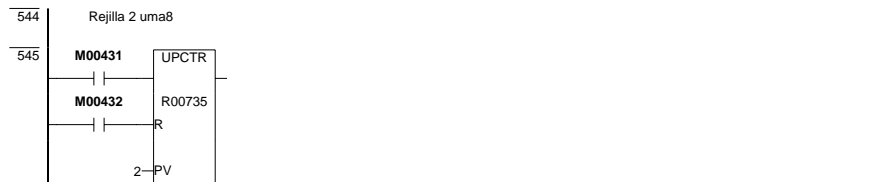
R00734 %R00734
LD Block,'UMA_N': LT_INT 00542;



M00428 %M00428
LD Block,'UMA_N': NOCON 00543; COIL 00542;

M00427 %M00427
LD Block,'UMA_N': NCCON 00543; COIL 00541;

M00429 %M00429
LD Block,'UMA_N': NCCON 00539; NOCON 00543; COIL 00543;



M00431 %M00431
LD Block,'UMA_N': NOCON 00545;

R00735 %R00735
LD Block,'UMA_N': UPCTR 00545; EQ_INT 00546, 00547;

M00432 %M00432
LD Block,'UMA_N': NOCON 00545; COIL 00547;



R00735[0] %R00735
LD Block,'UMA_N': EQ_INT 00546, 00547;

M00417 %M00417
LD Block,'UMA_N': NOCON 00500, 00548, 00550, 00551; COIL 00546;



R00735[0] %R00735
LD Block,'UMA_N': EQ_INT 00546, 00547;

M00432 %M00432
LD Block,'UMA_N': NOCON 00545; COIL 00547;



M00417 %M00417
LD Block,'UMA_N': NOCON 00500, 00548, 00550, 00551; COIL 00546;

M00436 %M00436
LD Block,'UMA_N': NCCON 00548; NOCON 00552; COIL 00552;

Q00292 %Q00292
LD Block,'UMA_N': NCCON 00548; COIL 00549;

Q00291 %Q00291
LD Block,'UMA_N': NOCON 00502, 00512; COIL 00548;



M00437 %M00437
LD Block,'UMA_N': NOCON 00549;

Q00292 %Q00292
LD Block,'UMA_N': NCCON 00548; COIL 00549;



M00417 %M00417
LD Block,'UMA_N': NOCON 00500, 00548, 00550, 00551; COIL 00546;

R00738 %R00738
LD Block,'UMA_N': LT_INT 00551; GT_INT 00550;

M00434 %M00434
LD Block,'UMA_N': NCCON 00552; COIL 00550;

R00739 %R00739
LD Block,'UMA_N': GT_INT 00550;



M00417 %M00417
LD Block,'UMA_N': NOCON 00500, 00548, 00550, 00551; COIL 00546;

R00738 %R00738
LD Block,'UMA_N': LT_INT 00551; GT_INT 00550;

M00435 %M00435
LD Block,'UMA_N': NOCON 00552; COIL 00551;

R00740 %R00740
LD Block,'UMA_N': LT_INT 00551;



M00435 %M00435
LD Block,'UMA_N': NOCON 00552; COIL 00551;

M00434 %M00434
LD Block,'UMA_N': NCCON 00552; COIL 00550;

M00436 %M00436
LD Block,'UMA_N': NCCON 00548; NOCON 00552; COIL 00552;



M00438 %M00438
LD Block,'UMA_N': NOCON 00554;

R00741 %R00741
LD Block,'UMA_N': UPCTR 00554; EQ_INT 00555, 00556;

M00439 %M00439
LD Block,'UMA_N': NOCON 00554; COIL 00556;



R00741[0] %R00741
LD Block,'UMA_N': EQ_INT 00555, 00556;

M00418 %M00418
LD Block,'UMA_N': NOCON 00500, 00557, 00559, 00560; COIL 00555;



R00741[0] %R00741
LD Block,'UMA_N': EQ_INT 00555, 00556;

M00439 %M00439
LD Block,'UMA_N': NOCON 00554; COIL 00556;



M00418 %M00418
LD Block,'UMA_N': NOCON 00500, 00557, 00559, 00560; COIL 00555;

M00443 %M00443
LD Block,'UMA_N': NCCON 00557; NOCON 00561; COIL 00561;

Q00294 %Q00294

LD Block,'UMA_N': NCCON 00557; COIL 00558;
Q00293 %Q00293
 LD Block,'UMA_N': NOCON 00503, 00513; COIL 00557;



M00444 %M00444
 LD Block,'UMA_N': NOCON 00558;
Q00294 %Q00294
 LD Block,'UMA_N': NCCON 00557; COIL 00558;



M00418 %M00418
 LD Block,'UMA_N': NOCON 00500, 00557, 00559, 00560; COIL 00555;
R00744 %R00744
 LD Block,'UMA_N': LT_INT 00560; GT_INT 00559;
M00441 %M00441
 LD Block,'UMA_N': NCCON 00561; COIL 00559;
R00745 %R00745
 LD Block,'UMA_N': GT_INT 00559;



M00418 %M00418
 LD Block,'UMA_N': NOCON 00500, 00557, 00559, 00560; COIL 00555;
R00744 %R00744
 LD Block,'UMA_N': LT_INT 00560; GT_INT 00559;
M00442 %M00442
 LD Block,'UMA_N': NOCON 00561; COIL 00560;
R00746 %R00746
 LD Block,'UMA_N': LT_INT 00560;



M00442 %M00442
 LD Block,'UMA_N': NOCON 00561; COIL 00560;
M00441 %M00441
 LD Block,'UMA_N': NCCON 00561; COIL 00559;
M00443 %M00443
 LD Block,'UMA_N': NCCON 00557; NOCON 00561; COIL 00561;



M00445 %M00445
 LD Block,'UMA_N': NOCON 00563;
R00747 %R00747
 LD Block,'UMA_N': UPCTR 00563; EQ_INT 00564, 00565;
M00446 %M00446
 LD Block,'UMA_N': NOCON 00563; COIL 00565;



R00747[0] %R00747
 LD Block,'UMA_N': EQ_INT 00564, 00565;
M00419 %M00419
 LD Block,'UMA_N': NOCON 00500, 00566, 00567, 00570, 00571; COIL 00564;



R00747[0] %R00747

LD Block,'UMA_N': EQ_INT 00564, 00565;
M00446 %M00446
 LD Block,'UMA_N': NOCON 00563; COIL 00565;



M00419 %M00419
 LD Block,'UMA_N': NOCON 00500, 00566, 00567, 00570, 00571; COIL 00564;
M00450 %M00450
 LD Block,'UMA_N': NCCON 00566, 00567; NOCON 00572; COIL 00572;
Q00297 %Q00297
 LD Block,'UMA_N': NCCON 00566; COIL 00568;
Q00295 %Q00295
 LD Block,'UMA_N': NOCON 00504, 00514; COIL 00566;



M00419 %M00419
 LD Block,'UMA_N': NOCON 00500, 00566, 00567, 00570, 00571; COIL 00564;
M00450 %M00450
 LD Block,'UMA_N': NCCON 00566, 00567; NOCON 00572; COIL 00572;
Q00298 %Q00298
 LD Block,'UMA_N': NCCON 00567; COIL 00569;
Q00296 %Q00296
 LD Block,'UMA_N': NOCON 00504, 00514; COIL 00567;



M00451 %M00451
 LD Block,'UMA_N': NOCON 00568;
Q00297 %Q00297
 LD Block,'UMA_N': NCCON 00566; COIL 00568;



M00452 %M00452
 LD Block,'UMA_N': NOCON 00569;
Q00298 %Q00298
 LD Block,'UMA_N': NCCON 00567; COIL 00569;



M00419 %M00419
 LD Block,'UMA_N': NOCON 00500, 00566, 00567, 00570, 00571; COIL 00564;
R00750 %R00750
 LD Block,'UMA_N': LT_INT 00571; GT_INT 00570;
M00448 %M00448
 LD Block,'UMA_N': NCCON 00572; COIL 00570;
R00751 %R00751
 LD Block,'UMA_N': GT_INT 00570;



M00419 %M00419
 LD Block,'UMA_N': NOCON 00500, 00566, 00567, 00570, 00571; COIL 00564;
R00750 %R00750
 LD Block,'UMA_N': LT_INT 00571; GT_INT 00570;
M00449 %M00449
 LD Block,'UMA_N': NOCON 00572; COIL 00571;
R00752 %R00752
 LD Block,'UMA_N': LT_INT 00571;



M00449 %M00449
 LD Block,'UMA_N': NOCON 00572; COIL 00571;
M00448 %M00448
 LD Block,'UMA_N': NCCON 00572; COIL 00570;
M00450 %M00450
 LD Block,'UMA_N': NCCON 00566, 00567; NOCON 00572; COIL 00572;

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M00453 %M00453
LD Block,'UMA_N': NOCON 00574;
R00753 %R00753
LD Block,'UMA_N': UPCTR 00574; EQ_INT 00575, 00576;
M00454 %M00454
LD Block,'UMA_N': NOCON 00574; COIL 00576;



R00753[0] %R00753
LD Block,'UMA_N': EQ_INT 00575, 00576;
M00420 %M00420
LD Block,'UMA_N': NOCON 00500, 00577, 00578, 00581, 00582; COIL 00575;



R00753[0] %R00753
LD Block,'UMA_N': EQ_INT 00575, 00576;
M00454 %M00454
LD Block,'UMA_N': NOCON 00574; COIL 00576;



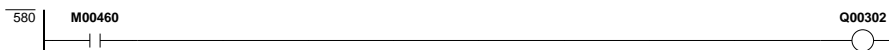
M00420 %M00420
LD Block,'UMA_N': NOCON 00500, 00577, 00578, 00581, 00582; COIL 00575;
M00458 %M00458
LD Block,'UMA_N': NCCON 00577, 00578; NOCON 00583; COIL 00583;
Q00301 %Q00301
LD Block,'UMA_N': NCCON 00577; COIL 00579;
Q00299 %Q00299
LD Block,'UMA_N': NOCON 00505, 00515; COIL 00577;



M00420 %M00420
LD Block,'UMA_N': NOCON 00500, 00577, 00578, 00581, 00582; COIL 00575;
M00458 %M00458
LD Block,'UMA_N': NCCON 00577, 00578; NOCON 00583; COIL 00583;
Q00302 %Q00302
LD Block,'UMA_N': NCCON 00578; COIL 00580;
Q00300 %Q00300
LD Block,'UMA_N': NOCON 00505, 00515; COIL 00578;



M00459 %M00459
LD Block,'UMA_N': NOCON 00579;
Q00301 %Q00301
LD Block,'UMA_N': NCCON 00577; COIL 00579;



M00460 %M00460
LD Block,'UMA_N': NOCON 00580;
Q00302 %Q00302
LD Block,'UMA_N': NCCON 00578; COIL 00580;



M00420 %M00420
LD Block,'UMA_N': NOCON 00500, 00577, 00578, 00581, 00582; COIL 00575;
R00757 %R00757
LD Block,'UMA_N': LT_INT 00582; GT_INT 00581;
M00456 %M00456
LD Block,'UMA_N': NCCON 00583; COIL 00581;
R00758 %R00758
LD Block,'UMA_N': GT_INT 00581;



M00420 %M00420
LD Block,'UMA_N': NOCON 00500, 00577, 00578, 00581, 00582; COIL 00575;

R00757 %R00757
LD Block,'UMA_N': LT_INT 00582; GT_INT 00581;

M00457 %M00457
LD Block,'UMA_N': NOCON 00583; COIL 00582;

R00759 %R00759
LD Block,'UMA_N': LT_INT 00582;



M00457 %M00457
LD Block,'UMA_N': NOCON 00583; COIL 00582;

M00456 %M00456
LD Block,'UMA_N': NCCON 00583; COIL 00581;

M00458 %M00458
LD Block,'UMA_N': NCCON 00577, 00578; NOCON 00583; COIL 00583;



M00461 %M00461
LD Block,'UMA_N': NOCON 00585;

R00760 %R00760
LD Block,'UMA_N': UPCTR 00585; EQ_INT 00586, 00587;

M00462 %M00462
LD Block,'UMA_N': NOCON 00585; COIL 00587;



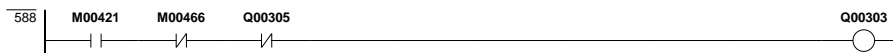
R00760[0] %R00760
LD Block,'UMA_N': EQ_INT 00586, 00587;

M00421 %M00421
LD Block,'UMA_N': NOCON 00500, 00588, 00589, 00592, 00593; COIL 00586;



R00760[0] %R00760
LD Block,'UMA_N': EQ_INT 00586, 00587;

M00462 %M00462
LD Block,'UMA_N': NOCON 00585; COIL 00587;



M00421 %M00421
LD Block,'UMA_N': NOCON 00500, 00588, 00589, 00592, 00593; COIL 00586;

M00466 %M00466
LD Block,'UMA_N': NCCON 00588, 00589; NOCON 00594; COIL 00594;

Q00305 %Q00305
LD Block,'UMA_N': NCCON 00588; COIL 00590;

Q00303 %Q00303
LD Block,'UMA_N': NOCON 00506, 00516; COIL 00588;



M00421 %M00421
LD Block,'UMA_N': NOCON 00500, 00588, 00589, 00592, 00593; COIL 00586;

M00466 %M00466
LD Block,'UMA_N': NCCON 00588, 00589; NOCON 00594; COIL 00594;

Q00306 %Q00306
LD Block,'UMA_N': NCCON 00589; COIL 00591;

Q00304 %Q00304
LD Block,'UMA_N': NOCON 00506, 00516; COIL 00589;



M00467 %M00467
 LD Block,'UMA_N': NOCON 00590;
Q00305 %Q00305
 LD Block,'UMA_N': NCCON 00588; COIL 00590;



M00468 %M00468
 LD Block,'UMA_N': NOCON 00591;
Q00306 %Q00306
 LD Block,'UMA_N': NCCON 00589; COIL 00591;



M00421 %M00421
 LD Block,'UMA_N': NOCON 00500, 00588, 00589, 00592, 00593; COIL 00586;
R00763 %R00763
 LD Block,'UMA_N': LT_INT 00593; GT_INT 00592;
M00464 %M00464
 LD Block,'UMA_N': NCCON 00594; COIL 00592;
R00764 %R00764
 LD Block,'UMA_N': GT_INT 00592;



M00421 %M00421
 LD Block,'UMA_N': NOCON 00500, 00588, 00589, 00592, 00593; COIL 00586;
R00763 %R00763
 LD Block,'UMA_N': LT_INT 00593; GT_INT 00592;
M00465 %M00465
 LD Block,'UMA_N': NOCON 00594; COIL 00593;
R00765 %R00765
 LD Block,'UMA_N': LT_INT 00593;



M00465 %M00465
 LD Block,'UMA_N': NOCON 00594; COIL 00593;
M00464 %M00464
 LD Block,'UMA_N': NCCON 00594; COIL 00592;
M00466 %M00466
 LD Block,'UMA_N': NCCON 00588, 00589; NOCON 00594; COIL 00594;



M00469 %M00469
 LD Block,'UMA_N': NOCON 00596;
R00766 %R00766
 LD Block,'UMA_N': UPCTR 00596; EQ_INT 00597, 00598;
M00470 %M00470
 LD Block,'UMA_N': NOCON 00596; COIL 00598;



R00766[0] %R00766
 LD Block,'UMA_N': EQ_INT 00597, 00598;
M00426 %M00426
 LD Block,'UMA_N': NOCON 00500, 00599, 00600, 00601, 00602, 00607, 00608; COIL 00597;



R00766[0] %R00766
 LD Block,'UMA_N': EQ_INT 00597, 00598;
M00470 %M00470
 LD Block,'UMA_N': NOCON 00596; COIL 00598;



M00426 %M00426
 LD Block,'UMA_N': NOCON 00500, 00599, 00600, 00601, 00602, 00607, 00608; COIL 00597;
M00474 %M00474
 LD Block,'UMA_N': NCCON 00599, 00600, 00601, 00602; NOCON 00609; COIL 00609;
Q00311 %Q00311
 LD Block,'UMA_N': NCCON 00599; COIL 00603;
Q00307 %Q00307
 LD Block,'UMA_N': NOCON 00507, 00517; COIL 00599;



M00426 %M00426
 LD Block,'UMA_N': NOCON 00500, 00599, 00600, 00601, 00602, 00607, 00608; COIL 00597;
M00474 %M00474
 LD Block,'UMA_N': NCCON 00599, 00600, 00601, 00602; NOCON 00609; COIL 00609;
Q00312 %Q00312
 LD Block,'UMA_N': NCCON 00600; COIL 00604;
Q00308 %Q00308
 LD Block,'UMA_N': NOCON 00507, 00517; COIL 00600;



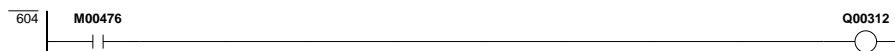
M00426 %M00426
 LD Block,'UMA_N': NOCON 00500, 00599, 00600, 00601, 00602, 00607, 00608; COIL 00597;
M00474 %M00474
 LD Block,'UMA_N': NCCON 00599, 00600, 00601, 00602; NOCON 00609; COIL 00609;
Q00313 %Q00313
 LD Block,'UMA_N': NCCON 00601; COIL 00605;
Q00309 %Q00309
 LD Block,'UMA_N': NOCON 00507, 00517; COIL 00601;



M00426 %M00426
 LD Block,'UMA_N': NOCON 00500, 00599, 00600, 00601, 00602, 00607, 00608; COIL 00597;
M00474 %M00474
 LD Block,'UMA_N': NCCON 00599, 00600, 00601, 00602; NOCON 00609; COIL 00609;
Q00314 %Q00314
 LD Block,'UMA_N': NCCON 00602; COIL 00606;
Q00310 %Q00310
 LD Block,'UMA_N': NOCON 00507, 00517; COIL 00602;



M00475 %M00475
 LD Block,'UMA_N': NOCON 00603;
Q00311 %Q00311
 LD Block,'UMA_N': NCCON 00599; COIL 00603;



M00476 %M00476
 LD Block,'UMA_N': NOCON 00604;
Q00312 %Q00312
 LD Block,'UMA_N': NCCON 00600; COIL 00604;



M00477 %M00477
 LD Block,'UMA_N': NOCON 00605;
Q00313 %Q00313
 LD Block,'UMA_N': NCCON 00601; COIL 00605;



M00478 %M00478
 LD Block,'UMA_N': NOCON 00606;
Q00314 %Q00314
 LD Block,'UMA_N': NCCON 00602; COIL 00606;



M00426 %M00426
 LD Block,'UMA_N': NOCON 00500, 00599, 00600, 00601, 00602, 00607, 00608; COIL 00597;
R00769 %R00769
 LD Block,'UMA_N': LT_INT 00608; GT_INT 00607;
M00472 %M00472
 LD Block,'UMA_N': NCCON 00609; COIL 00607;
R00770 %R00770
 LD Block,'UMA_N': GT_INT 00607;



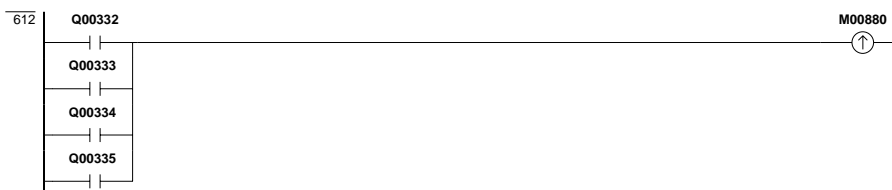
M00426 %M00426
 LD Block,'UMA_N': NOCON 00500, 00599, 00600, 00601, 00602, 00607, 00608; COIL 00597;
R00769 %R00769
 LD Block,'UMA_N': LT_INT 00608; GT_INT 00607;
M00473 %M00473
 LD Block,'UMA_N': NOCON 00609; COIL 00608;
R00771 %R00771
 LD Block,'UMA_N': LT_INT 00608;



M00473 %M00473
 LD Block,'UMA_N': NOCON 00609; COIL 00608;
M00472 %M00472
 LD Block,'UMA_N': NCCON 00609; COIL 00607;
M00474 %M00474
 LD Block,'UMA_N': NCCON 00599, 00600, 00601, 00602; NOCON 00609; COIL 00609;



M00487 %M00487
 LD Block,'UMA_N': NOCON 00611, 00640, 00641, 00642, 00643, 00648, 00649; COIL 00638;
M00479 %M00479
 LD Block,'_MAIN': NOCON 00012, 00080, 00090, 00098;
 LD Block,'UMA_N': NOCON 00621, 00622, 00623, 00626, 00632, 00633, 00634, 00635; COIL 00611;
M00488 %M00488
 LD Block,'UMA_N': NOCON 00611, 00655, 00656, 00659, 00660; COIL 00653;



Q00332 %Q00332
 LD Block,'UMA_N': NOCON 00612, 00617; COIL 00640;
M00880 %M00880
 LD Block,'UMA_N': POSCOIL 00612; NOCON 00614;
Q00333 %Q00333
 LD Block,'UMA_N': NOCON 00612, 00617; COIL 00641;
Q00334 %Q00334
 LD Block,'UMA_N': NOCON 00612, 00617; COIL 00642;
Q00335 %Q00335
 LD Block,'UMA_N': NOCON 00612, 00617; COIL 00643;



Q00340 %Q00340
 LD Block,'UMA_N': NOCON 00613, 00618; COIL 00655;
M00881 %M00881
 LD Block,'UMA_N': POSCOIL 00613; NOCON 00614;
Q00341 %Q00341
 LD Block,'UMA_N': NOCON 00613, 00618; COIL 00656;



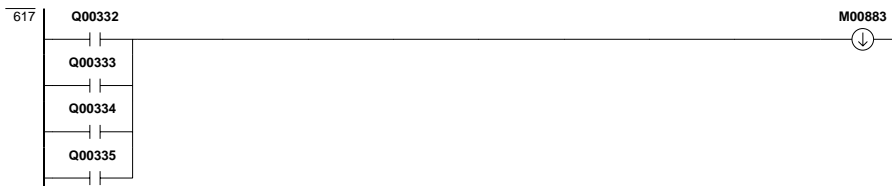
M00880 %M00880
 LD Block,'UMA_N': POSCOIL 00612; NOCON 00614;
M00882 %M00882
 LD Block,'UMA_N': NOCON 00615; COIL 00614;
M00881 %M00881
 LD Block,'UMA_N': POSCOIL 00613; NOCON 00614;



M00882 %M00882
 LD Block,'UMA_N': NOCON 00615; COIL 00614;
R00037[20] %R00057
 LD Block,'UMA_N': SUB_INT 00620, 00620; ADD_INT 00615, 00615; MOVE_INT 00616;



#ALW_ON %S00007
 LD Block,'Reloj': NOCON 00001, 00002, 00003, 00004, 00005, 00006, 00007, 00008;
 LD Block,'flujo_1': NOCON 00003, 00006;
 LD Block,'UMA_N': NOCON 00011, 00102, 00198, 00273, 00395, 00510, 00616;
 LD Block,'N_T_ch': NOCON 00001, 00002, 00003, 00004, 00005, 00006, 00007;
 LD Block,'N_cist': NOCON 00002, 00003, 00004, 00005, 00006, 00007, 00008, 00010, 00011, 00012, 00013, 00014, 00015, 00016;
 LD Block,'cister': NOCON 00020;
 LD Block,'genee': NOCON 00011;
 LD Block,'incend': NOCON 00026, 00032, 00033, 00034, 00036, 00039, 00040, 00041, 00042, 00043, 00044, 00045;
 LD Block,'agua': NOCON 00001, 00002, 00003, 00006, 00007, 00008, 00009, 00010, 00011, 00012, 00020, 00021, 00022, 00031, 00032, 00033;
 LD Block,'_MAIN': NOCON 00001, 00002, 00003, 00004, 00005, 00006, 00007, 00008, 00009, 00010, 00011, 00026, 00030, 00033, 00034, 00035, 00049, 00067, 00068, 00078, 00095, 00096, 00121, 00126, 00128, 00129;
R00037[20] %R00057
 LD Block,'UMA_N': SUB_INT 00620, 00620; ADD_INT 00615, 00615; MOVE_INT 00616;
AQ0011 %AQ0011
 LD Block,'UMA_N': MOVE_INT 00616;



Q00332 %Q00332
 LD Block,'UMA_N': NOCON 00612, 00617; COIL 00640;
M00883 %M00883
 LD Block,'UMA_N': NEGCOIL 00617; NOCON 00619;
Q00333 %Q00333
 LD Block,'UMA_N': NOCON 00612, 00617; COIL 00641;
Q00334 %Q00334
 LD Block,'UMA_N': NOCON 00612, 00617; COIL 00642;
Q00335 %Q00335
 LD Block,'UMA_N': NOCON 00612, 00617; COIL 00643;



Q00340 %Q00340
 LD Block,'UMA_N': NOCON 00613, 00618; COIL 00655;
M00884 %M00884
 LD Block,'UMA_N': NEGCOIL 00618; NOCON 00619;
Q00341 %Q00341
 LD Block,'UMA_N': NOCON 00613, 00618; COIL 00656;



M00883 %M00883
 LD Block,'UMA_N': NEGCOIL 00617; NOCON 00619;
M00885 %M00885
 LD Block,'UMA_N': NOCON 00620; COIL 00619;
M00884 %M00884

LD Block,'UMA_N': NEGCOIL 00618; NOCON 00619;



M00885 %M00885

LD Block,'UMA_N': NOCON 00620; COIL 00619;

R00037[20] %R00057

LD Block,'UMA_N': SUB_INT 00620, 00620; ADD_INT 00615, 00615; MOVE_INT 00616;



M00479 %M00479

LD Block,'_MAIN': NOCON 00012, 00080, 00090, 00098;

LD Block,'UMA_N': NOCON 00621, 00622, 00623, 00626, 00632, 00633, 00634, 00635; COIL 00611;

M00486 %M00486

LD Block,'UMA_N': NCCON 00621;

M00484 %M00484

LD Block,'UMA_N': NCCON 00621, 00624;

Q00322 %Q00322

LD Block,'UMA_N': COIL 00621;



M00479 %M00479

LD Block,'_MAIN': NOCON 00012, 00080, 00090, 00098;

LD Block,'UMA_N': NOCON 00621, 00622, 00623, 00626, 00632, 00633, 00634, 00635; COIL 00611;

Q00323 %Q00323

LD Block,'UMA_N': COIL 00622;



M00479 %M00479

LD Block,'_MAIN': NOCON 00012, 00080, 00090, 00098;

LD Block,'UMA_N': NOCON 00621, 00622, 00623, 00626, 00632, 00633, 00634, 00635; COIL 00611;

M00481 %M00481

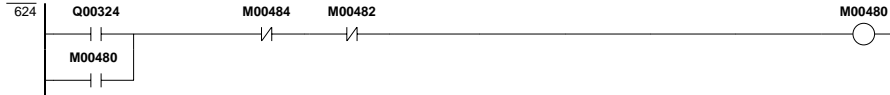
LD Block,'UMA_N': NCCON 00623; NOCON 00626; COIL 00625;

M00482 %M00482

LD Block,'UMA_N': NCCON 00623, 00624; NOCON 00628;

M00493 %M00493

LD Block,'UMA_N': NOCON 00630; COIL 00623;



Q00324 %Q00324

LD Block,'UMA_N': NOCON 00624; COIL 00630;

M00484 %M00484

LD Block,'UMA_N': NCCON 00621, 00624;

M00482 %M00482

LD Block,'UMA_N': NCCON 00623, 00624; NOCON 00628;

M00480 %M00480

LD Block,'UMA_N': NOCON 00624, 00625; COIL 00624;



M00480 %M00480

LD Block,'UMA_N': NOCON 00624, 00625; COIL 00624;

R00772 %R00772

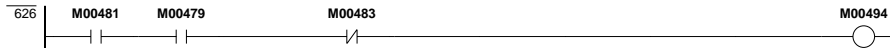
LD Block,'UMA_N': ONDTR_TENTHS 00625;

M00481 %M00481

LD Block,'UMA_N': NCCON 00623; NOCON 00626; COIL 00625;

M00485 %M00485

LD Block,'UMA_N': NOCON 00625, 00627; COIL 00627;



M00481 %M00481

LD Block,'UMA_N': NCCON 00623; NOCON 00626; COIL 00625;

M00479 %M00479

LD Block,'_MAIN': NOCON 00012, 00080, 00090, 00098;

LD Block,'UMA_N': NOCON 00621, 00622, 00623, 00626, 00632, 00633, 00634, 00635; COIL 00611;

M00483 %M00483

LD Block,'UMA_N': NCCON 00626; NOCON 00629;

M00494 %M00494

LD Block,'UMA_N': NOCON 00631; COIL 00626;



Q00325 %Q00325
 LD Block,'UMA_N': NOCON 00627; COIL 00631;
R00775 %R00775
 LD Block,'UMA_N': ONDTR_TENTHS 00627;
M00485 %M00485
 LD Block,'UMA_N': NOCON 00625, 00627; COIL 00627;



M00482 %M00482
 LD Block,'UMA_N': NCCON 00623, 00624; NOCON 00628;
Q00326 %Q00326
 LD Block,'UMA_N': NOCON 00631; COIL 00628;



M00483 %M00483
 LD Block,'UMA_N': NCCON 00626; NOCON 00629;
Q00327 %Q00327
 LD Block,'UMA_N': NOCON 00630; COIL 00629;



M00493 %M00493
 LD Block,'UMA_N': NOCON 00630; COIL 00623;
Q00324 %Q00324
 LD Block,'UMA_N': NOCON 00624; COIL 00630;
Q00327 %Q00327
 LD Block,'UMA_N': NOCON 00630; COIL 00629;



M00494 %M00494
 LD Block,'UMA_N': NOCON 00631; COIL 00626;
Q00325 %Q00325
 LD Block,'UMA_N': NOCON 00627; COIL 00631;
Q00326 %Q00326
 LD Block,'UMA_N': NOCON 00631; COIL 00628;



M00479 %M00479
 LD Block,'_MAIN': NOCON 00012, 00080, 00090, 00098;
 LD Block,'UMA_N': NOCON 00621, 00622, 00623, 00626, 00632, 00633, 00634, 00635; COIL 00611;
R00779 %R00779
 LD Block,'UMA_N': GT_INT 00632;
Q00328 %Q00328
 LD Block,'UMA_N': COIL 00632;



M00479 %M00479
 LD Block,'_MAIN': NOCON 00012, 00080, 00090, 00098;
 LD Block,'UMA_N': NOCON 00621, 00622, 00623, 00626, 00632, 00633, 00634, 00635; COIL 00611;
R00780 %R00780
 LD Block,'UMA_N': GT_INT 00633;
Q00329 %Q00329
 LD Block,'UMA_N': COIL 00633;



M00479 %M00479
 LD Block,'_MAIN': NOCON 00012, 00080, 00090, 00098;
 LD Block,'UMA_N': NOCON 00621, 00622, 00623, 00626, 00632, 00633, 00634, 00635; COIL 00611;
Q00331 %Q00331

LD Block,'UMA_N': COIL 00634;
R00781 %R00781
 LD Block,'UMA_N': GE_INT 00634;



M00479 %M00479
 LD Block,'_MAIN': NOCON 00012, 00080, 00090, 00098;
 LD Block,'UMA_N': NOCON 00621, 00622, 00623, 00626, 00632, 00633, 00634, 00635; COIL 00611;
R00782 %R00782
 LD Block,'UMA_N': GT_INT 00635;
Q00330 %Q00330
 LD Block,'UMA_N': COIL 00635;



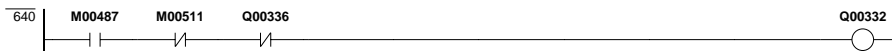
M00495 %M00495
 LD Block,'UMA_N': NOCON 00637;
R00783 %R00783
 LD Block,'UMA_N': UPCTR 00637; EQ_INT 00638, 00639;
M00496 %M00496
 LD Block,'UMA_N': NOCON 00637; COIL 00639;



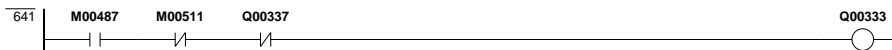
R00783[0] %R00783
 LD Block,'UMA_N': EQ_INT 00638, 00639;
M00487 %M00487
 LD Block,'UMA_N': NOCON 00611, 00640, 00641, 00642, 00643, 00648, 00649; COIL 00638;



R00783[0] %R00783
 LD Block,'UMA_N': EQ_INT 00638, 00639;
M00496 %M00496
 LD Block,'UMA_N': NOCON 00637; COIL 00639;



M00487 %M00487
 LD Block,'UMA_N': NOCON 00611, 00640, 00641, 00642, 00643, 00648, 00649; COIL 00638;
M00511 %M00511
 LD Block,'UMA_N': NCCON 00640, 00641, 00642, 00643; NOCON 00650; COIL 00650;
Q00336 %Q00336
 LD Block,'UMA_N': NCCON 00640; COIL 00644;
Q00332 %Q00332
 LD Block,'UMA_N': NOCON 00612, 00617; COIL 00640;



M00487 %M00487
 LD Block,'UMA_N': NOCON 00611, 00640, 00641, 00642, 00643, 00648, 00649; COIL 00638;
M00511 %M00511
 LD Block,'UMA_N': NCCON 00640, 00641, 00642, 00643; NOCON 00650; COIL 00650;
Q00337 %Q00337
 LD Block,'UMA_N': NCCON 00641; COIL 00645;
Q00333 %Q00333
 LD Block,'UMA_N': NOCON 00612, 00617; COIL 00641;



M00487 %M00487
 LD Block,'UMA_N': NOCON 00611, 00640, 00641, 00642, 00643, 00648, 00649; COIL 00638;
M00511 %M00511
 LD Block,'UMA_N': NCCON 00640, 00641, 00642, 00643; NOCON 00650; COIL 00650;
Q00338 %Q00338
 LD Block,'UMA_N': NCCON 00642; COIL 00646;

Q00334 %Q00334

LD Block,'UMA_N': NOCON 00612, 00617; COIL 00642;



M00487 %M00487

LD Block,'UMA_N': NOCON 00611, 00640, 00641, 00642, 00643, 00648, 00649; COIL 00638;

M00511 %M00511

LD Block,'UMA_N': NCCON 00640, 00641, 00642, 00643; NOCON 00650; COIL 00650;

Q00339 %Q00339

LD Block,'UMA_N': NCCON 00643; COIL 00647;

Q00335 %Q00335

LD Block,'UMA_N': NOCON 00612, 00617; COIL 00643;

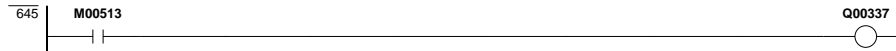


M00512 %M00512

LD Block,'UMA_N': NOCON 00644;

Q00336 %Q00336

LD Block,'UMA_N': NCCON 00640; COIL 00644;



M00513 %M00513

LD Block,'UMA_N': NOCON 00645;

Q00337 %Q00337

LD Block,'UMA_N': NCCON 00641; COIL 00645;



M00514 %M00514

LD Block,'UMA_N': NOCON 00646;

Q00338 %Q00338

LD Block,'UMA_N': NCCON 00642; COIL 00646;



M00515 %M00515

LD Block,'UMA_N': NOCON 00647;

Q00339 %Q00339

LD Block,'UMA_N': NCCON 00643; COIL 00647;



M00487 %M00487

LD Block,'UMA_N': NOCON 00611, 00640, 00641, 00642, 00643, 00648, 00649; COIL 00638;

R00786 %R00786

LD Block,'UMA_N': LT_INT 00649; GT_INT 00648;

M00498 %M00498

LD Block,'UMA_N': NCCON 00650; COIL 00648;

R00787 %R00787

LD Block,'UMA_N': GT_INT 00648;



M00487 %M00487

LD Block,'UMA_N': NOCON 00611, 00640, 00641, 00642, 00643, 00648, 00649; COIL 00638;

R00786 %R00786

LD Block,'UMA_N': LT_INT 00649; GT_INT 00648;

M00499 %M00499

LD Block,'UMA_N': NOCON 00650; COIL 00649;

R00788 %R00788

LD Block,'UMA_N': LT_INT 00649;



M00499 %M00499

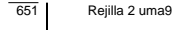
LD Block,'UMA_N': NOCON 00650; COIL 00649;

M00498 %M00498

LD Block,'UMA_N': NCCON 00650; COIL 00648;

M00511 %M00511

LD Block,'UMA_N': NCCON 00640, 00641, 00642, 00643; NOCON 00650; COIL 00650;





M00516 %M00516
LD Block,'UMA_N': NOCON 00652;
R00789 %R00789
LD Block,'UMA_N': UPCTR 00652; EQ_INT 00653, 00654;
M00517 %M00517
LD Block,'UMA_N': NOCON 00652; COIL 00654;



R00789[0] %R00789
LD Block,'UMA_N': EQ_INT 00653, 00654;
M00488 %M00488
LD Block,'UMA_N': NOCON 00611, 00655, 00656, 00659, 00660; COIL 00653;



R00789[0] %R00789
LD Block,'UMA_N': EQ_INT 00653, 00654;
M00517 %M00517
LD Block,'UMA_N': NOCON 00652; COIL 00654;



M00488 %M00488
LD Block,'UMA_N': NOCON 00611, 00655, 00656, 00659, 00660; COIL 00653;
M00521 %M00521
LD Block,'UMA_N': NCCON 00655, 00656; NOCON 00661; COIL 00661;
Q00342 %Q00342
LD Block,'UMA_N': NCCON 00655; COIL 00657;
Q00340 %Q00340
LD Block,'UMA_N': NOCON 00613, 00618; COIL 00655;



M00488 %M00488
LD Block,'UMA_N': NOCON 00611, 00655, 00656, 00659, 00660; COIL 00653;
M00521 %M00521
LD Block,'UMA_N': NCCON 00655, 00656; NOCON 00661; COIL 00661;
Q00343 %Q00343
LD Block,'UMA_N': NCCON 00656; COIL 00658;
Q00341 %Q00341
LD Block,'UMA_N': NOCON 00613, 00618; COIL 00656;



M00522 %M00522
LD Block,'UMA_N': NOCON 00657;
Q00342 %Q00342
LD Block,'UMA_N': NCCON 00655; COIL 00657;



M00523 %M00523
LD Block,'UMA_N': NOCON 00658;
Q00343 %Q00343
LD Block,'UMA_N': NCCON 00656; COIL 00658;



M00488 %M00488
LD Block,'UMA_N': NOCON 00611, 00655, 00656, 00659, 00660; COIL 00653;
R00792 %R00792
LD Block,'UMA_N': LT_INT 00660; GT_INT 00659;
M00519 %M00519
LD Block,'UMA_N': NCCON 00661; COIL 00659;
R00793 %R00793
LD Block,'UMA_N': GT_INT 00659;



M00488 %M00488

LD Block,'UMA_N': NOCON 00611, 00655, 00656, 00659, 00660; COIL 00653;

R00792 %R00792

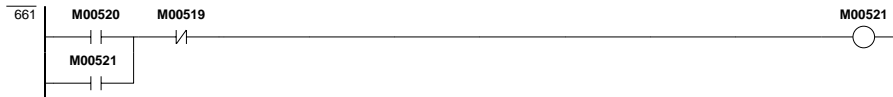
LD Block,'UMA_N': LT_INT 00660; GT_INT 00659;

M00520 %M00520

LD Block,'UMA_N': NOCON 00661; COIL 00660;

R00794 %R00794

LD Block,'UMA_N': LT_INT 00660;



M00520 %M00520

LD Block,'UMA_N': NOCON 00661; COIL 00660;

M00519 %M00519

LD Block,'UMA_N': NCCON 00661; COIL 00659;

M00521 %M00521

LD Block,'UMA_N': NCCON 00655, 00656; NOCON 00661; COIL 00661;